MOTOROLA
SEMICONDUCTOR
TECHNICAL DATA

BD234
BD236
BD238

PLASTIC MEDIUM POWER
SILICON PNP TRANSISTOR

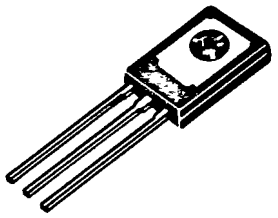
. . . designed for use in 5 to 10 Watt audio amplifiers and drivers utilizing complementary or quasi complementary circuits.

- DC Current Gain—h_{FE} = 40 (Min) @ I_C = 0.15 Adc
- BD 234, 236, 238 are complementary with BD 233, 235, 237

2 AMPERE
POWER TRANSISTOR

PNP SILICON

45, 60, 80 VOLTS
25 WATTS



MAXIMUM RATINGS

Rating	Symbol	Type	Value	Unit
Collector-Emitter Voltage	V_{CEO}	BD 234 BD 236 BD 238	45 60 80	Vdc
Collector-Base Voltage	V_{CBO}	BD 234 BD 236 BD 238	45 60 80	Vdc
Emitter-Base Voltage	V_{EBO}		5	Vdc
Collector Current	I_C		2.0	Adc
Base Current	I_B		1.0	Adc
Total Device Dissipation T_C=25°C	P_D		25	Watts
Operating and Storage Junction Temperature Range	T_J, T_{stg}		—55 to +150	°C

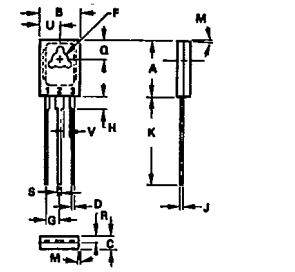
THERMAL CHARACTERISTICS

Characteristic	Symbol		Max	Unit
Thermal Resistance, Junction to Case	θ_{JC}		5.0	° C/W

ELECTRICAL CHARACTERISTICS (T_c = 25°C unless otherwise noted)

Characteristic	Symbol	Type	Min	Max	Unit
Collector-Emitter Sustaining Voltage* (I_C = 0.1 Adc, I_B = 0)	BV_{CEO}*	BD 234 BD 236 BD 238	45 60 80	— — —	Vdc
Collector Cutoff Current (V_{CB} = 45 Vdc, I_E = 0) (V_{CB} = 60 Vdc, I_E = 0) (V_{CB} = 80 Vdc, I_E = 0)	I_{CBO}	BD 234 BD 236 BD 238	— — —	0.1 0.1 0.1	mAdc
Emitter Cutoff Current (V_{BE} = 5.0 Vdc, I_C = 0)	I_{EBO}		—	1.0	mAdc
DC current Gain (I_C = 0.15 A, V_{CE} = 2 V) (I_C = 1 A, V_{CE} = 2 V)	h_{FE1} h_{FE2}		40 25	— —	
Collector-Emitter Saturation Voltage* (I_C = 1 Adc, I_B = 0.1 Adc)	$V_{CE(sat)}$*		—	0.6	Vdc
Base-Emitter On Voltage* (I_C = 1 Adc, V_{CE} = 2.0 Vdc)	$V_{BE(on)}$*		—	1.3	Vdc
Current-Gain-Bandwidth Product (I_C = 250 mAdc, V_{CE} = 10 Vdc, f = 1.0 MHz)	f_T		3.0	—	MHz

* Pulse Test: Pulse Width ≤ 300 μs, Duty Cycle ≤ 2.0%.



DIM	MILLIMETERS MIN	MILLIMETERS MAX	INCHES MIN	INCHES MAX
A	10.80	11.04	0.425	0.435
B	7.50	7.74	0.295	0.305
C	2.42	2.66	0.095	0.105
D	0.51	0.86	0.020	0.034
F	2.83	3.17	0.111	0.125
G	2.32	2.46	0.091	0.097
H	1.27	2.41	0.050	0.095
J	0.39	0.63	0.015	0.025
K	14.61	14.83	0.575	0.956
M	3° TYP		3° TYP	
Q	3.76	4.01	0.148	0.158
R	1.15	1.39	0.045	0.055
S	0.64	0.88	0.025	0.035
U	5.08	3.93	0.145	0.155
V	1.02	—	0.040	—

STYLE 1
PIN 1. EMITTER
2. COLLECTOR
3. BASE

NOTES
1. MT = MAIN TERMINAL.
2. LEADS, TRUE POSITIONED WITHIN 0.25mm (0.010) DIA TO DIM A & B AT MAXIMUM MATERIAL CONDITION.

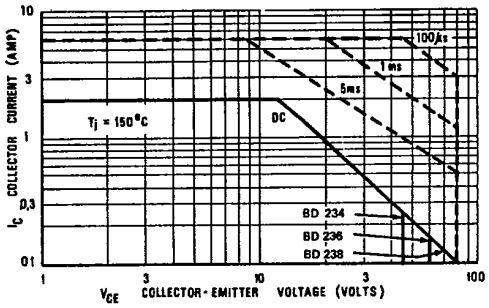
CASE 77-05
TO-126

3

BD234, BD236, BD238

T-33-19

FIGURE 1 – ACTIVE-REGION SAFE OPERATING AREA



The Safe Operating Area Curves indicate I_C–V_{CE} limits below which the device will not enter secondary breakdown. Collector load lines for specific circuits must fall within the applicable Safe Area to avoid causing a catastrophic failure. To insure operation below the maximum T_J, power-temperature derating must be observed for both steady state and pulse power conditions.

FIGURE 2 – COLLECTOR SATURATION REGION

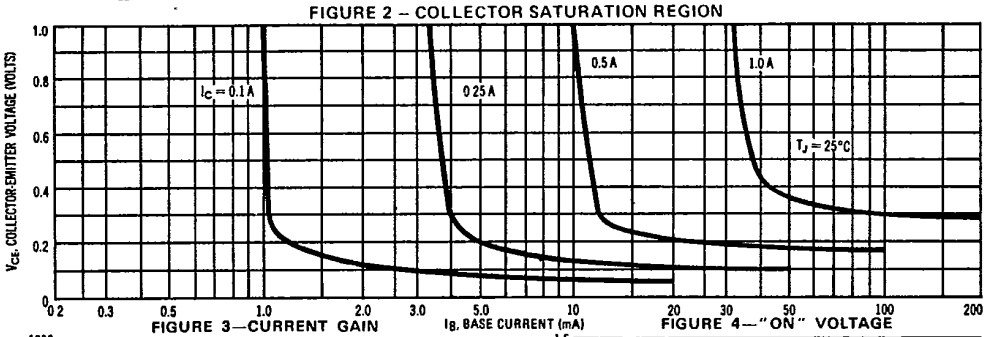


FIGURE 3—CURRENT GAIN

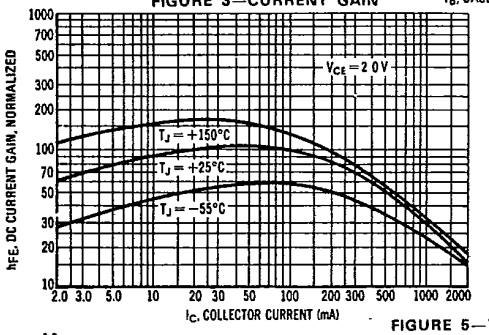


FIGURE 4—"ON" VOLTAGE

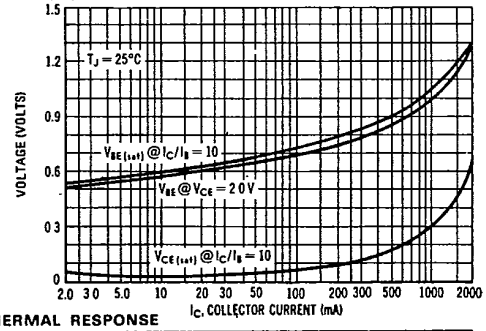


FIGURE 5—THERMAL RESPONSE