



# MOTOROLA

## Product Preview

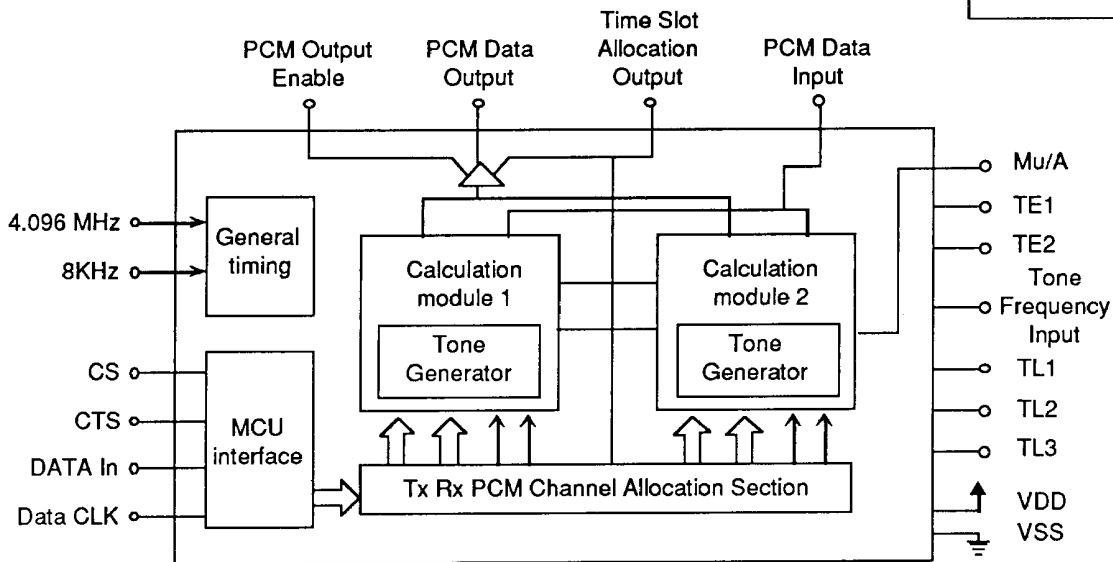
# PCM conference circuit

The MC145611 is a CMOS device designed for voice conference in a digital PABX system. It can handle a maximum of up to 8 parties in conference so that every party can hear when one or more parties speak at the same time. When required, the 2 calculation modules allow the 8 parties to split into 2 groups of 4 and set up 2 separate conference calls simultaneously. The use of level priority coding technique provides a low cost means of mixing PCM voice codes for voice conference application in a digital telephone switch.

Features:

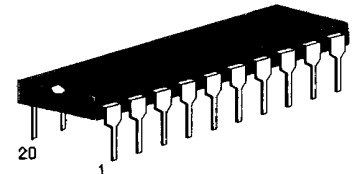
- Single +5v power supply
- Support standard Mu-law or A-law PCM codes
- Built-in time slot assignment circuit
- Directly off the PCM highways
- 4.096MHz clock, 8kHz frame sync and serial PCM data comply with codec timing used in the PABX system
- Serial data with MCU interface
- 8 parties conference in single group or split into two groups
- Intrusion party channel time slot assignment provided
- Built-in maskable tone signalling. Tone level and frequency externally adjustable.

### Block Diagram



# MC145611

## HCMOS



P suffix  
Plastic package  
Case 738-02

### PIN ASSIGNMENT

DCI	1	20	VDD
FS	2	19	NC
PCM IN	3	18	TSAO
PCM OUT	4	17	TE2
PCM EN	5	16	TE1
$\overline{\text{Mu/A}}$	6	15	TL3
CTS	7	14	TL2
CS	8	13	TL1
DI	9	12	TF
VSS	10	11	DCLK