

Preliminary Data Sheet

Insulated Gate Bipolar Transistor

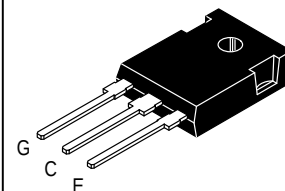
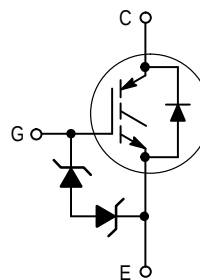
N-Channel Enhancement-Mode Silicon Gate

MGW21N60ED

IGBT IN TO-247
21 A @ 90°C
31 A @ 25°C
600 VOLTS
SHORT CIRCUIT RATED
ON-VOLTAGE

This Insulated Gate Bipolar Transistor (IGBT) is co-packaged with a soft recovery ultra-fast rectifier and uses an advanced termination scheme to provide an enhanced and reliable high voltage-blocking capability. Its new 600V IGBT technology is specifically suited for applications requiring both a high temperature short circuit capability and a low $V_{CE(on)}$. It also provides fast switching characteristics and results in efficient operation at high frequencies. Co-packaged IGBTs save space, reduce assembly time and cost. This new E-series introduces an energy efficient, ESD protected, and rugged short circuit device.

- Industry Standard TO-247 Package
- High Speed: $E_{off} = 65 \mu\text{J/A}$ typical at 125°C
- High Voltage Short Circuit Capability – 10 μs minimum at 125°C, 400 V
- Low On-Voltage — 2.1 V typical at 20 A, 125°C
- Soft Recovery Free Wheeling Diode is included in the Package
- Robust High Voltage Termination
- ESD Protection Gate-Emitter Zener Diodes



CASE 340K-01,
TO-247 AE

MAXIMUM RATINGS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CES}	600	Vdc
Collector-Gate Voltage ($R_{GE} = 1.0 \text{ M}\Omega$)	V_{CGR}	600	Vdc
Gate-Emitter Voltage — Continuous	V_{GE}	± 20	Vdc
Collector Current — Continuous @ $T_C = 25^\circ\text{C}$ — Continuous @ $T_C = 90^\circ\text{C}$ — Repetitive Pulsed Current (1)	I_{C25} I_{C90} I_{CM}	31 21 42	Adc Adc Apk
Total Power Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	142 1.14	Watts W/°C
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-55 to 150	°C
Short Circuit Withstand Time ($V_{CC} = 400 \text{ Vdc}$, $V_{GE} = 15 \text{ Vdc}$, $T_J = 125^\circ\text{C}$, $R_G = 20 \Omega$)	t_{sc}	10	μs
Thermal Resistance — Junction to Case – IGBT — Junction to Diode — Junction to Ambient	$R_{\theta JC}$ $R_{\theta JD}$ $R_{\theta JA}$	0.88 1.4 45	°C/W
Maximum Lead Temperature for Soldering Purposes, 1/8" from case for 5 seconds	T_L	260	°C
Mounting Torque, 6-32 or M3 screw	10 lbf•in (1.13 N•m)		

(1) Pulse width is limited by maximum junction temperature. Repetitive rating.

This document contains information on a new product. Specifications and information herein are subject to change without notice.

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ELECTRICAL CHARACTERISTICS ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
OFF CHARACTERISTICS					
Collector-to-Emitter Breakdown Voltage (V _{GE} = 0 Vdc, I _C = 25 μAdc) Temperature Coefficient (Positive)	V _{(BR)CES}	600 —	— 870	— —	Vdc mV/°C
Emitter-to-Collector Breakdown Voltage (V _{GE} = 0 Vdc, I _{EC} = 100 mAdc)	BV _{ECS}	15	—	—	Vdc
Zero Gate Voltage Collector Current (V _{CE} = 600 Vdc, V _{GE} = 0 Vdc) (V _{CE} = 600 Vdc, V _{GE} = 0 Vdc, T _J = 125°C)	I _{CES}	— —	— —	10 200	μAdc
Gate-Body Leakage Current (V _{GE} = ± 20 Vdc, V _{CE} = 0 Vdc)	I _{GES}	—	—	50	μAdc
ON CHARACTERISTICS (1)					
Collector-to-Emitter On-State Voltage (V _{GE} = 15 Vdc, I _C = 10 Adc) (V _{GE} = 15 Vdc, I _C = 10 Adc, T _J = 125°C) (V _{GE} = 15 Vdc, I _C = 20 Adc)	V _{CE(on)}	— — —	1.7 1.5 2.2	2.1 — 2.5	Vdc
Gate Threshold Voltage (V _{CE} = V _{GE} , I _C = 1.0 mAdc) Threshold Temperature Coefficient (Negative)	V _{GE(th)}	4.0 —	6.0 10	8.0 —	Vdc mV/°C
Forward Transconductance (V _{CE} = 10 Vdc, I _C = 20 Adc)	g _{fe}	—	8.6	—	Mhos
DYNAMIC CHARACTERISTICS					
Input Capacitance	(V _{CE} = 25 Vdc, V _{GE} = 0 Vdc, f = 1.0 MHz)	C _{ies}	—	1605	pF
Output Capacitance		C _{oes}	—	146	
Transfer Capacitance		C _{res}	—	23	
SWITCHING CHARACTERISTICS (1)					
Turn-On Delay Time	(V _{CC} = 360 Vdc, I _C = 20 Adc, V _{GE} = 15 Vdc, L = 300 μH, R _G = 20 Ω, T _J = 25°C) Energy losses include “tail”	t _{d(on)}	—	29	ns
Rise Time		t _r	—	60	
Turn-Off Delay Time		t _{d(off)}	—	238	
Fall Time		t _f	—	140	
Turn-Off Switching Loss		E _{off}	—	0.8	mJ
Turn-On Switching Loss		E _{on}	—	0.6	
Total Switching Loss		E _{ts}	—	1.4	
Turn-On Delay Time	(V _{CC} = 360 Vdc, I _C = 20 Adc, V _{GE} = 15 Vdc, L = 300 μH, R _G = 20 Ω, T _J = 125°C) Energy losses include “tail”	t _{d(on)}	—	28	ns
Rise Time		t _r	—	62	
Turn-Off Delay Time		t _{d(off)}	—	338	
Fall Time		t _f	—	220	
Turn-Off Switching Loss		E _{off}	—	1.3	mJ
Turn-On Switching Loss		E _{on}	—	0.8	
Total Switching Loss		E _{ts}	—	2.1	
Gate Charge	(V _{CC} = 360 Vdc, I _C = 20 Adc, V _{GE} = 15 Vdc)	Q _T	—	86	nC
		Q ₁	—	18	
		Q ₂	—	39	
DIODE CHARACTERISTICS					
Diode Forward Voltage Drop (I _{EC} = 10 Adc) (I _{EC} = 10 Adc, T _J = 125°C) (I _{EC} = 17 Adc)	V _{FEC}	— — 1.7	1.6 1.3 2.0	1.9 — 2.3	Vdc

(1) Pulse Test: Pulse Width $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 2\%$.

(continued)

ELECTRICAL CHARACTERISTICS — continued ($T_J = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Typ	Max	Unit
DIODE CHARACTERISTICS — continued					
Reverse Recovery Time	t_{rr}	—	94	—	ns
	t_a	—	32	—	
	t_b	—	62	—	
Reverse Recovery Stored Charge	Q_{RR}	—	0.16	—	μC
Reverse Recovery Time	t_{rr}	—	145	—	ns
	t_a	—	50	—	
	t_b	—	95	—	
Reverse Recovery Stored Charge	Q_{RR}	—	0.75	—	μC
INTERNAL PACKAGE INDUCTANCE					
Internal Emitter Inductance (Measured from the emitter lead 0.25" from package to emitter bond pad)	L_E	—	13	—	nH

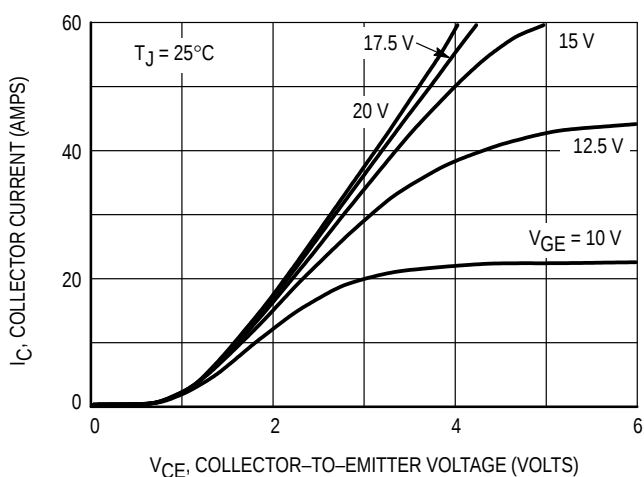


Figure 1. Output Characteristics

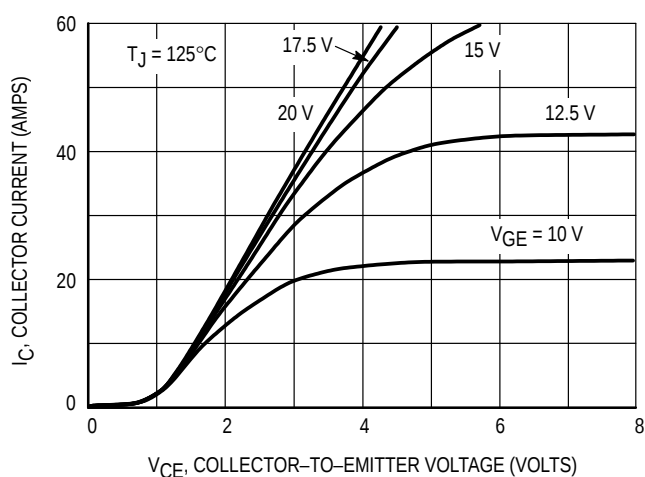


Figure 2. Output Characteristics

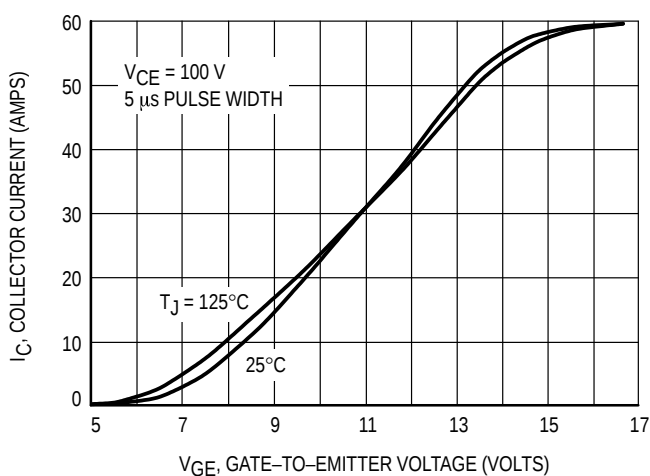


Figure 3. Transfer Characteristics

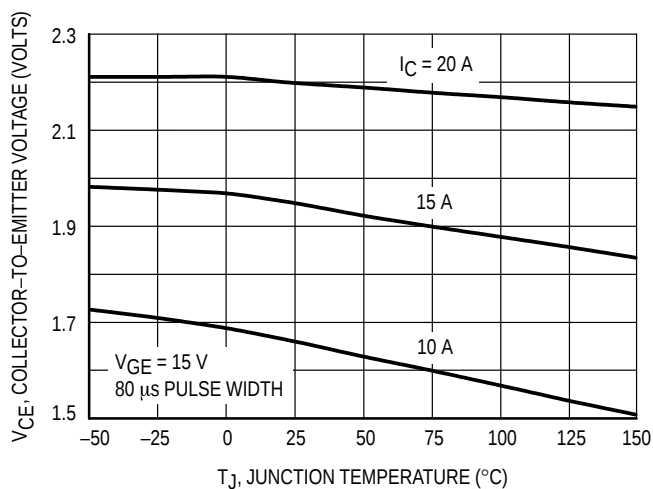


Figure 4. Collector-To-Emitter Saturation Voltage versus Junction Temperature

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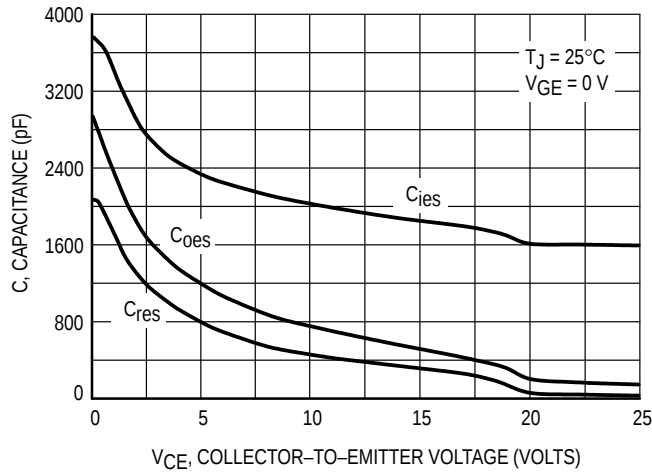


Figure 5. Capacitance Variation

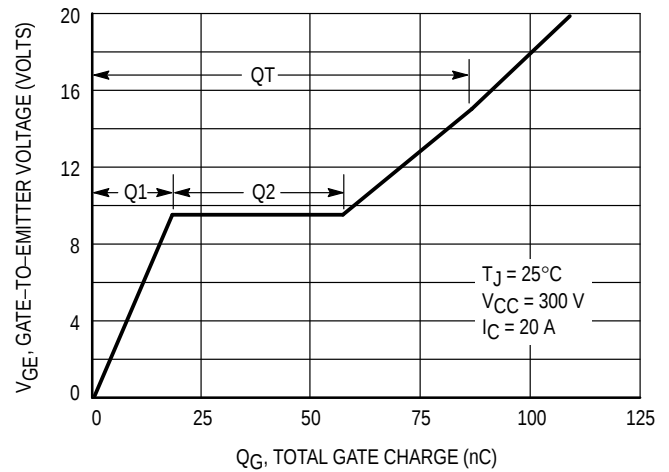


Figure 6. Gate-To-Emitter Voltage versus Total Charge

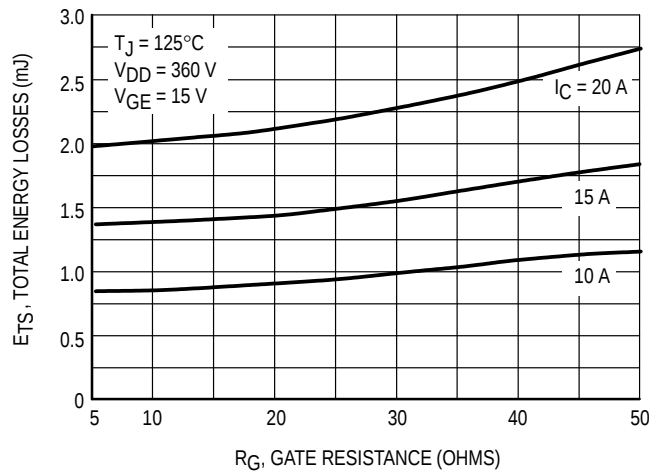


Figure 7. Total Energy Losses versus Gate Resistance

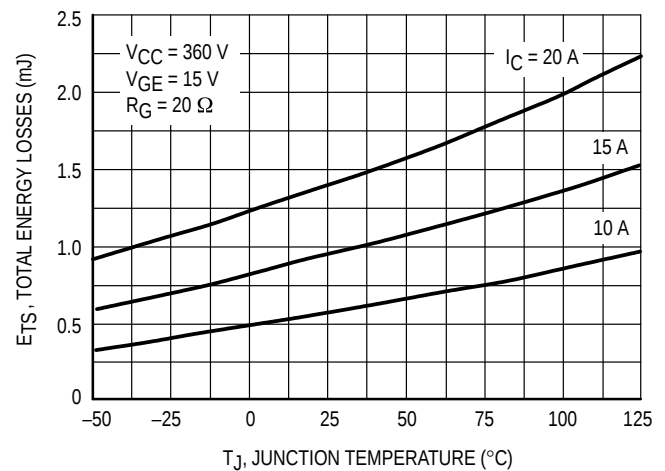


Figure 8. Total Energy Losses versus Junction Temperature

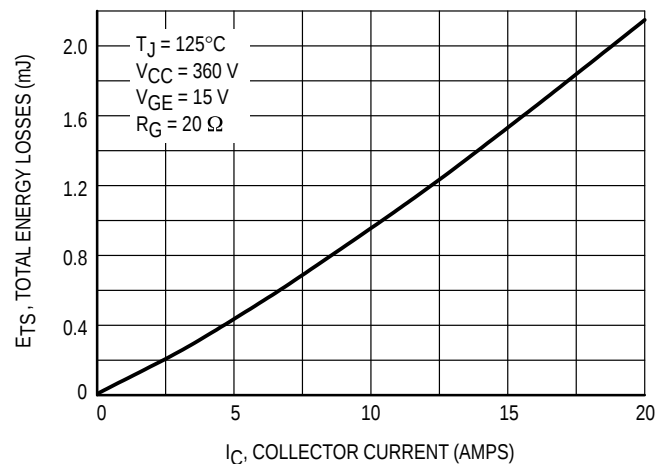


Figure 9. Total Energy Losses versus Collector Current

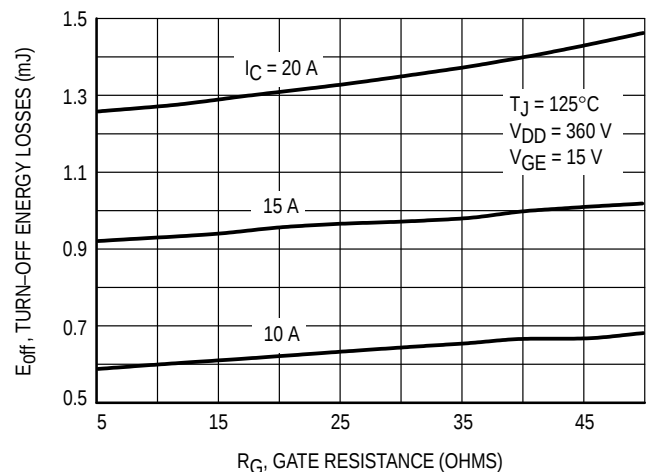


Figure 10. Turn-Off Losses versus Gate Resistance

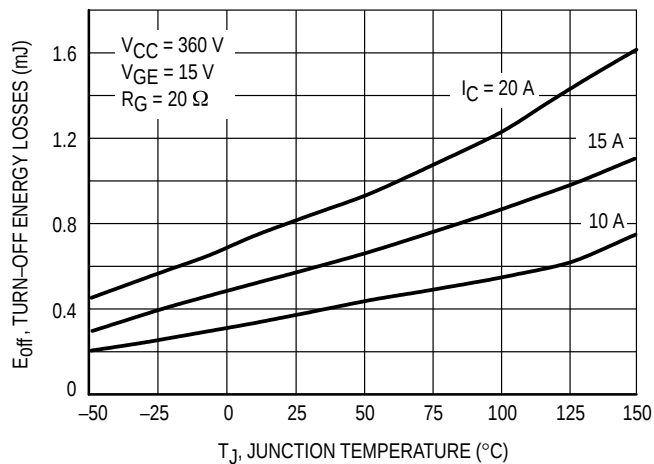


Figure 11. Turn-Off Losses versus Junction Temperature

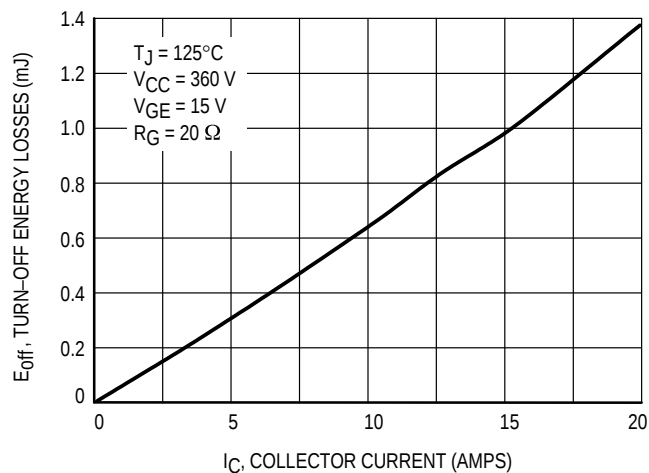


Figure 12. Turn-Off Losses versus Collector Current

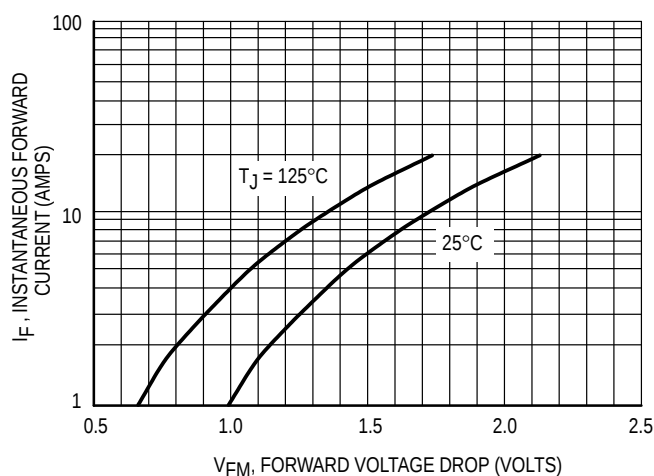


Figure 13. Forward Characteristics versus Current

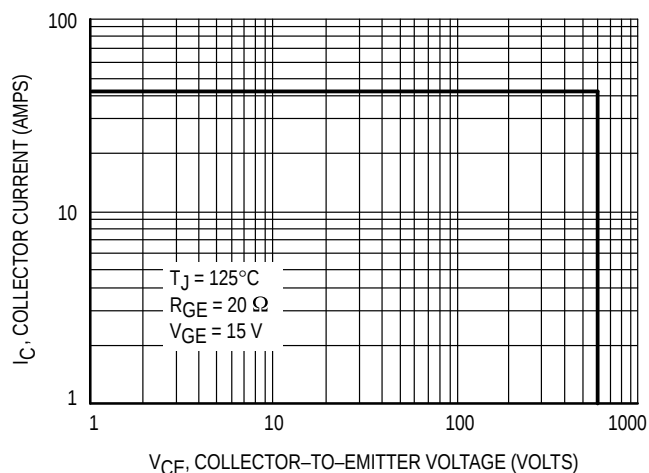
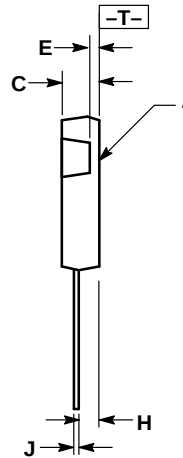
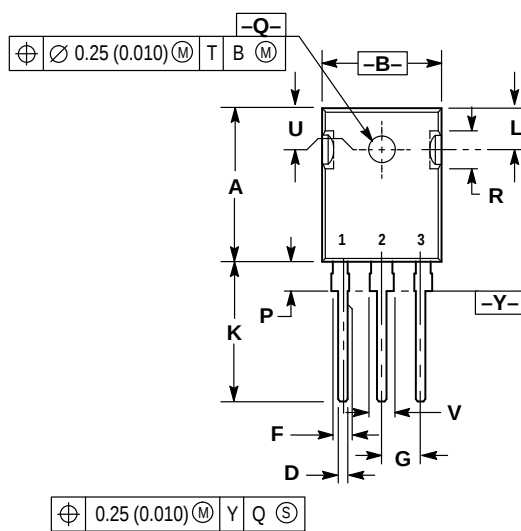


Figure 14. Reverse Biased Safe Operating Area

PACKAGE DIMENSIONS



NOTES:
DIMENSIONING AND TOLERANCING PER ANSI
Y14.5M, 1982.
CONTROLLING DIMENSION: MILLIMETER.

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	19.7	20.3	0.776	0.799
B	15.3	15.9	0.602	0.626
C	4.7	5.3	0.185	0.209
D	1.0	1.4	0.039	0.055
E	1.27 REF		0.050 REF	
F	2.0	2.4	0.079	0.094
G	5.5 BSC		0.216 BSC	
H	2.2	2.6	0.087	0.102
J	0.4	0.8	0.016	0.031
K	14.2	14.8	0.559	0.583
L	5.5 NOM		0.217 NOM	
P	3.7	4.3	0.146	0.169
Q	3.55	3.65	0.140	0.144
R	5.0 NOM		0.197 NOM	
U	5.5 BSC		0.217 BSC	
V	3.0	3.4	0.118	0.134

STYLE 4:
PIN 1. GATE
2. COLLECTOR
3. EMITTER
4. COLLECTOR

CASE 340K-01 ISSUE A

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