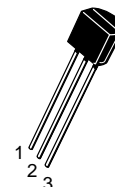
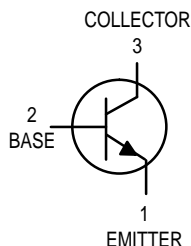


# Amplifier Transistor

## NPN Silicon

**MPS6530**



CASE 29-04, STYLE 1  
TO-92 (TO-226AA)

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector–Emitter Voltage	$V_{CE0}$	40	Vdc
Collector–Base Voltage	$V_{CBO}$	60	Vdc
Emitter–Base Voltage	$V_{EBO}$	5.0	Vdc
Collector Current — Continuous	$I_C$	600	mAdc
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above $25^\circ\text{C}$	$P_D$	625	mW
Junction Temperature	$T_J, T_{stg}$	150	$^\circ\text{C}$

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max	Unit
Thermal Resistance, Junction to Ambient	$R_{\theta JA}$	0.2	$^\circ\text{C}/\text{mW}$

### ELECTRICAL CHARACTERISTICS ( $T_A = 25^\circ\text{C}$ unless otherwise noted)

Characteristic	Symbol	Min	Max	Unit
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### OFF CHARACTERISTICS

Collector–Emitter Breakdown Voltage ( $I_C = 10 \text{ mAdc}, I_B = 0$ )	$V_{(BR)CE0}$	40	—	Vdc
Collector–Base Breakdown Voltage ( $I_C = 10 \mu\text{Adc}, I_E = 0$ )	$V_{(BR)CBO}$	60	—	Vdc
Emitter–Base Breakdown Voltage ( $I_B = 10 \mu\text{Adc}, I_C = 0$ )	$V_{(BR)EBO}$	5.0	—	Vdc
Collector Cutoff Current ( $V_{CB} = 40 \text{ Vdc}, I_E = 0$ ) ( $V_{CB} = 40 \text{ Vdc}, I_E = 0, T_A = 60^\circ\text{C}$ )	$I_{CBO}$	—	0.05 2.0	$\mu\text{Adc}$

# MPS6530

## ELECTRICAL CHARACTERISTICS (T<sub>A</sub> = 25°C unless otherwise noted) (Continued)

Characteristic	Symbol	Min	Max	Unit
<b>ON CHARACTERISTICS</b>				
DC Current Gain (I <sub>C</sub> = 10 mA <sub>dc</sub> , V <sub>CE</sub> = 1.0 V <sub>dc</sub> ) (I <sub>C</sub> = 100 mA <sub>dc</sub> , V <sub>CE</sub> = 1.0 V <sub>dc</sub> ) (I <sub>C</sub> = 500 mA <sub>dc</sub> , V <sub>CE</sub> = 10 V <sub>dc</sub> )	h <sub>FE</sub>	30 40 25	— 120 —	—
Collector–Emitter Saturation Voltage (I <sub>C</sub> = 100 mA <sub>dc</sub> , I <sub>B</sub> = 10 mA <sub>dc</sub> )	V <sub>CE(sat)</sub>	—	0.5	V <sub>dc</sub>
Base–Emitter Saturation Voltage (I <sub>C</sub> = 100 mA <sub>dc</sub> , I <sub>B</sub> = 10 mA <sub>dc</sub> )	V <sub>BE(sat)</sub>	—	1.0	V <sub>dc</sub>
<b>SMALL–SIGNAL CHARACTERISTICS</b>				
Output Capacitance (V <sub>CB</sub> = 10 V <sub>dc</sub> , I <sub>E</sub> = 0, f = 1.0 MHz)	C <sub>obo</sub>	—	5.0	pF

### SWITCHING TIME EQUIVALENT TEST CIRCUITS

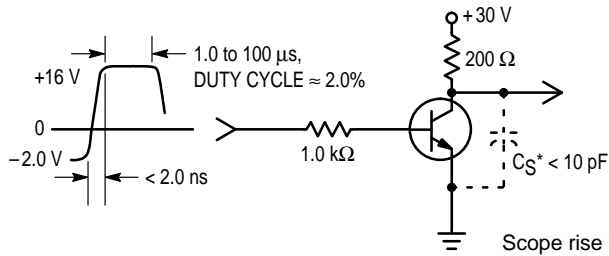


Figure 1. Turn–On Time

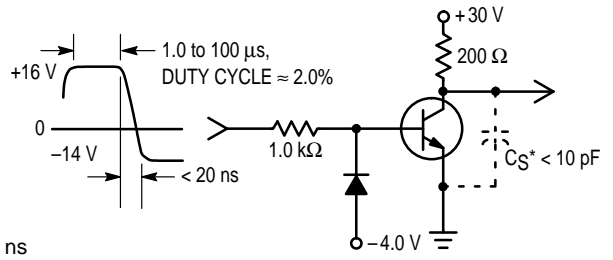


Figure 2. Turn–Off Time

Scope rise time < 4.0 ns  
\*Total shunt capacitance of test jig connectors, and oscilloscope

TRANSIENT CHARACTERISTICS

— 25°C    - - - 100°C

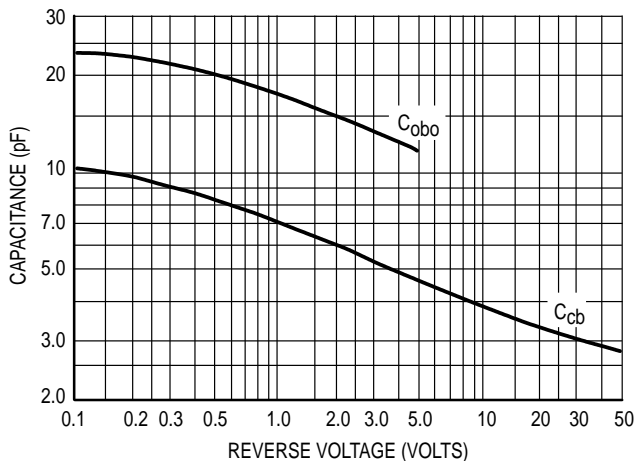


Figure 3. Capacitances

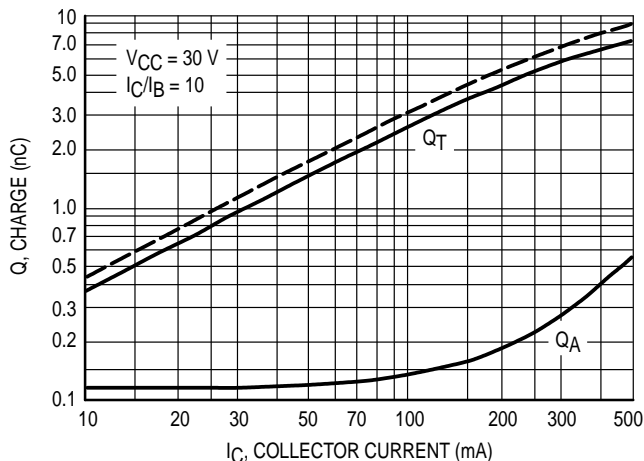


Figure 4. Charge Data

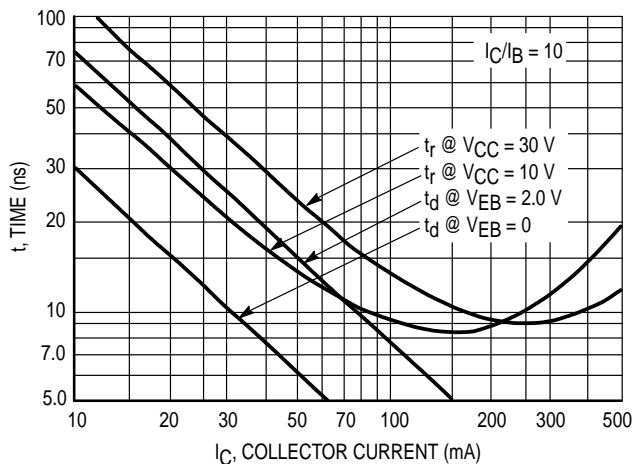


Figure 5. Turn-On Time

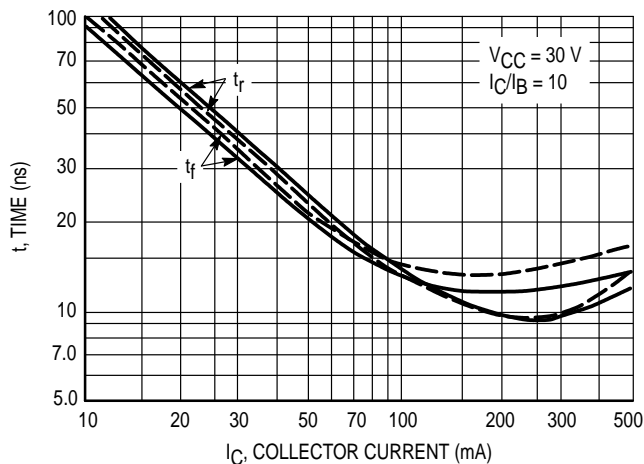


Figure 6. Rise and Fall Times

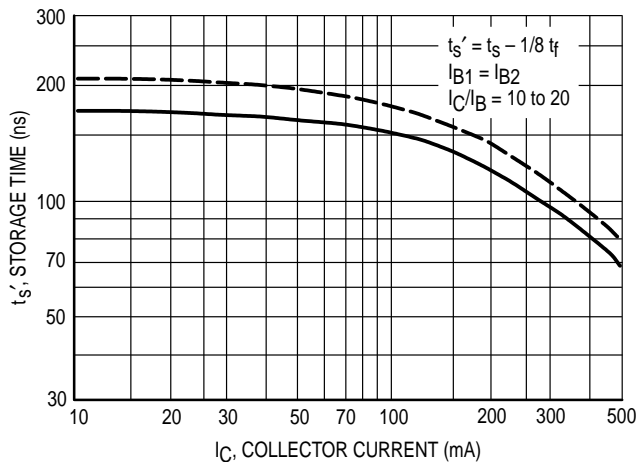


Figure 7. Storage Time

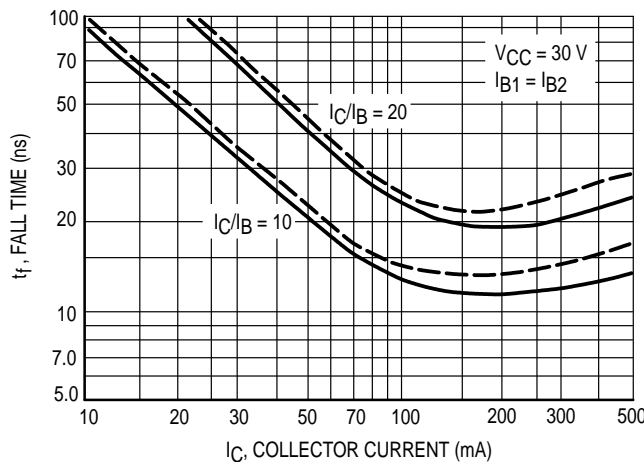


Figure 8. Fall Time

SMALL-SIGNAL CHARACTERISTICS

NOISE FIGURE

$V_{CE} = 10 \text{ Vdc}$ ,  $T_A = 25^\circ\text{C}$

Bandwidth = 1.0 Hz

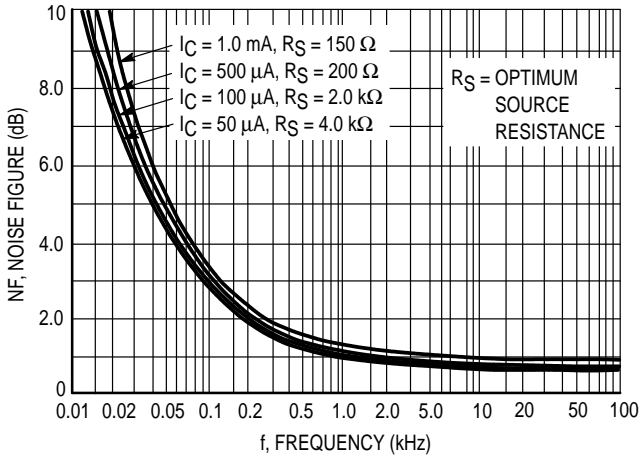


Figure 9. Frequency Effects

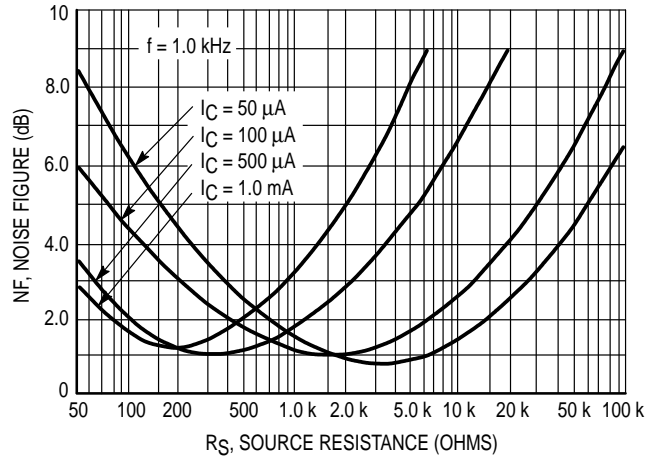


Figure 10. Source Resistance Effects

h PARAMETERS

$V_{CE} = 10 \text{ Vdc}$ ,  $f = 1.0 \text{ kHz}$ ,  $T_A = 25^\circ\text{C}$

This group of graphs illustrates the relationship between  $h_{fe}$  and other "h" parameters for this series of transistors. To obtain these curves, a high-gain and a low-gain unit were

selected from both the 2N4400 and 2N4401 lines, and the same units were used to develop the correspondingly numbered curves on each graph.

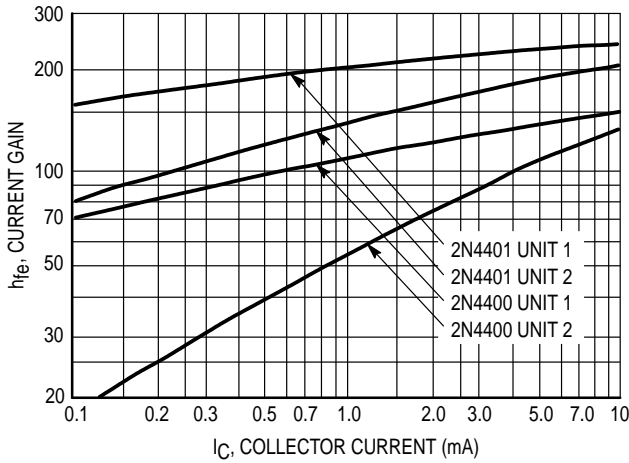


Figure 11. Current Gain

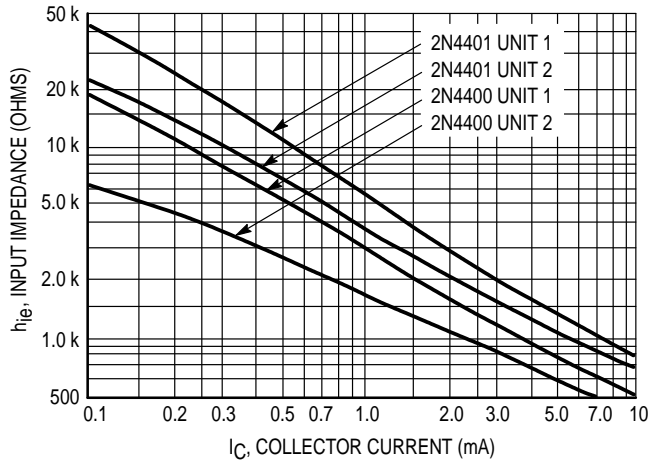


Figure 12. Input Impedance

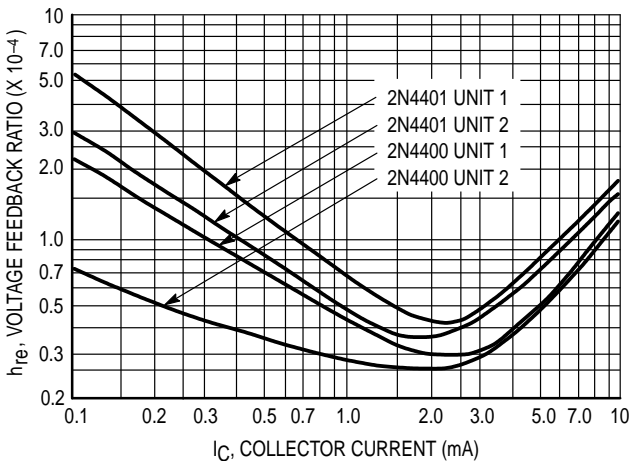


Figure 13. Voltage Feedback Ratio

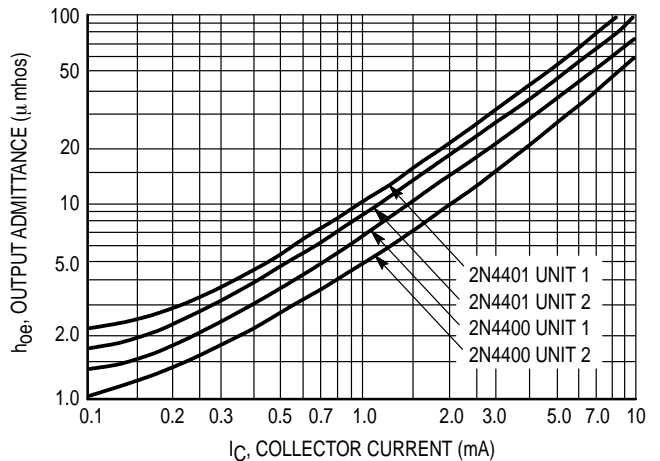


Figure 14. Output Admittance

STATIC CHARACTERISTICS

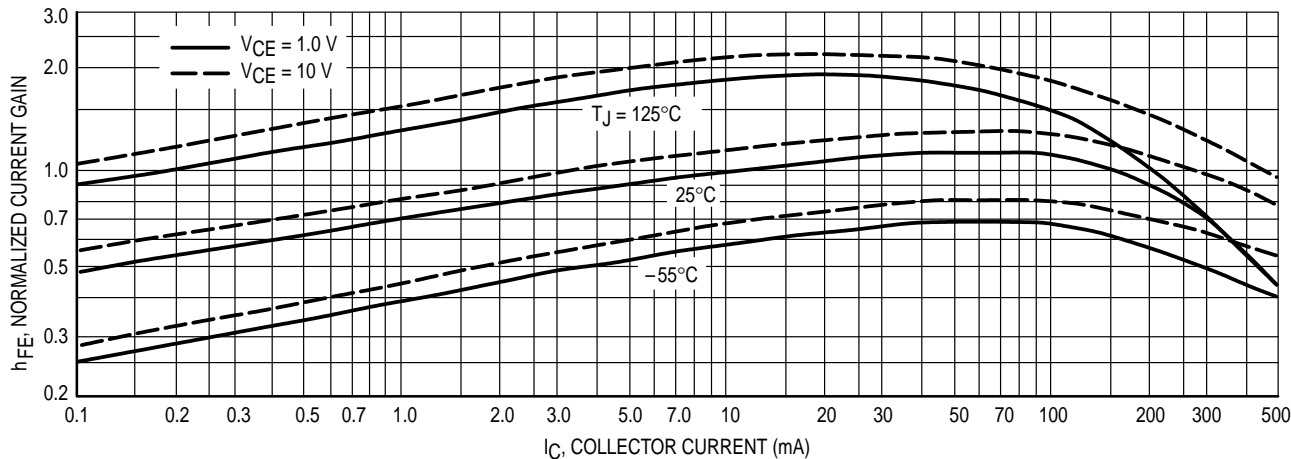


Figure 15. DC Current Gain

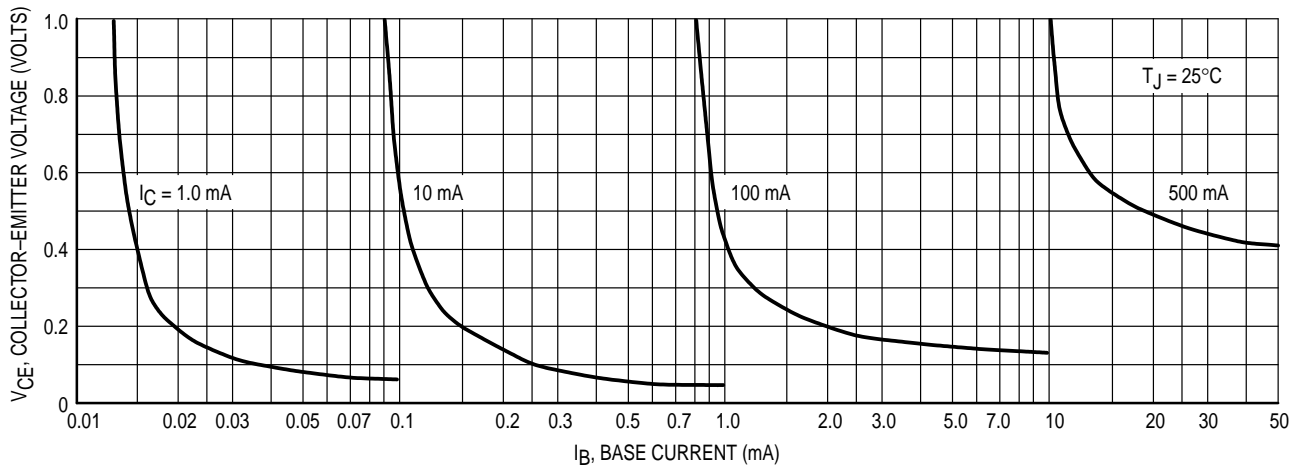


Figure 16. Collector Saturation Region

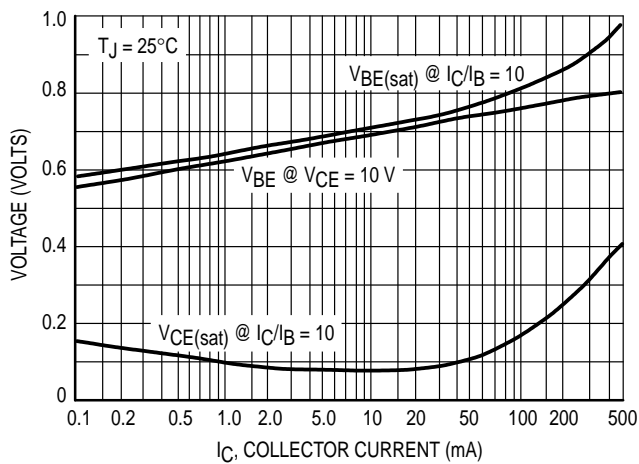


Figure 17. "On" Voltages

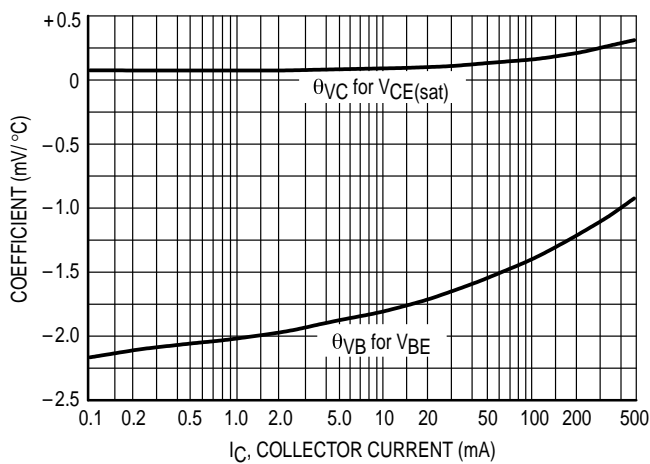
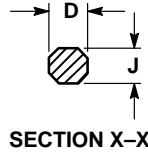
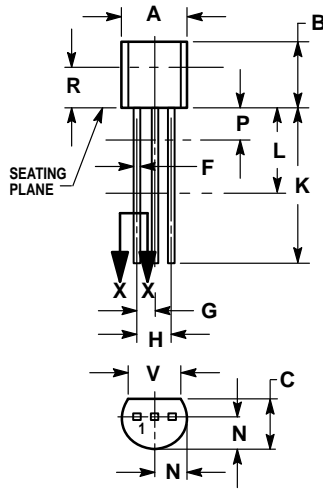


Figure 18. Temperature Coefficients

PACKAGE DIMENSIONS



CASE 029-04  
(TO-226AA)  
ISSUE AD

- NOTES:
1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
  2. CONTROLLING DIMENSION: INCH.
  3. CONTOUR OF PACKAGE BEYOND DIMENSION R IS UNCONTROLLED.
  4. DIMENSION F APPLIES BETWEEN P AND L. DIMENSION D AND J APPLY BETWEEN L AND K. MINIMUM LEAD DIMENSION IS UNCONTROLLED IN P AND BEYOND DIMENSION K MINIMUM.

DIM	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.175	0.205	4.45	5.20
B	0.170	0.210	4.32	5.33
C	0.125	0.165	3.18	4.19
D	0.016	0.022	0.41	0.55
F	0.016	0.019	0.41	0.48
G	0.045	0.055	1.15	1.39
H	0.095	0.105	2.42	2.66
J	0.015	0.020	0.39	0.50
K	0.500	—	12.70	—
L	0.250	—	6.35	—
N	0.080	0.105	2.04	2.66
P	—	0.100	—	2.54
R	0.115	—	2.93	—
V	0.135	—	3.43	—

- STYLE 1:
1. EMITTER
  2. BASE
  3. COLLECTOR

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