

## F100393

### Low Power 9-Bit ECL-to-TTL Translator with Latches

#### General Description

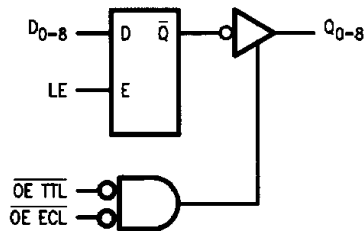
The F100393 is a 9-bit translator for converting F100K logic levels to FAST® TTL logic levels. A LOW on the latch enable (LE) latches the data at the input state. A HIGH on the LE makes the latches transparent. A HIGH on either the ECL or TTL output enable ( $\overline{OE}$  ECL or  $\overline{OE}$  TTL), holds the outputs in a high impedance state.

The F100393 is designed with FAST® TTL, 64 mA outputs for Bus Driving capability. All ECL inputs have 50 k $\Omega$  pull down resistors. When the inputs are either unconnected or at the same potential, the outputs will go LOW.

#### Features

- 64 mA  $I_{OL}$  drive capability
- 2000V ESD protection
- -4.2V to -5.7V operating range
- Latched outputs
- FAST® TTL outputs

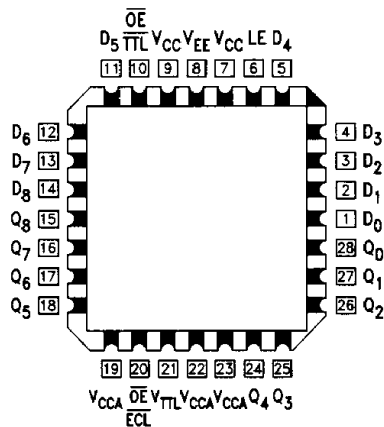
#### Logic Symbol



TL/F/10650-1

Pin Names	Description
D <sub>0</sub> -D <sub>8</sub>	Data Inputs (ECL)
Q <sub>0</sub> -Q <sub>8</sub>	Data Outputs (TTL)
LE	Latch Enable Input
$\overline{OE}$ TTL	Output Enable (TTL)
$\overline{OE}$ ECL	Output Enable (ECL)

#### Connection Diagram



TL/F/10650-2