



MILITARY DATA SHEET

MNLM120-15-H REV 0BL

Original Creation Date: 07/06/95
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THREE TERMINAL NEGATIVE REGULATOR

Industry Part Number

LM120H-15

NS Part Numbers

LM120H-15/883

Prime Die

LMJ056DG-15

Processing

MIL-STD-883, Method 5004

Quality Conformance Inspection

MIL-STD-883, Method 5005

Subgrp Description

Temp (°C)

1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55

Electrical Characteristics

DC PARAMETERS

(The following conditions apply to all the following parameters, unless otherwise specified.)
DC: $V_{in} = -10.5V$, $I_l = 5mA$

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
Iq	Quiescent Current	$V_{in} = -17V$	2			4	mA	1, 2, 3
		$V_{in} = -35V$	2			4	mA	1, 2, 3
Delta Iq	Quiescent Current Change	$V_{in} = -17V$, $5mA \leq I_l \leq 200mA$	2			0.4	mA	1
		$V_{in} = -17V$, $5mA \leq I_l \leq 200mA$	2			0.5	mA	2, 3
		$-17V \leq V_{in} \leq -35V$	2			0.4	mA	1
		$-17V \leq V_{in} \leq -35V$	2			0.5	mA	2, 3
Rload	Load Regulation	$V_{in} = -20V$, $5mA \leq I_l \leq 200mA$	2			± 25	mV	1
		$V_{in} = -20V$, $5mA \leq I_l \leq 200mA$	2			± 50	mV	2, 3
Rline	Line Regulation	$-17V \leq V_{in} \leq -35V$	2			± 10	mV	1
		$-17V \leq V_{in} \leq -35V$	2			± 20	mV	2, 3
Vout	Output Voltage	$V_{in} = -20V$	2		-15.3	-14.7	V	1
		$V_{in} = -35V$	2		-15.5	-14.5	V	1, 2, 3
		$V_{in} = -35V$, $I_l = 100mA$	2		-15.5	-14.5	V	1, 2, 3
		$V_{in} = -17.5V$	2		-15.5	-14.5	V	1, 2, 3
		$V_{in} = -17.5V$, $I_l = 200mA$	2		-15.5	-14.5	V	1, 2, 3
Ios	Short Circuit Output Current	$V_{in} = -35V$	2		-1.5	-0.1	A	1
Rr	Ripple Rejection	$f = 120Hz$	1, 2		56		dB	4

DC PARAMETERS: DRIFT VALUES

(The following conditions apply to all the following parameters, unless otherwise specified.)
DC: $V_{in} = -10.5V$, $I_l = 5mA$. "Deltas not required on B-Level product. Deltas required for S-Level product ONLY as specified on Internal Processing Instructions (IPI)."

Iq	Quiescent Current	$V_{in} = -17V$	2		-0.4	0.4	mA	1
Vout	Output Voltage	$V_{in} = -17.5V$	2		-0.15	0.15	V	1

Note 1: Bench test per (SG)RPI-3-363.

Note 2: Pre Burn-In Stress Test per (SG)RPI-3-371.

Graphics and Diagrams

GRAPHICS#	DESCRIPTION
09107HR	(blank)
H03ARB	(blank)

See attached graphics following this page.