

MNLM140-05H REV 0B0

 Original Creation Date: 05/09/95
 Last Update Date: 09/05/95
 Last Major Revision Date: 05/09/95

THREE TERMINAL POSITIVE REGULATORS

General Description

The monolithic 3-terminal positive voltage regulators employ internal current-limiting, thermal shutdown and safe-area compensation, making them essentially indestructible. If adequate heat sinking is provided, they can deliver over 0.5A output current. They are intended as fixed voltage regulators in a wide range of applications including local (on-card) regulation for elimination of noise and distribution problems associated with single-point regulation. In addition to use as fixed voltage regulators, these devices can be used with external components to obtain adjustable output voltages and currents.

Considerable effort was expended to make the entire series of regulators easy to use and minimize the number of external components. It is not necessary to bypass the output, although this does improve transient response. Input bypassing is needed only if the regulator is located far from the filter capacitor of the power supply.

Industry Part Number

LM140

NS Part Numbers

LM140H-5.0/883

Prime Die

LM141

Processing

MIL-STD-883, Method 5004

Quality Conformance Inspection

MIL-STD-883, Method 5005

Subgrp	Description	Temp (°C)
1	Static tests at	+25
2	Static tests at	+125
3	Static tests at	-55
4	Dynamic tests at	+25
5	Dynamic tests at	+125
6	Dynamic tests at	-55
7	Functional tests at	+25
8A	Functional tests at	+125
8B	Functional tests at	-55
9	Switching tests at	+25
10	Switching tests at	+125
11	Switching tests at	-55

Features

- Output current in excess of 0.5A
- No external components
- Internal thermal overload protection
- Internal short circuit current-limiting
- Output transistor safe-area compensation

(Absolute Maximum Ratings)

(Note 1)

DC Input Voltage	35V
Internal Power Dissipation (Note 2)	Internally Limited
Maximum Junction Temperature	150 C
Storage Temperature Range	-65 C to +150 C
Lead Temperature Soldering, (10 seconds)	300 C
Thermal Resistance ThetaJA (Still Air) (500 LF/Min Air Flow)	232 C/W 77 C/W
ThetaJC	15 C/W
ESD Susceptibility (Note 3)	2KV

Note 1: Absolute Maximum Ratings are limits beyond which damage to the device may occur. Operating Conditions are conditions under which the device functions but the specification might not be guaranteed. For guaranteed specifications and test conditions see the Electrical Characteristics.

Note 2: The Maximum allowable power dissipation at any ambient temperature is a function of the maximum junction temperature for operation ($T_{jMAX} = 150\text{ C}$), the junction-to-ambient thermal resistance (Θ_{JA}), and the ambient temperature (T_A), $P_{DMAX} = (T_{jMAX} - T_A)/\Theta_{JA}$. If this dissipation is exceeded, the die temperature will rise above T_{jMAX} and the electrical specifications do not apply. If the die temperature rises above 150 C , the device will go into thermal shutdown.

Note 3: Human body model, 100pF discharged through 1.5K Ohms .

Recommended Operating Conditions

(Note 1)

Temperature Range (T_A) (Note 2)	-55 C to +125 C
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Electrical Characteristics

DC PARAMETERS:

(The following conditions apply to all the following parameters, unless otherwise specified.)

DC: $V_{in} = 10V$, $I_L = 350mA$

SYMBOL	PARAMETER	CONDITIONS	NOTES	PIN-NAME	MIN	MAX	UNIT	SUB-GROUPS
Vout	Output Voltage	$V_{in} = 35V$, $I_L = 5mA$			4.75	5.75	V	1
					4.80	5.20	V	1
		$V_{in} = 8V$			4.70	5.30	V	1, 2, 3
		$V_{in} = 8V$, $I_L = 5mA$			4.70	5.30	V	1, 2, 3
		$V_{in} = 20V$, $I_L = 5mA$			4.70	5.30	V	1, 2, 3
		$V_{in} = 20V$			4.70	5.30	V	1, 2, 3
RLINE	Line Regulation	$7V \leq V_{in} \leq 25V$, $I_L = 200mA$			-50	50	mV	1
		$8V \leq V_{in} \leq 25V$, $I_L = 200mA$			-50	50	mV	2, 3
		$8V \leq V_{in} \leq 20V$, $I_L = 200mA$			-25	25	mV	1
RLOAD	Load Regulation	$5mA \leq I_L \leq 500mA$			-50	50	mV	1
					-100	100	mV	2, 3
		$5mA \leq I_L \leq 200mA$			-25	25	mV	1
					-50	50	mV	2, 3
IQ	Quiescent Current					7	mA	1, 2, 3
Delta IQ	Quiescent Current Change	$8V \leq V_{in} \leq 25V$, $I_L = 200mA$			-0.8	0.8	mA	1, 2, 3
		$5mA \leq I_L \leq 350mA$			-0.5	0.5	mA	1, 2, 3
Ipk	Peak Current	$V_{in} - V_{out} = 7V$	1		0.4	2.0	A	1, 2, 3
Vdo	Dropout Voltage		2			2.5	V	1
Ios	Short Circuit Current	$V_{in} = 35V$				1.0	A	1, 2, 3

AC PARAMETERS:

(The following conditions apply to all the following parameters, unless otherwise specified.)

AC: $V_{in} = 10V$, $I_L = 350mA$

Rr	Ripple Rejection	$I_L = 125mA$, $e_i = 1V_{rms}$, $f = 2.4KHz$, $V_{in} = 10V$			62		dB	4, 5, 6
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Note 1: V_{out} is set to 90% Reference Voltage.

Note 2: $V_{do} = V_{in} - V_{out}$ when V_o is 95% of Reference Voltage.

Graphics and Diagrams

GRAPHICS#	DESCRIPTION
H03ARC	(blank)

See attached graphics following this page.