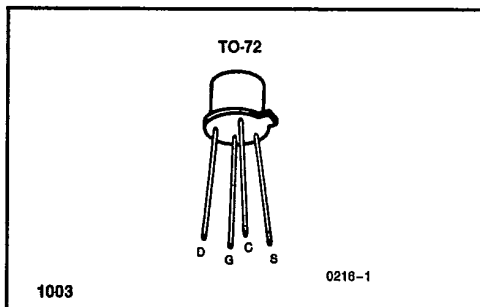


2N4351**N-Channel Enhancement Mode
MOSFET General Purpose
Amplifier/Switch**

2N4351

FEATURES

- Low ON Resistance
- Low Capacitance
- High Gain
- High Gate Breakdown Voltage
- Low Threshold Voltage

PIN CONFIGURATION**ABSOLUTE MAXIMUM RATINGS**(T_A = 25°C unless otherwise noted)

Drain-Source Voltage or Drain-Body Voltage	25V
Peak Gate-Source Voltage (Note 1)	±125V
Drain Current	100mA
Storage Temperature Range	-65°C to +200°C
Operating Temperature Range	-55°C to +150°C
Lead Temperature (Soldering, 10sec)	+300°C
Power Dissipation	375mW
Derate above 25°C	3mW/°C

NOTE: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ORDERING INFORMATION

TO-72

2N4351

ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise specified) Substrate connected to source.

Symbol	Parameter	Test Conditions	Min	Max	Units
BV _{DSS}	Drain-Source Breakdown Voltage	I _D = 10μA, V _{GS} = 0	25		V
I _{GSS}	Gate Leakage Current	V _{GS} = ±30V, V _{DS} = 0		10	pA
I _{DSS}	Zero-Gate-Voltage Drain Current	V _{DS} = 10V, V _{GS} = 0		10	nA
V _{GS(th)}	Gate-Source Threshold Voltage	V _{DS} = 10V, I _D = 10μA	1	5	V
I _{D(on)}	'ON' Drain Current	V _{GS} = 10V, V _{DS} = 10V	3		mA
V _{DS(on)}	Drain-Source "ON" Voltage	I _D = 2mA, V _{GS} = 10V		1	V
r _{DS(on)}	Drain-Source Resistance	V _{GS} = 10V, I _D = 0, f = 1kHz		300	ohms
y _{fs}	Forward Transfer Admittance	V _{DS} = 10V, I _D = 2mA, f = 1kHz	1000		μS
C _{rss}	Reverse Transfer Capacitance (Note 2)	V _{DS} = 0, V _{GS} = 0, f = 1MHz		1.3	pF
C _{iss}	Input Capacitance (Note 2)	V _{DS} = 10V, V _{GS} = 0, f = 1MHz		5.0	
C _{d(sub)}	Drain-Substrate Capacitance (Note 2)	V _{D(SUB)} = 10V, f = 1MHz		5.0	
t _{d(on)}	Turn-On Delay (Note 2)			45	ns
t _r	Rise Time (Note 2)			65	
t _{d(off)}	Turn-Off Delay (Note 2)			60	
t _f	Fall Time (Note 2)			100	

NOTES: 1. Device must not be tested at ±125V more than once or longer than 300ms.
2. For design reference only, not 100% tested.

INTERMIL'S SOLE AND EXCLUSIVE WARRANTY OBLIGATION WITH RESPECT TO THIS PRODUCT SHALL BE THAT STATED IN THE WARRANTY ARTICLE OF THE CONDITION OF SALE. THE WARRANTY SHALL BE EXCLUSIVE AND SHALL BE IN LIEU OF ALL OTHER WARRANTIES, EXPRESS, IMPLIED OR STATUTORY, INCLUDING THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR USE.

NOTE: All typical values have been characterized but are not tested.