

3875081 G E SOLID STATE

01E 11062 D

T-35-25

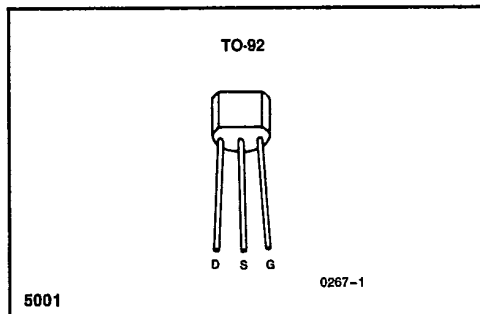


U1897-U1899 N-Channel JFET Switch

FEATURES

- Low Insertion Loss
- No Error or Offset Voltage Generated By Closed Switch

PIN CONFIGURATION



APPLICATIONS

- Analog Switches, Choppers

ABSOLUTE MAXIMUM RATINGS

($T_A = 25^\circ\text{C}$ unless otherwise noted)

Gate-Drain or Gate-Source Voltage		-40V
Forward Gate Current		10mA
Storage Temperature Range		-55°C to +150°C
Operating Temperature Range		-55°C to +135°C
Lead Temperature (Soldering, 10sec)		+300°C
Power Dissipation		350mW
Derate above 25°C		3.2mW/°C

NOTE: Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ORDERING INFORMATION

TO-92	TO-92-18
U1897	U1897-18
U1898	U1898-18
U1899	U1899-18

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	U1897		U1898		U1899		Units
			Min	Max	Min	Max	Min	Max	
BV_{GSS}	Gate-Source Breakdown Voltage	$I_G = -1\mu\text{A}, V_{DS} = 0$	-40		-40		-40		V
I_{GSS}	Gate Reverse Current	$V_{GS} = -20\text{V}, V_{DS} = 0$		-400		-400		-400	pA
I_{DGO}	Drain-Gate Leakage Current	$V_{DG} = 20\text{V}, I_S = 0$		200		200		200	
I_{SGO}	Source-Gate Leakage Current	$V_{SG} = 20\text{V}, I_D = 0$		200		200		200	
$I_{D(off)}$	Drain Cutoff Current	$V_{DS} = 20\text{V}, V_{GS} = -12\text{V}$ (U1897) $V_{GS} = -8\text{V}$ (U1898) $V_{GS} = -6\text{V}$ (U1899) $T_A = 85^\circ\text{C}$		200		200		200	nA
$V_{GS(off)}$	Gate-Source Cutoff Voltage	$V_{DS} = 20\text{V}, I_D = 1\text{nA}$	-5.0	-10	-2.0	-7.0	-1.0	-5.0	
I_{DSS}	Saturation Drain Current (Note 1)	$V_{DS} = 20\text{V}, V_{GS} = 0$	30		15		8.0		mA
$V_{DS(on)}$	Drain-Source ON Voltage	$V_{GS} = 0, I_D = 6.6\text{mA}$ (U1897) $I_D = 4.0\text{mA}$ (U1898) $I_D = 2.5\text{mA}$ (U1899)		0.2		0.2		0.2	V
$r_{DS(on)}$	Static Drain-Source ON Resistance	$I_D = 1\text{mA}, V_{GS} = 0$		30		50		80	Ω

INTERSIL'S SOLE AND EXCLUSIVE WARRANTY OBLIGATION WITH RESPECT TO THIS PRODUCT SHALL BE THAT STATED IN THE WARRANTY ARTICLE OF THE CONDITION OF SALE. THE WARRANTY SHALL BE EXCLUSIVE AND SHALL BE IN LIEU OF ALL OTHER WARRANTIES, EXPRESS, IMPLIED OR STATUTORY, INCLUDING THE IMPLIED WARRANTIES OF MERCHANTABILITY AND FITNESS FOR A PARTICULAR USE.

NOTE: All typical values have been characterized but are not tested.

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U1897-U1899**INTERSIL**

T-35-25

U1897-U1899

ELECTRICAL CHARACTERISTICS (Continued) ($T_A = 25^\circ\text{C}$ unless otherwise specified)

Symbol	Parameter	Test Conditions	U1897		U1898		U1899		Units
			Min	Max	Min	Max	Min	Max	
C_{dg}	Drain-Gate Capacitance	$V_{DG} = 20\text{V}, I_S = 0$		5		5		5	pF
C_{sg}	Source-Gate Capacitance	$V_{SG} = 20\text{V}, I_D = 0$		5		5		5	
C_{iss}	Common-Source Input Capacitance	$V_{DS} = 20\text{V}, V_{GS} = 0$ $f = 1\text{MHz}$ (Note 2)		16		16		16	
C_{rss}	Common-Source Reverse Transfer Capacitance			3.5		3.5		3.5	
$t_{d(on)}$	Turn ON Delay Time (Note 2)	Switching Time Test Conditions		15		15		20	ns
t_r	Rise Time (Note 2)			10		20		40	
t_{off}	Turn OFF Time (Note 2)	V_{DD}		3V		3V		3V	
		$V_{GS(on)}$		0		0		0	
		$V_{GS(off)}$		-12V		-8V		-6V	
		R_L		425 Ω		770 Ω		1120 Ω	
		$I_{D(on)}$		6.6mA		4mA		2.5mA	

NOTES: 1. Pulse test pulsewidth = 300 μs ; duty cycle < 3%.
 2. For design reference only, not 100% tested.

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