

40 V, 200 mA NPN switching transistor Rev. 1 – 7 March 2012

Product data sheet

1. **Product profile**

1.1 General description

NPN single switching transistor in a leadless ultra small SOT883B Surface-Mounted Device (SMD) plastic package.

PNP complement: PMBT3906MB.

1.2 Features and benefits

- Single general-purpose switching transistor
- AEC-Q101 qualified
- Ultra small SMD plastic package
- Board-space reduction
- Low package height of 0.37 mm

1.3 Applications

- General-purpose switching and amplification
- Mobile applications

1.4 Quick reference data

Table 1 Ouick reference data

Table I.	Quick reference data					
Symbol	Parameter	Conditions	Min	Тур	Мах	Unit
V_{CEO}	collector-emitter voltage	open base	-	-	40	V
I _C	collector current		-	-	200	mA
h _{FE}	DC current gain	V _{CE} = 1 V; I _C = 10 mA	100	180	300	

Pinning information 2.

Pin	Description	Simplified outline Graphic sym
1	base	
2	emitter	
3	collector	
		Transparent top view



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3. Ordering information

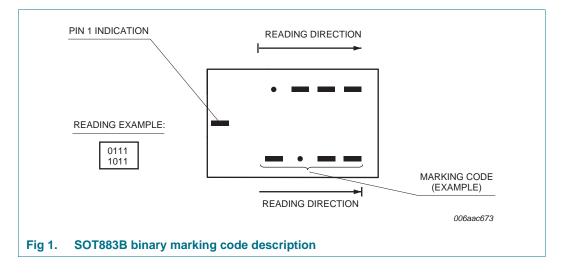
Table 3. Ordering	informatio	n	
Type number	Package		
	Name	Description	Version
PMBT3904MB		leadless ultra small plastic package; 3 solder lands; body $1.0 \times 0.6 \times 0.37$ mm	SOT883B

4. Marking

Table 4. Marking codes	
Type number	Marking code ^[1]
PMBT3904MB	0100 0111

[1] For SOT883B binary marking code description see Figure 1.

4.1 Binary marking code description



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5. Limiting values

Table 5.	. Limiting values rdance with the Absolute Maximum Rating System (IEC 60134).					
Symbol	Parameter	Conditions	Min	Мах	Unit	
V _{CBO}	collector-base voltage	open emitter	-	60	V	
V _{CEO}	collector-emitter voltage	open base	-	40	V	
V _{EBO}	emitter-base voltage	open collector	-	6	V	
I _C	collector current		-	200	mA	
I _{CM}	peak collector current	single pulse; t _p ≤ 1 ms	-	200	mA	
I _{BM}	peak base current	single pulse; t _p ≤ 1 ms	-	100	mA	
P _{tot}	total power dissipation	$T_{amb} \le 25 \ ^{\circ}C$	[1][2]	250	mW	
			[1][3]	590	mW	
Tj	junction temperature		-	150	°C	
T _{amb}	ambient temperature		-55	+150	°C	
T _{stg}	storage temperature		-65	+150	°C	

[1] Reflow soldering is the only recommended soldering method.

[2] Device mounted on an FR4 Printed-Circuit Board (PCB), single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².

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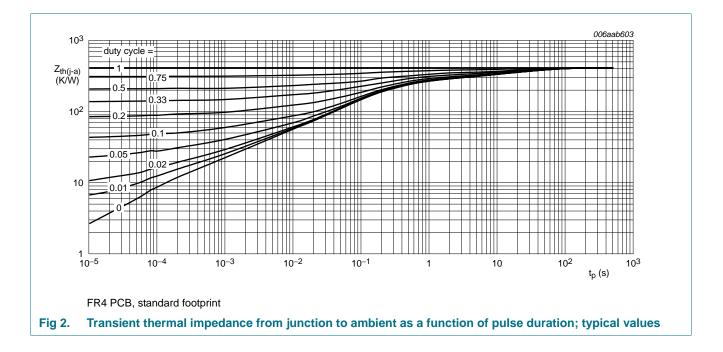
6. Thermal characteristics

Table 6.	Thermal characteristics					
Symbol	Parameter	Conditions	Min	Тур	Max	Unit
R _{th(j-a)}	thermal resistance from	in free air	<u>[1][2]</u> _	-	500	K/W
	junction to ambient		[1][3]	-	212	K/W

[1] Reflow soldering is the only recommended soldering method.

[2] Device mounted on an FR4 PCB, single-sided copper, tin-plated and standard footprint.

[3] Device mounted on an FR4 PCB, single-sided copper, tin-plated, mounting pad for collector 1 cm².



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7. Characteristics

Symbol	Parameter	Conditions		Min	Тур	Max	Unit
I _{CBO}	collector-base cut-off current	$V_{CB} = 30 \text{ V}; I_E = 0 \text{ A}$		-	-	50	nA
I _{EBO}	emitter-base cut-off current	$V_{EB} = 6 V; I_C = 0 A$		-	-	50	nA
h _{FE}	DC current gain	$V_{CE} = 1 V$					
		I _C = 0.1 mA		60	180	-	
		$I_{\rm C} = 1 \rm{mA}$		80	180	-	
		I _C = 10 mA		100	180	300	
		I _C = 50 mA		60	105	-	
		I _C = 100 mA	[1]	30	50	-	
OLGUI	collector-emitter	$I_C = 10 \text{ mA}; I_B = 1 \text{ mA}$		-	75	200	mV
	saturation voltage	$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}$		-	120	300	mV
V _{BEsat}	base-emitter saturation voltage	$I_C = 10 \text{ mA}; I_B = 1 \text{ mA}$		650	750	850	mV
		$I_C = 50 \text{ mA}; I_B = 5 \text{ mA}$		-	850	950	mV
t _d	delay time	$V_{CC} = 3 \text{ V}; \text{ I}_{C} = 10 \text{ mA};$ $I_{Bon} = 1 \text{ mA};$ $I_{Boff} = -1 \text{ mA}$		-	-	35	ns
t _r	rise time			-	-	35	ns
t _{on}	turn-on time			-	-	70	ns
t _s	storage time			-	-	200	ns
t _f	fall time			-	-	50	ns
t _{off}	turn-off time			-	-	250	ns
C _c	collector capacitance	$V_{CB} = 5 \text{ V}; I_E = i_e = 0 \text{ A};$ f = 1 MHz		-	-	4	pF
C _e	emitter capacitance	$V_{EB} = 500 \text{ mV};$ $I_C = i_c = 0 \text{ A}; \text{ f} = 1 \text{ MHz}$		-	-	8	pF
f _T	transition frequency	$\label{eq:Vce} \begin{array}{l} V_{CE} = 20 \text{ V}; \text{ I}_{C} = 10 \text{ mA}; \\ \text{f} = 100 \text{ MHz} \end{array}$		300	-	-	MHz
NF	noise figure	$V_{CE} = 5 \text{ V}; \text{ I}_{C} = 100 \mu\text{A};$ $R_{S} = 1 k\Omega;$ f = 10 Hz to 15.7 kHz		-	-	5	dB

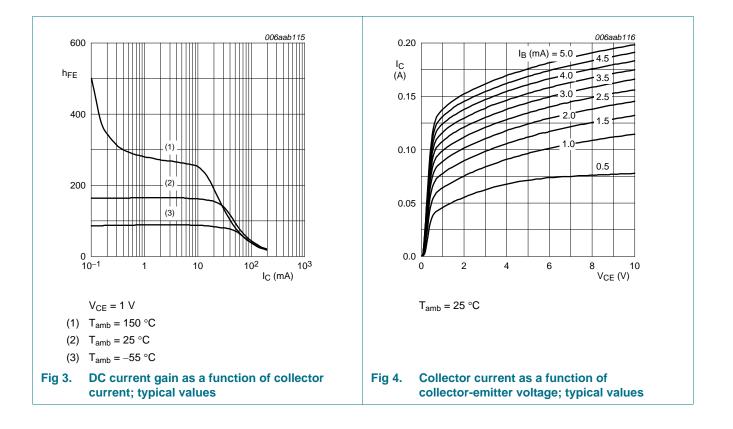
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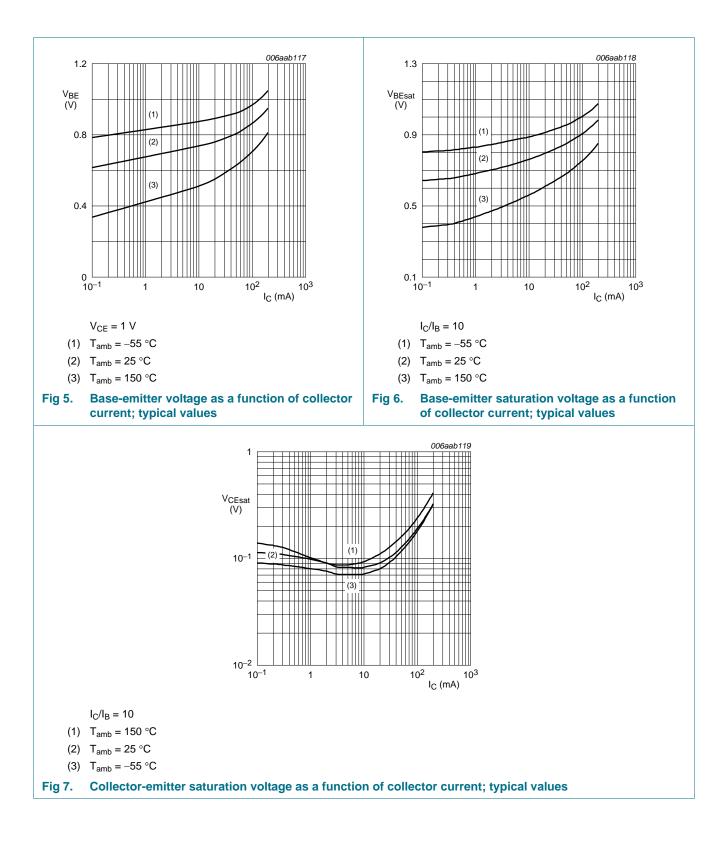
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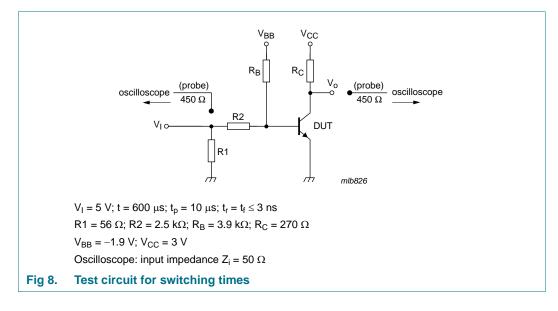
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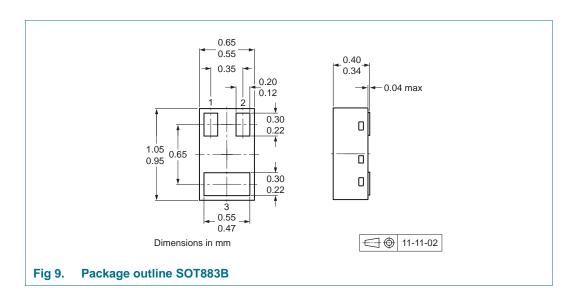
8. Test information



8.1 Quality information

This product has been qualified in accordance with the Automotive Electronics Council (AEC) standard *Q101* - *Stress test qualification for discrete semiconductors*, and is suitable for use in automotive applications.

9. Package outline



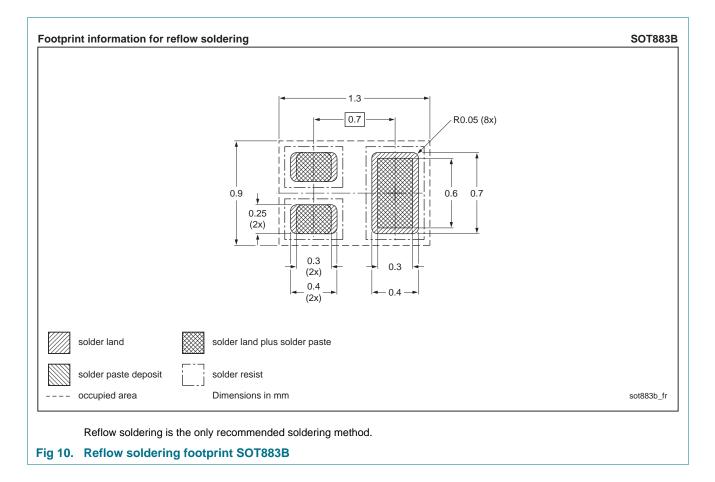
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10. Packing information

I he indicated ->	xx are the la	ast three digits of the 12NC ordering code.[1]	
Type number	Package	Description	Packing quantity
			10000
PMBT3904MB	SOT883B	2 mm pitch, 8 mm tape and reel	-315

11. Soldering



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12. Revision history

Table 9. Revision hist	Revision history					
Document ID	Release date	Data sheet status	Change notice	Supersedes		
PMBT3904MB v.1	20120307	Product data sheet	-	-		

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13. Legal information

13.1 Data sheet status

Document status[1][2]	Product status ^[3]	Definition
Objective [short] data sheet	Development	This document contains data from the objective specification for product development.
Preliminary [short] data sheet	Qualification	This document contains data from the preliminary specification.
Product [short] data sheet	Production	This document contains the product specification.

[1] Please consult the most recently issued document before initiating or completing a design.

[2] The term 'short data sheet' is explained in section "Definitions".

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