



MOS FIELD EFFECT TRANSISTOR 2SK3377

SWITCHING N-CHANNEL POWER MOS FET INDUSTRIAL USE

DESCRIPTION

The 2SK3377 is N-Channel MOS Field Effect Transistor designed for high current switching applications.

FEATURES

- Low On-state Resistance
- ★ RDS(on)1 = 44 mΩ MAX. (VGS = 10 V, ID = 10 A)
- ★ RDS(on)2 = 78 mΩ MAX. (VGS = 4.0 V, ID = 10 A)
- ★ Low Ciss : Ciss = 760 pF TYP.
 - Built-in Gate Protection Diode
 - TO-251/TO-252 package

ABSOLUTE MAXIMUM RATINGS (TA = 25 °C)

	Drain to Source Voltage	VDSS	60	V
	Gate to Source Voltage	Vgss	±20	V
	Drain Current (DC)	D(DC)	±20	А
*	Drain Current (Pulse) Note1	D(pulse)	±50	А
	Total Power Dissipation (Tc = 25°C)	Рт	30	W
	Total Power Dissipation (TA = 25° C)	Рт	1.0	W
	Channel Temperature	Tch	150	°C
	Storage Temperature	Tstg	–55 to +150	°C
\star	Single Avalanche Current Note2	las	15	А
\star	Single Avalanche Energy Note2	Eas	23	mJ

ORDERING INFORMATION

PART NUMBER	PACKAGE	
2SK3377	TO-251	
2SK3377-Z	TO-252	



(TO-251)

(TO-252)



Notes 1. PW \leq 10 μ s, Duty cycle \leq 1 %

2. Starting T_{ch} = 25 °C, R_G = 25 Ω , V_{GS} = 20 V \rightarrow 0 V

THERMAL RESISTANCE

Channel to Case	Rth(ch-C)	4.17	°C/W
Channel to Ambient	Rth(ch-A)	125	°C/W

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90 %

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toff

10 %

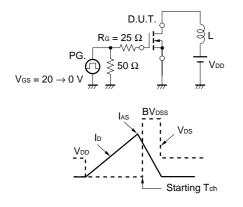
* ELECTRICAL CHARACTERISTICS (T_A = 25 °C)

NEC

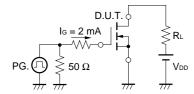
CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain to Source On-state Resistance	RDS(on)1	Vgs = 10 V, Id = 10 A		35	44	mΩ
	RDS(on)2	Vgs = 4.0 V, Id = 10 A		54	78	mΩ
Gate to Source Cut-off Voltage	V _{GS(off)}	Vds = 10 V, Id = 1 mA	1.5	2.0	2.5	V
Forward Transfer Admittance	yfs	Vds = 10 V, Id = 10 A	5	10		S
Drain Leakage Current	IDSS	Vds = 60 V, Vgs = 0 V			10	μA
Gate to Source Leakage Current	lgss	$V_{GS} = \pm 20 \text{ V}, \text{ V}_{DS} = 0 \text{ V}$			±10	μA
Input Capacitance	Ciss	V _{DS} = 10 V		760		pF
Output Capacitance	Coss	Vgs = 0 V		150		pF
Reverse Transfer Capacitance	Crss	f = 1 MHz		71		pF
Turn-on Delay Time	td(on)	ID = 10 A		13		ns
Rise Time	tr	$V_{GS(on)} = 10 V$		170		ns
Turn-off Delay Time	td(off)	Vdd = 30 V		43		ns
Fall Time	tr	R _G = 10 Ω		34		ns
Total Gate Charge	Q _G	ID = 20 A		17		nC
Gate to Source Charge	QGS	Vdd = 48 V		3.0		nC
Gate to Drain Charge	Qgd	$V_{GS(on)} = 10 V$		4.7		nC
Body Diode Forward Voltage	VF(S-D)	IF = 20 A, VGS = 0 V		1.0		V
Reverse Recovery Time	trr	IF = 20 A, VGS = 0 V		39		ns
Reverse Recovery Charge	Qrr	di/dt = 100 A/ <i>µ</i> s		62		nC

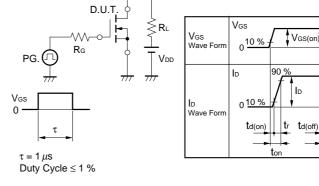
TEST CIRCUIT 1 AVALANCHE CAPABILITY

TEST CIRCUIT 2 SWITCHING TIME



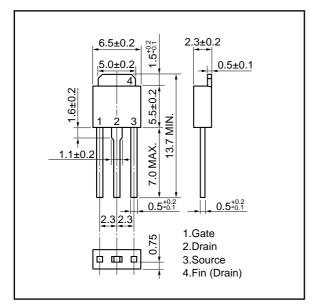
TEST CIRCUIT 3 GATE CHARGE



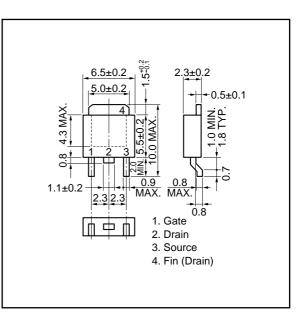


PACKAGE DRAWINGS (Unit : mm)

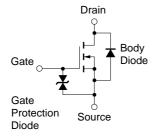
1) TO-251 (MP-3)



2) TO-252 (MP-3Z)



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

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