

MOS FIELD EFFECT TRANSISTOR

2SK3424

SWITCHING

N-CHANNEL POWER MOS FET

INDUSTRIAL USE

DESCRIPTION

The 2SK3424 is N-Channel MOS FET device that features a low on-state resistance and excellent switching characteristics, designed for low voltage high current applications such as DC/DC converter with synchronous rectifier.

FEATURES

- 4.5 V drive available
- Low on-state resistance
 $R_{DS(on)1} = 11.5 \text{ m}\Omega \text{ MAX. (} V_{GS} = 10 \text{ V, } I_D = 24 \text{ A)}$
- Low gate charge
 $Q_G = 34 \text{ nC TYP. (} I_D = 48 \text{ A, } V_{DD} = 24 \text{ V, } V_{GS} = 10 \text{ V)}$
- Built-in gate protection diode
- Surface mount device available

ORDERING INFORMATION

PART NUMBER	PACKAGE
2SK3424	TO-220AB
2SK3424-ZK	TO-263(MP-25ZK)
2SK3424-ZJ	TO-263(MP-25ZJ)

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$)

Drain to Source Voltage ($V_{GS} = 0 \text{ V}$)	V_{bss}	30	V
Gate to Source Voltage ($V_{DS} = 0 \text{ V}$)	V_{GSS}	± 20	V
Drain Current (DC) ($T_C = 25^\circ\text{C}$)	$I_{D(DC)}$	± 48	A
Drain Current (Pulse) ^{Note}	$I_{D(pulse)}$	± 192	A
Total Power Dissipation ($T_A = 25^\circ\text{C}$)	P_{T1}	1.5	W
Total Power Dissipation ($T_C = 25^\circ\text{C}$)	P_{T2}	50	W
Channel Temperature	T_{ch}	150	$^\circ\text{C}$
Storage Temperature	T_{stg}	-55 to +150	$^\circ\text{C}$

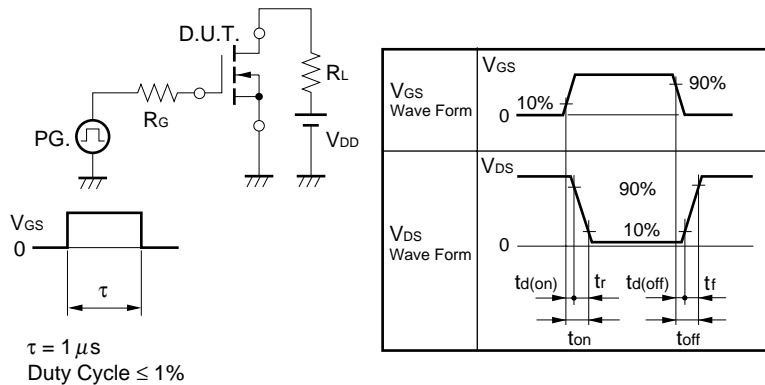
Note $PW \leq 10 \mu\text{s}$, Duty Cycle $\leq 1\%$

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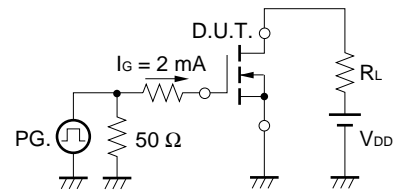
ELECTRICAL CHARACTERISTICS(T_A = 25°C)

CHARACTERISTICS	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Drain Leakage Current	I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V			10	μA
Gate Leakage Current	I _{GSS}	V _{GS} = ±20 V, V _{DS} = 0 V			±10	μA
Gate Cut-off Voltage	V _{GS(off)}	V _{DS} = 10 V, I _D = 1 mA	1.5		2.5	V
Forward Transfer Admittance	y _{fs}	V _{DS} = 10 V, I _D = 24 A	13			S
Drain to Source On-state Resistance	R _{DS(on)1}	V _{GS} = 10 V, I _D = 24 A		7.7	11.5	mΩ
	R _{DS(on)2}	V _{GS} = 4.5 V, I _D = 24 A		10.5	17.0	mΩ
Input Capacitance	C _{iss}	V _{DS} = 10 V		1900		pF
Output Capacitance	C _{oss}	V _{GS} = 0 V		580		pF
Reverse Transfer Capacitance	C _{rss}	f = 1 MHz		270		pF
Turn-on Delay Time	t _{d(on)}	V _{DD} = 15 V, I _D = 24 A		14		ns
Rise Time	t _r	V _{GS(on)} = 10 V		13		ns
Turn-off Delay Time	t _{d(off)}	R _G = 10 Ω		61		ns
Fall Time	t _f			22		ns
Total Gate Charge	Q _G	V _{DD} = 24 V		34		nC
Gate to Source Charge	Q _{GS}	V _{GS} = 10 V		6.4		nC
Gate to Drain Charge	Q _{GD}	I _D = 48 A		9.1		nC
Diode Forward Voltage	V _{F(S-D)}	I _F = 48 A, V _{GS} = 0 V		1.0		V
Reverse Recovery Time	t _{rr}	I _F = 48 A, V _{GS} = 0 V		34		ns
Reverse Recovery Charge	Q _{rr}	di/dt = 100 A/μs		26		nC

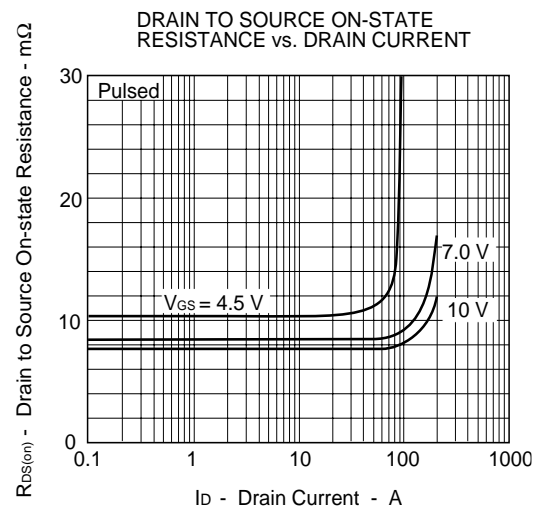
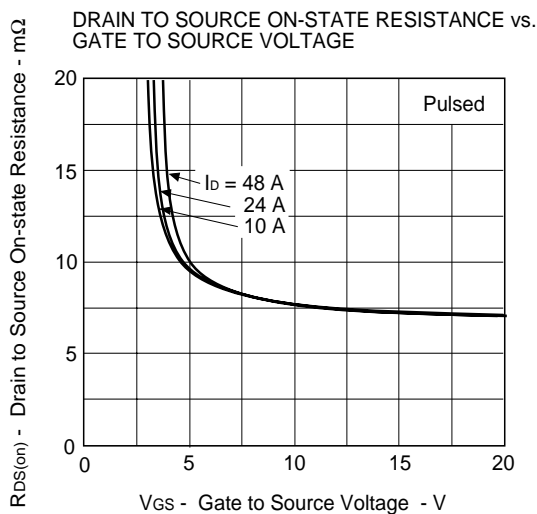
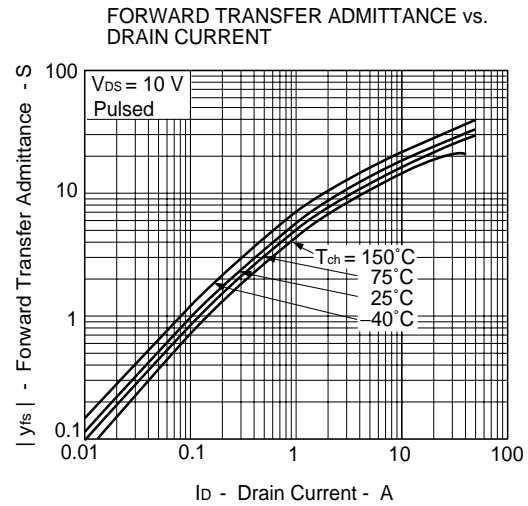
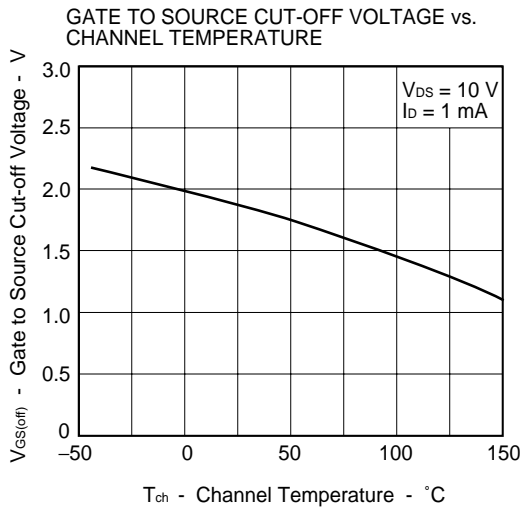
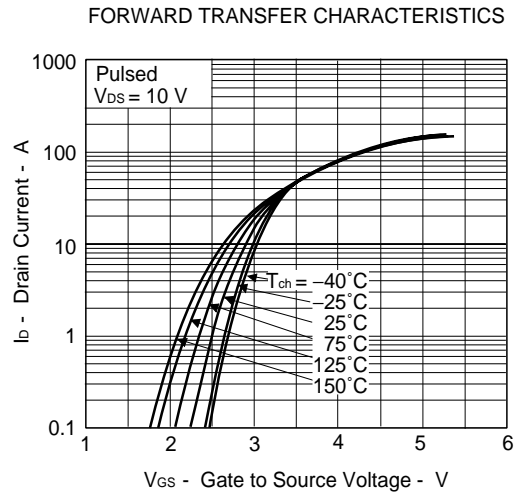
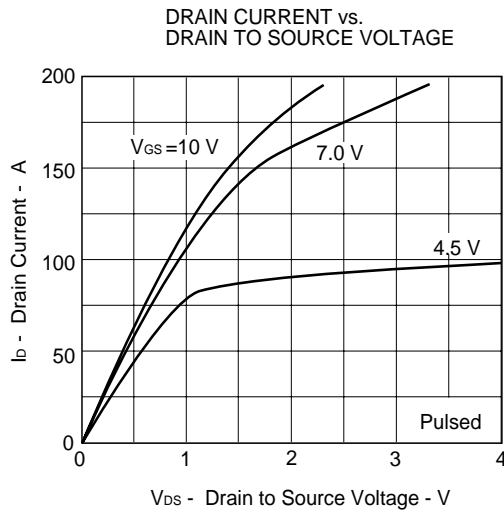
TEST CIRCUIT 1 SWITCHING TIME



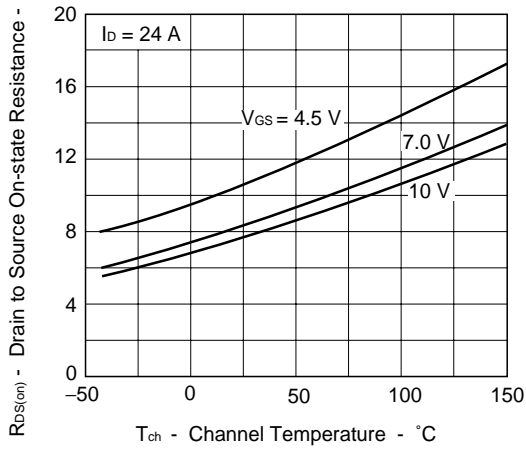
TEST CIRCUIT 2 GATE CHARGE



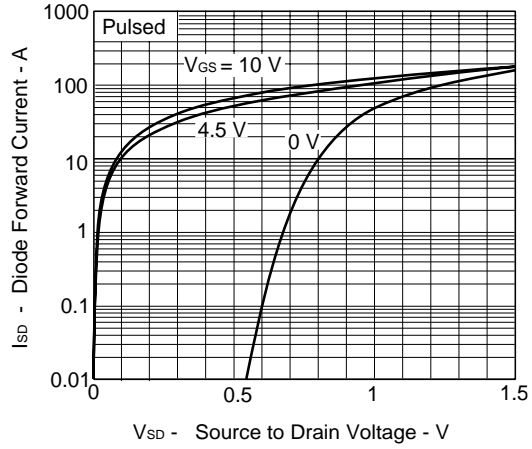
TYPICAL CHARACTERISTICS (T_A = 25°C)



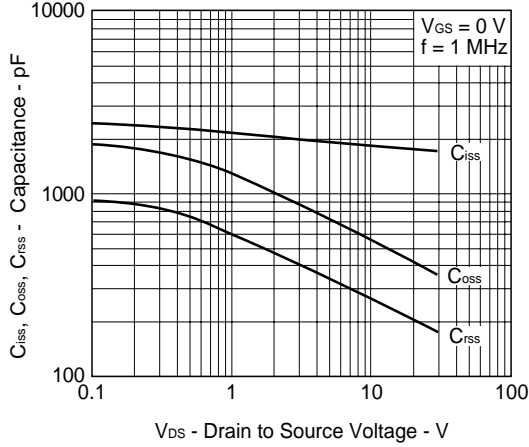
DRAIN TO SOURCE ON-STATE RESISTANCE vs. CHANNEL TEMPERATURE



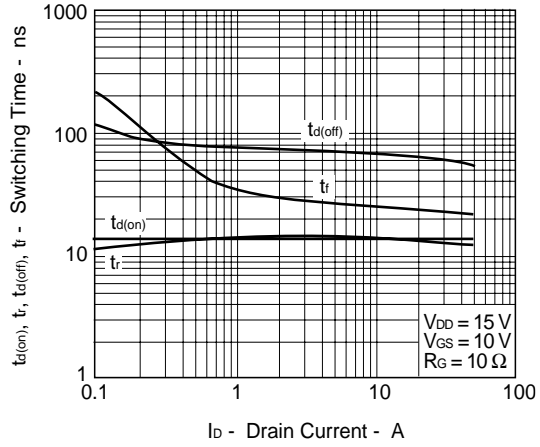
SOURCE TO DRAIN DIODE FORWARD VOLTAGE



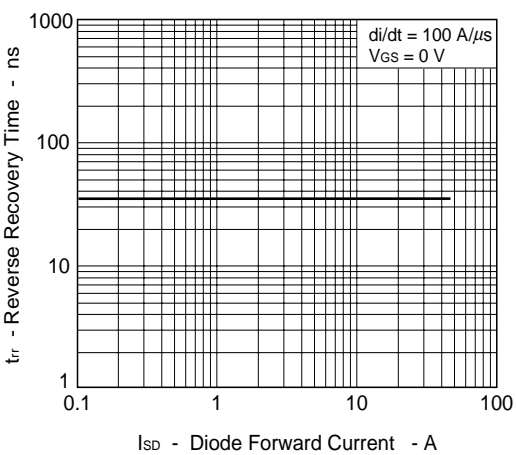
CAPACITANCE vs. DRAIN TO SOURCE VOLTAGE



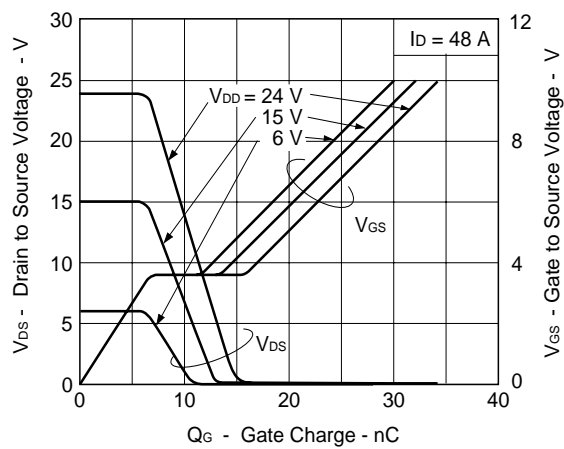
SWITCHING CHARACTERISTICS



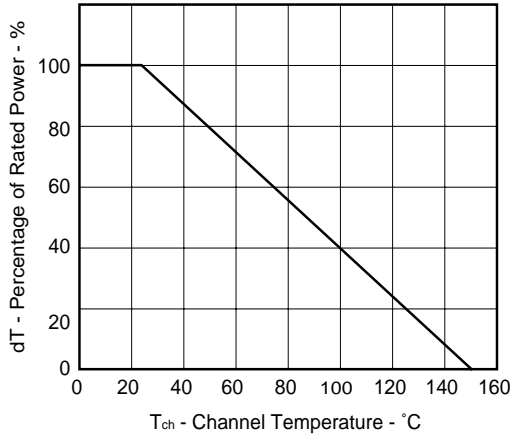
REVERSE RECOVERY TIME vs. DIODE FORWARD CURRENT



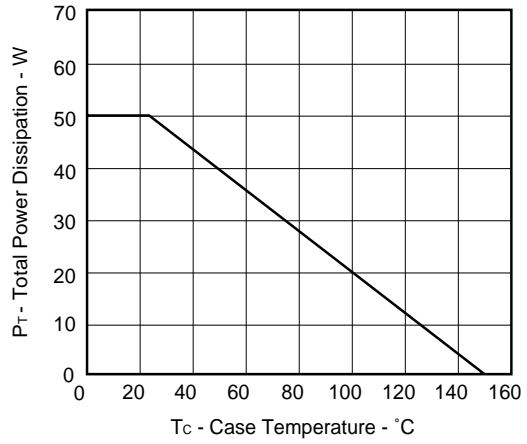
DYNAMIC INPUT/OUTPUT CHARACTERISTICS



DERATING FACTOR OF FORWARD BIAS
SAFE OPERATING AREA

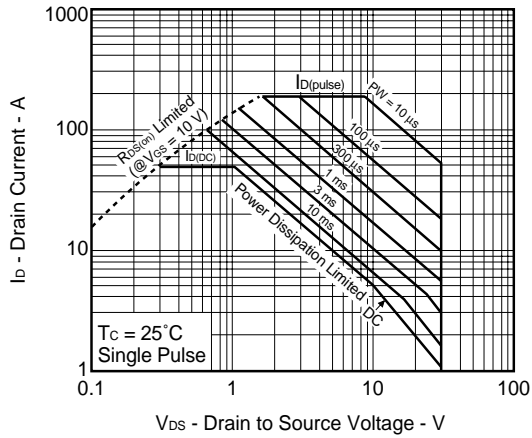


TOTAL POWER DISSIPATION vs.
CASE TEMPERATURE

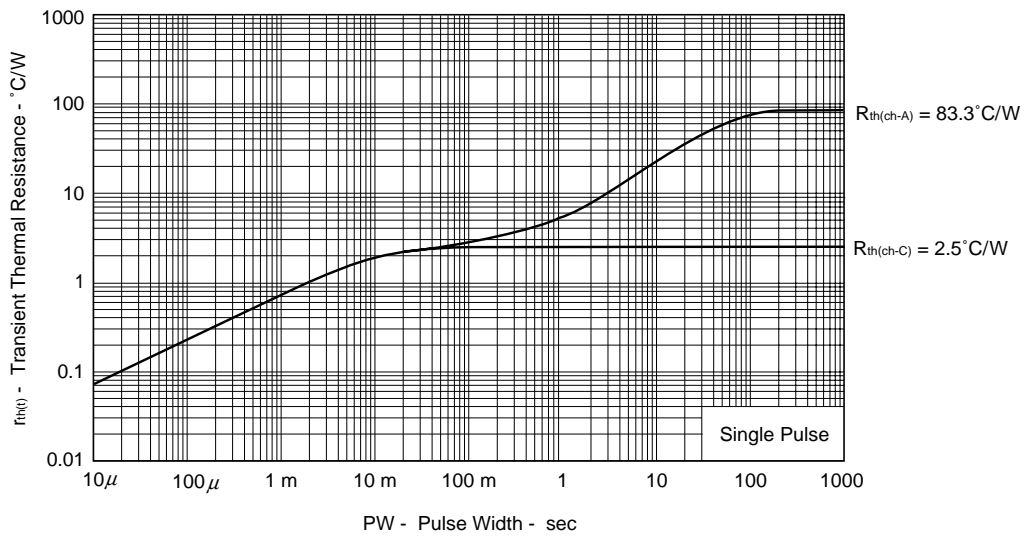


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FORWARD BIAS SAFE OPERATING AREA

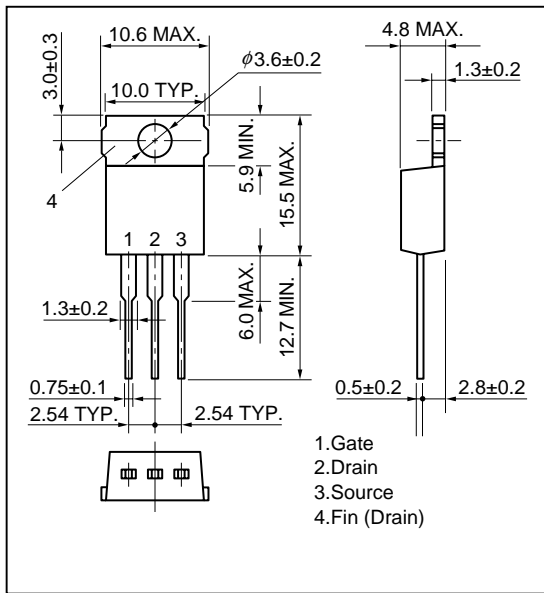


TRANSIENT THERMAL RESISTANCE vs. PULSE WIDTH

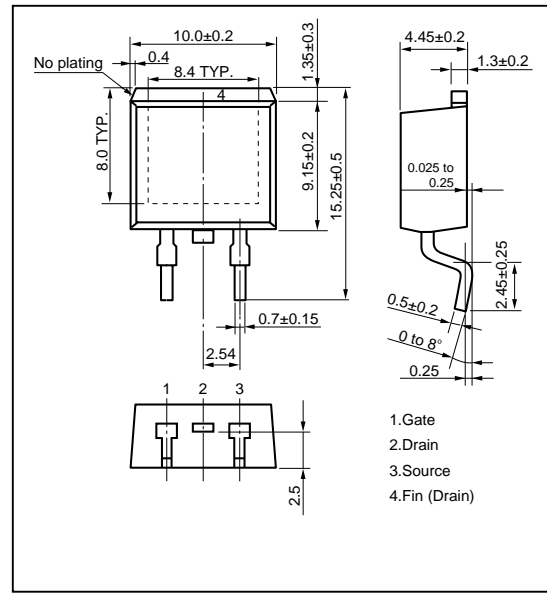


PACKAGE DRAWINGS (Unit : mm)

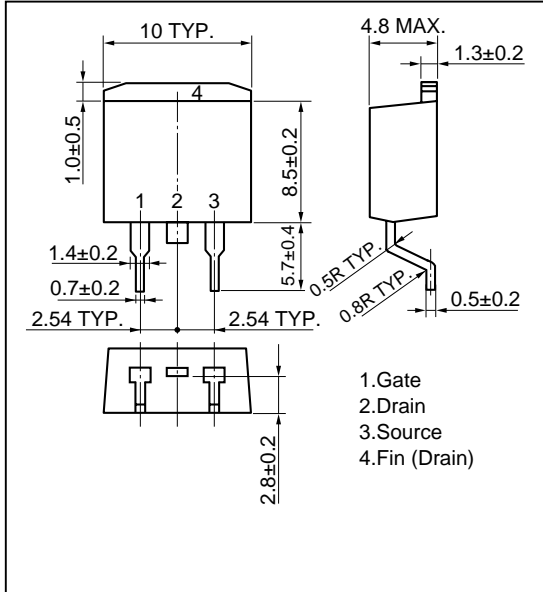
★ 1)TO-220AB (MP-25)



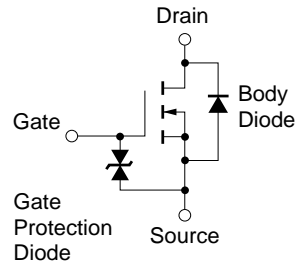
2)TO-263 (MP-25ZK)



★ 3)TO-263 (MP-25ZJ)



EQUIVALENT CIRCUIT



Remark The diode connected between the gate and source of the transistor serves as a protector against ESD. When this device actually used, an additional protection circuit is externally required if a voltage exceeding the rated voltage may be applied to this device.

[MEMO]

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