

• 4-Bit Single-Chip Microcomputers

Type No.	Process	Supply Voltage (V)	Instruction Time ( $\mu$ s)	ROM x8	RAM	I/O	Features	Package
$\mu$ COM43C Series	CMOS	+5	10	2k	96x4	4 bit x 8		42-pin DIP
				1k	64x4	3 bit		
				1k	32x4	4 bit x 5 1 bit		28-pin DIP
$\mu$ COM7500 Series	CMOS	2.7~5.5	20~10	2k	128x4	4 bit x 5	LCD Driver Standby Mode	64-pin Flat
				4k	224x4	3 bit		
				1k	64x4	4 bit x 5 2 bit	Standby	28-pin DIP
				5	2k	128x4		
			4k		224x4			40-pin DIP
								FIP Controller Driver High-Voltage Output

\* Under development

• 8-Bit Single-Chip Microcomputers

Type No.	Process	Supply Voltage (V)	Instruction Time ( $\mu$ s)	ROM x8	RAM	I/O	Features	Package
$\mu$ COM84 Series	NMOS	+5	8.38	1k	64x8	8 bit x 2 4 bit	Zero Cross High Output Current	28-pin DIP
				2k		8 bit x 3	8 bit A/D Converter Memory & I/O Expandable	40-pin DIP
				1k				
	CMOS		1.36	2k	128x8	Standby Mode		
			2.5	1k	64x8			
			2.5	2k	128x8			
$\mu$ COM87 Series	NMOS	+5	2	4k	128x8	8 bit x 6	Expandable Memory 64 kB	64-pin (QUIP)
			3	6k	64x8			
			1	4k	256x8	44	8 bit A/D Converter 8 bit x 8 bit 16 bit $\div$ 8 bit	

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• Evaluation Chips for Single-Chip Microcomputers

Type No.	Process	Functions	Package
$\mu$ PD7500G	CMOS	Evaluation Chip for $\mu$ COM75 Series	64-pin (QUIP)
$\mu$ PD8035HLC	NMOS	ROM Less Version of $\mu$ PD8048HC	40-pin DIP
$\mu$ PD8039HLC		ROM Less Version of $\mu$ PD8049C	
$\mu$ PD80C35C	CMOS	ROM Less Version of $\mu$ PD80C48HC	
$\mu$ PD80C39C		ROM Less Version of $\mu$ PD80C49C	
$\mu$ PD7800G	NMOS	Evaluation Chip for $\mu$ PD7801G/7802G	64-pin (QUIP)
* $\mu$ PD7810G		Evaluation Chip for $\mu$ PD7811G	
$\mu$ PD8748D	NMOS	EPROM Version of $\mu$ PD8048HC	40-pin DIP
$\mu$ PD8749HD	NMOS	EPROM Version of $\mu$ PD8049HC	40-pin DIP
* $\mu$ PD75CG08E	CMOS	PIGGYBACK Version of $\mu$ PD7507/08C	40-pin DIP
* $\mu$ PD87PG11E	NMOS	PIGGYBACK Version of $\mu$ PD7811G	64-pin (QUIP)

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• Peripheral ICs

Type No.	Process	Functions	Package
$\mu$ PD8253C-5	NMOS	Programable Interval Timer (Triple 16-bit Timers)	24-pin DIP
$\mu$ PD8355C/D	NMOS	2048 x 8 bit Mask ROM, 16-bit I/O	40-pin DIP