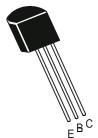




An ISO/TS16949 and ISO 9001 Certified Company

NPN EPITAXIAL PLANAR SILICON HIGH VOLTAGE TRANSISTOR

2N5550



TO-92 Plastic Package

High Voltage NPN Transistor For General Purpose and Telephony Applications.

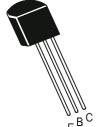
ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	VALUE	UNITS	
Collector Emitter Voltage	V_{CEO}	140	V	
Collector Base Voltage	V_{CBO}	160	V	
Emitter Base Voltage	V_{EBO}	6	V	
Collector Current Continuous	I _C	600	mA	
Power Dissipation@ Ta=25ºC	P_{D}	625	mW	
Derate Above 25ºC		5.0	mW/ºC	
Power Dissipation@ Tc=25ºC	P_{D}	1.5	W	
Derate Above 25ºC		12	mW/ºC	
Operating And Storage Junction	T_{j},T_{stg}	-55 to +150	°C	
Temperature Range				
THERMAL RESISTANCE				
Junction to ambient	$R_{th(j-a)(1)}$	357	°C/W	
Junction to case	$R_{th(j-c)}$	125	°C/W	
(1) Rth (j-a) is measured with the d		pical printed circuit board		

Continental Device India Limited

NPN EPITAXIAL PLANAR SILICON HIGH VOLTAGE TRANSISTOR

2N5550



TO-92 Plastic Package

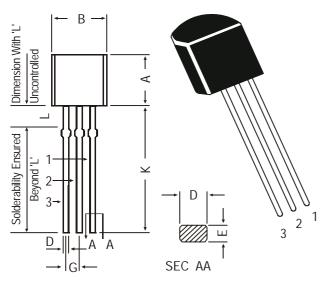
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Base Voltage	V _{CEO} *	$I_C=1mA,I_B=0$	140			V
Collector Base Voltage	V_{CBO}	I_{C} =100 μ A, I_{E} =0	160			V
Emitter Base Voltage	V_{EBO}	$I_E = 10 \mu A, I_C = 0$	6			V
Collector Cut off Current	I_{CBO}	$V_{CB} = 100V, I_{E} = 0$			100	nA
		Ta=100 ^O C				
		$V_{CB} = 100V, I_{E} = 0$			100	μΑ
Emitter Cut off Current	I _{EBO}	V_{BE} =4 V , I_C = 0			50	nA
						μΑ
DC Current Gain	h _{FE} *	$V_{CE}=5V,I_{C}=1mA$	60			
		V_{CE} =5 V , I_{C} =10 mA	60		250	
		V_{CE} =5 V , I_{C} =50 mA	20			
Base Emitter Saturation Voltage	$V_{BE(sat)}^*$	$I_C=10mA, I_B=1mA$			1.0	V
		$I_C=50$ mA, $I_B=5$ mA			1.2	V
Collector Emitter Saturation Voltage	$V_{CE(sat)}^{*}$	$I_C=10mA, I_B=1mA$			0.15	V
		$I_C=50$ mA, $I_B=5$ mA			0.25	V

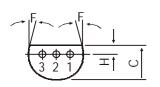
DYNAMIC CHARACTERISTICS

Small Signal Current Gain	I hfe I	I _C =1mA, V _{CE} =10V f=1KHz	50	200	
Transition Frequency	f_T	I _C =10mA, V _{CE} =10V			
		f=100MHz	100	300	MHz
Output Capacitance	C_obo	I _E =0, V _{CB} =10V			
		f=1MHz		6.0	$_{P}F$
Input Capacitance	C_{ibo}	$Ic=0, V_{EB}=0.5V$			
		f=1MHz		30	$_{P}F$
Noise Figure	NF	$V_{CE} = 5V, I_{C} = 250 \mu A$		10	dB
_		$R=1\Omega, f=10H_z$ to 15.7kHz			
*Pulse Condition: Width = 300µs	s, Duty Cycle=	= 2%.			

TO-92 Plastic Package

TO-92 Plastic Package





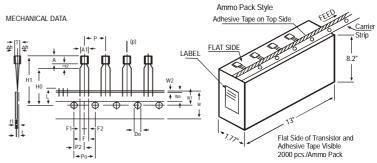
PIN CONFIGURATION

- 1. COLLECTOR
- 2. BASE
- **EMITTER**

DIM	MIN.	MAX.				
Α	4.32	5.33				
В	4.45	5.20				
С	3.18	4.19				
D	0.41	0.55				
Е	0.35	0.50				
F	5 DEG					
G	1.14	1.40				
Н	1.14	1.53				
K	12.70	_				
L	1.982	2.082				
All Parts to the same						

All diminsions in mm.

TO-92 Transistors on Tape and Ammo Pack



All dimensions in mm unless specified otherwise

ITEM		SPECIFICATION			DEMARKS		
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL .	REMARKS	
BODY WIDTH	A1	4.0		4.8			
BODY HEIGHT	Α	4.8		5.2			
BODY THICKNESS	T	3.9		4.2			
PITCH OF COMPONENT	Р		12.7		±1		
FEED HOLE PITCH	Po		12.7		±0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH	
FEED HOLE CENTRE TO COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED AT BOTTOM OF CLINCH	
DISTANCE BETWEEN OUTER LEADS	F		5.08		+0.6 -0.2		
COMPONENT ALIGNMENT	Δh		0	1		AT TOP OF BODY	
TAPE WIDTH	W		18		±0.5		
HOLD-DOWN TAPE WIDTH	Wo		6		±0.2		
HOLE POSITION	W1		9		+0.7 -0.5		
HOLD-DOWN TAPE POSITION	W2		0.5		±0.2		
LEAD WIRE CLINCH HEIGHT	Но		16		±0.5		
COMPONENT HEIGHT	H1			23.25			
LENGTH OF SNIPPED LEADS	L			11.0			
FEED HOLE DIAMETER	Do		4		±0.2		
TOTAL TAPE THICKNESS	t		0.57	1.2		t1 0.3 - 0.6	
LEAD - TO - LEAD DISTANCEF1,	F2		2.54		+0.4 -0.1		
CLINCH HEIGHT	H2			3			
PULL - OUT FORCE	(P)	6N					

- NOTES

 1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.

 2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20 PITCHES.

 2. MAXIMUM TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THE EDGE(S) OF CARRIER TAPE AND
- HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO
- HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.
 NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.
 A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT.
 SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX				
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt		
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs		
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs		

Notes 2N5550

TO-92 Plastic Package

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of

Continental Device India Limited

C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119

email@cdil.com www.cdilsemi.com

2N5550Rev190701