

Continental Device India Limited

An IS/ISO 9002 and IECQ Certified Manufacturer



NPN SILICON PLANAR EPITAXIAL AMPLIFIER TRANSISTORS

NPN SILICON PLANAR EPITAXIAL AMIPLIFIER TRANSISTORS

BC184L, BC184LB BC184LC

TO-92 Plastic Package

General Purpose Amplifier Transistors

ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	VALUE	UNITS
Collector -Emitter Voltage	V_{CEO}	30	V
Collector -Base Voltage	V_{CBO}	45	V
Emitter -Base Voltage	V_{EBO}	6.0	V
Collector Current Continuous	I_{C}	100	mA
Power Dissipation @ Ta=25ºC	P_{D}	350	mW
Derate Above 25ºC		2.8	mW/ºC
Power Dissipation @ Tc=25ºC	P_{D}	1.0	W
Derate Above 25ºC		8.0	mW/°C
Operating And Storage Junction	T_{j},T_{stg}	-55 to +150	٥C
Temperature Range			
THERMAL RESISTANCE			
Junction to Case	$R_{th(j-c)}$	125	°C/W
Junction to Ambient R _{th(j-a)}		357	°C/W

ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Specified Otherwise)

DESCRIPTION	·	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector -Emitter Voltage		V_{CEO}	$I_C=2mA,I_B=0$	30			V
Collector -Base Voltage		V_{CBO}	$I_C = 10 \mu A.I_E = 0$	45			V
Emitter-Base Voltage		V_{EBO}	$I_E=100\mu A,\ I_C=0$	6			V
Collector-Cut off Current		I_{CBO}	$V_{CB}=30V,I_{E}=0$		0.2	15	nA
Emitter-Cut off Current		I_{EBO}	$V_{EB}=4V$, $I_{C}=0$			15	nA
DC Current Gain		h_{FE}	$I_C=10\mu A, V_{CE}=5V$	100			
	BC184L		$I_C=2mA, V_{CE}=5V$	240		800	
			$I_C=100mA, V_{CE}=5V$	130			

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ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Specified Otherwise)

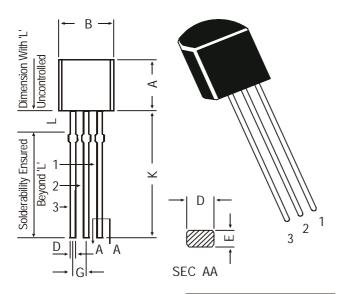
DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Emitter Saturation Voltage	$V_{CE(Sat)}$	$I_C=10$ mA, $I_B=0.5$ mA		0.07	0.25	V
		I _C =100mA,I _B =5.0mA*		0.2	0.6	V
Base Emitter Saturation Voltage	$V_{\text{BE}(\text{Sat})}$	$I_C=100$ mA, $I_B=5$ mA*			1.2	V
Base Emitter On Voltage	$V_{BE(On)}$	$I_C=2.0$ mA, $V_{CE}=5$ V	0.55	0.62	0.7	V
		$I_C=100\mu A, V_{CE}=5V$		0.5		V
		$I_C=100$ mA, $V_{CE}=5$ V*		0.83		V
Transistors Frequency	f _⊤	I _C =0.5mA, V _{CE} =3V f=100MHz		140		MHz
		I _C =10mA, V _{CE} =5V f=100MHz	150	280		MHz
Common Base OutPut Capacitance	C_ob	$V_{CB}=10V, I_{C}=0$			5.0	pF
Common Baco Gati at Capacitance	3 00	f=1MHz			0.0	P.
Input Capacitance	C_{ib}	V_{BE} =0.5V, I_{C} =0 f=1MHz		8.0		pF
		I= I IVI IT Z				
Small Signal Current Gain BC184L	. h _{fe}	$I_C=2mA$, $V_{CE}=5V$ f = 1kHz	240		900	
BC184L	В	1 – 11(12	240		500	
BC184L			450		900	
Noise Figure		I _C =0.2mA, V _{CE} =5.0V				
		Rs=2kW, f=30Hz to 15kHz			4	dB
		I _C =0.2mA, V _{CE} =5.0V			4	dB
		Rs=2kW, f=1kHz to F=200Hz				

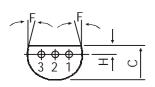
*Pulse Condition: =300us, Duty Cycle=2%

TO-92 Plastic Package

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TO-92 Transistors on Tape and Ammo Pack





DIM	MIN.	MAX.				
Α	4.32	5.33				
В	4.45	5.20				
С	3.18	4.19				
D	0.41	0.55				
Е	0.35	0.50				
F	5 DEG					
G	1.14	1.40				
Н	1.14	1.53				
K	12.70	_				
l i	1.982	2.082				

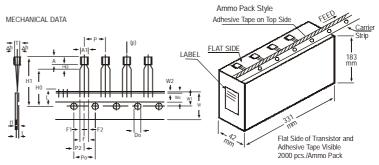
COLLECTOR 2.

PIN CONFIGURATION

EMITTER

1. BASE

All diminsions in mm.



All dimensions in mm unless specified otherwise

ITEM.		SPECIFICATION					
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL.	REMARKS	
BODY WIDTH	A1	4.0		4.8			
BODY HEIGHT	Α	4.8		5.2			
BODY THICKNESS	T	3.9		4.2			
PITCH OF COMPONENT	Р		12.7		±1		
FEED HOLE PITCH	Po		12.7		±0.3	CUMULATIVE PITCH ERROR 1.0 mm/20	
FEED HOLE CENTRE TO						PITCH	
COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED AT BOTTOM OF CLINCH	
DISTANCE BETWEEN OUTER					+0.6		
LEADS	F		5.08		-0.2		
COMPONENT ALIGNMENT	△h		0	1		AT TOP OF BODY	
TAPE WIDTH	W		18		±0.5		
HOLD-DOWN TAPE WIDTH	Wo		6 9		±0.2		
HOLE POSITION	W1		9		+0.7 -0.5		
HOLD-DOWN TAPE POSITION	W2		0.5		±0.2		
LEAD WIRE CLINCH HEIGHT	Ho		16		±0.5		
COMPONENT HEIGHT	H1			23.25			
LENGTH OF SNIPPED LEADS	L			11.0			
FEED HOLE DIAMETER	Do		4		±0.2		
TOTAL TAPE THICKNESS	t			1.2		t1 0.3 - 0.6	
LEAD - TO - LEAD DISTANCEF1,	F2		2.54		+0.4 -0.1		
CLINCH HEIGHT	H2			3			
PULL - OUT FORCE	(P)	6N					

- NOTES

 1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.

 2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20 PITCHES.
- HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.

 NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.

 A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"		17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

Notes

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Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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