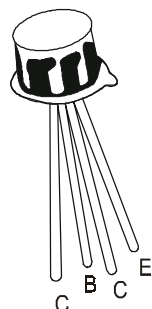


NPN SILICON PLANAR EPITAXIAL TRANSISTOR
BF200

TO-72
Metal Can Package
BF200 IS A SILICON NPN TRANSISTOR INTENDED FOR USE AT VERY HIGH FREQUENCIES.
ABSOLUTE MAXIMUM RATINGS (Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	VALUE	UNIT
Collector Base Voltage	V_{CBO}	30	V
Collector Emitter Voltage	V_{CEO}	20	V
Emitter Base Voltage	V_{EBO}	3	V
Collector Current Continuous	I_C	20	mA
Power Dissipation @ Ta=25°C	P_D	150	mW
Junction Temperature	T_j	17	°C
Storage Temperature	T_{stg}	-65 to +175	°C

THERMAL RESISTANCE

Junction to Ambient In Free Air	$R_{th(j-a)}$	1	°C/W
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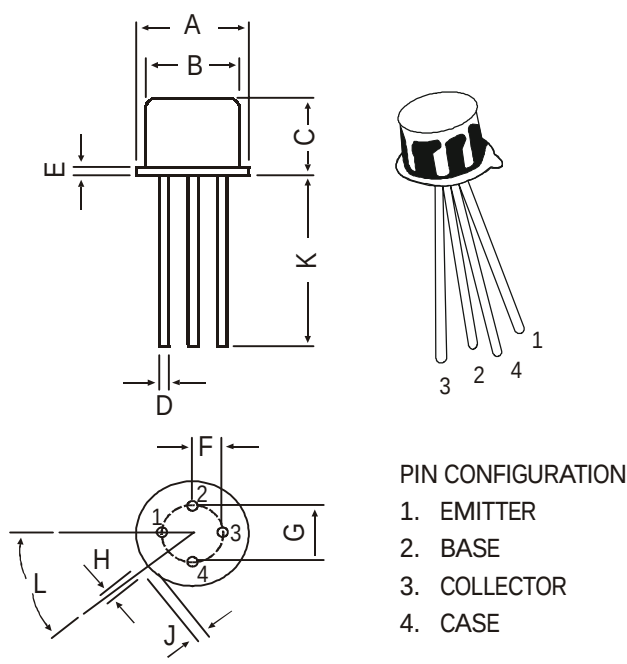
ELECTRICAL CHARACTERISTICS (Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	VALUE			UNIT
			MIN	TYP	MAX	
Collector Emitter Breakdown Voltage	BV_{CEO}	$I_C=1mA, I_B=0$	20			V
Collector Base Breakdown Voltage	BV_{CBO}	$I_C=10\mu A, I_E=0$	30			V
Emitter Base Breakdown Voltage	BV_{EBO}	$I_E=10\mu A, I_C=0$	3			V
Collector Cut off Current	I_{CBO}	$V_{CB}=15V, I_E=0$			500	nA
DC Current Gain	h_{FE}	$I_C=1mA, V_{CE}=10V$	40			

DYNAMIC CHARACTERISTICS

Transition Frequency	f_T	$I_C=3mA, V_{CE}=10V$ $f=100MHz$		700		MHz
Common Emitter Feedback Capacitance	C_{re}	$V_{CB}=10V, f=10MHz$		0.65		pF

TO-72 Metal Can Package



DIM	MIN.	MAX.
A	5.24	5.84
B	4.52	4.95
C	4.31	5.33
D	0.40	0.53
E	—	0.76
F	1.14	1.39
G	2.28	2.97
H	0.91	1.17
J	0.71	1.22
K	12.70	—
L	12 DEG	48 DEG

All dimensions in mm.

Packing Detail

PACKAGE	STANDARD PACK		INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size	Qty	Size	Qty	Gr Wt
TO-72	1 K/Polybag	325 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	32 kgs

Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD is believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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