

An ISO/TS16949 and ISO 9001 Certified Company

#### PNP SILICON PLANAR EPITAXIAL TRANSISTORS

**MPS2907 MPS2907A** 

**TO-92 Plastic Package** 

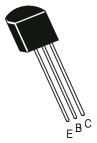
General Purpose Transistors
ABSOLUTE MAXIMUM RATINGS(Ta=25°C unless specified otherwise)

DESCRIPTION	SYMBOL	MPS2907	MPS2907A	UNITS
Collector Emitter Voltage	$V_{CEO}$	40	60	V
Collector Base Voltage	$V_{CBO}$	60	0 75	V
Emitter Base Voltage	$V_{EBO}$	5		V
<b>Collector Current Continuous</b>	$I_{C}$	60	0	mA
Power Dissipation @ Ta=25°C	$P_{D}$	62	25	mW
Derate Above 25°C		5		mW/ºC
Power Dissipation @ Tc=25°C	$P_{D}$	1.	W	
Derate Above 25°C		12	2	mW/ºC
Operating And Storage Junction	$T_{j},T_{stg}$	-55 to	+150	٥C
Temperature Range				
THERMAL RESISTANCE				
Junction to ambient	$R_{th(j-a)}$	20	0	°C/W
Junction to case	$R_{th(j-c)}$	83	.3	°C/W

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**ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Specified Otherwise)** 

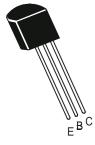
DESCRIPTION		TEST CONDITION	MPS	S2907	MPS	S2907A	UNITS
			MIN	MAX	MIN	MAX	
Collector Emitter Voltage	BV <sub>CEO</sub> *	$I_C=10$ mA, $I_B=0$	40		60		V
Collector Base Voltage	$BV_CBO$	$I_{CBO}$ $I_{C}=10\mu A, I_{E}=0$			60		V
Emitter-Base Voltage	$BV_{EBO}$	$I_{E}=10\mu A, I_{C}=0$	5		5		V
Collector-Cut off Current	$I_{CBO}$						
		$V_{CB} = 50V, I_{E} = 0$		20		10	nA
		$V_{CB} = 50V, I_{E} = 0,$		20		10	μΑ
		$T_A = 150^{\circ}C$					
Collector-Cut off Current	$I_{CEX}$	$V_{CE}$ =30V, $V_{EB}$ (off)=0.5V		50		50	nA
Collector-Cut off Current	$I_{CEO}$	V <sub>CE</sub> =10V		10		10	nA
Emitter Cut off Current	$I_{EBO}$	$V_{EB}=3V$ , $I_C=0$		10		10	nA
Base Cut off Current	$I_{BEX}$	$V_{CE}$ =30V, $V_{EB}$ (off)=0.5V		50		50	nA
Collector-Emitter Saturation Voltage	$V_{CE(sat)}^*$	$I_C=150$ mA, $I_B=15$ mA		0.4		0.4	V
		$I_C$ =500mA, $I_B$ =50mA		1.6		1.6	V
Base-Emitter Saturation Voltage	$V_{BE(sat)}^*$	$I_C=150$ mA, $I_B=15$ mA		1.3		1.3	V
		$I_C$ =500mA, $I_B$ =50mA		2.6		2.6	V
DC Current Gain							μΑ
	$h_{FE}$	$V_{CE}=10V,I_{C}=0.1mA$	35		75		
		$V_{CE}=10V,I_{C}=1mA$	50		100		
		$V_{CE}=10V,I_{C}=10mA$	75		100		
		$V_{CE}=10V^*,I_{C}=150mA$	100	300	100	300	
		$V_{CE}=10V^*,I_{C}=500mA$	30		50		

ELECTRICAL CHARACTERISTICS (Ta=25°C Unless Specified Otherwise)

DYNAMIC CHARACTERISTICS	SYMBOL	SYMBOL TEST CONDITION		MPS2907		MPS2907A	
			MIN	MAX	MIN	MAX	
Transition Frequency	f <sub>T</sub> *(1)	I <sub>C</sub> =50mA, V <sub>CE</sub> =20V					
		f=100MHz	200		200		MHz
Output Capacitance	$C_ob$	I <sub>E</sub> =0, V <sub>CB</sub> =10V					
		f=1MHz		8		8	$_{P}F$
Input Capacitance	Ci <sub>b</sub>	$Ic=0, V_{EB}=2V$					
		f=1MHz		30		30	$_{P}F$

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SWITCHING CHARCTERISTICS	SYMBOL	TEST CONDITION	MPS2907		MPS2907A		UNITS
		$I_C = 150 \text{mA}, I_{B1} = 15 \text{mA},$	MIN	MAX	MIN	MAX	
		$V_{CC} = 30V$					
Delay Time	$t_d$			10		10	ns
Rise Time	t <sub>r</sub>			40		40	ns
Turn On Time	$t_{on}$			45		45	ns
		$I_C = 150 \text{mA}, I_{B1} = I_{B2} 15 \text{m}$	nΑ,				
		$V_{CC} = 6V$					
Storage Time	$t_s$			80		80	ns
Fall Time	$t_f$			30		30	ns
Turn Off Time	$t_{off}$			100		100	ns

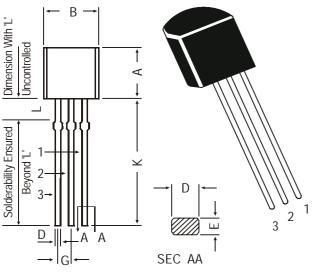
<sup>\*</sup>Pulse Condition: Width ≤ 300us, Duty Cycle ≤ 2%.

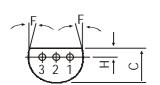
<sup>(1)</sup> f<sub>T</sub> is defined as the frequency at which Ih<sub>fe</sub>l extrapolates to unity.

## **TO-92 Plastic Package**

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#### **TO-92 Transistors on Tape and Ammo Pack**



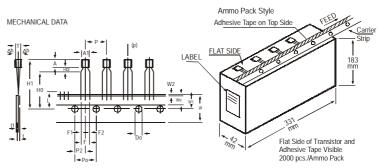


## PIN CONFIGURATION

- COLLECTOR
- BASE 2.
- 3. **EMITTER**

		_				
DIM	MIN.	MAX.				
Α	4.32	5.33				
В	4.45	5.20				
С	3.18	4.19				
D	0.41	0.55				
Е	0.35	0.50				
F	5 DEG					
G	1.14	1.40				
Н	1.14	1.53				
K	12.70	_				
L	1.982	2.082				

All diminsions in mm.



#### All dimensions in mm unless specified otherwise

ITEM			SPECIF	CATIO	N	
ITEM	SYMBOL	MIN.	MIN. NOM. MAX. TOL.		REMARKS	
BODY WIDTH	A1	4.0		4.8		
BODY HEIGHT	A T	4.8		5.2 4.2		
BODY THICKNESS PITCH OF COMPONENT	I P	3.9	12.7	4.2	±1	
FEED HOLE PITCH	Po		12.7		±0.3	CUMULATIVE PITCH
						ERROR 1.0 mm/20
FEED HOLE CENTRE TO	D.0					PITCH
COMPONENT CENTRE	P2		6.35		±0.4	TO BE MEASURED AT BOTTOM OF CLINCH
DISTANCE BETWEEN OUTER					+0.6	
LEADS	F		5.08	4	-0.2	4T TOD OF DODY
COMPONENT ALIGNMENT TAPF WIDTH	∆h W		0 18	1	+0.5	AT TOP OF BODY
HOLD-DOWN TAPE WIDTH	Wo		6		±0.5 ±0.2	
HOLE POSITION	W1		9		+0.7	
					-0.5	
HOLD-DOWN TAPE POSITION	W2		0.5		±0.2	
LEAD WIRE CLINCH HEIGHT	Но		16		±0.5	
COMPONENT HEIGHT	H1			23.25 11.0		
LENGTH OF SNIPPED LEADS FFFD HOLF DIAMFTER	L Do		4	11.0	+0.2	
TOTAL TAPE THICKNESS	t		,	1.2	±0.2	t1 0.3 - 0.6
LEAD - TO - LEAD DISTANCEF1,	F2		2.54		+0.4	
CLINCH HEIGHT	H2			3	-0.1	
PULL - OUT FORCE	H2 (P)	6N		3		

- NOTES

  1. MAXIMUM ALIGNMENT DEVIATION BETWEEN LEADS NOT TO BE GREATER THAN 0.2 mm.

  2. MAXIMUM NON-CUMULATIVE VARIATION BETWEEN TAPE FEED HOLES SHALL NOT EXCEED 1 mm IN 20
- HOLDDOWN TAPE NOT TO EXCEED BEYOND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO
- NOUNDOWN THE ROT TO EXCEED BETOIND THE EDGE(S) OF CARRIER TAPE AND THERE SHALL BE NO EXPOSURE OF ADHESIVE.

  NO MORE THAN 3 CONSECUTIVE MISSING COMPONENTS ARE PERMITTED.

  A TAPE TRAILER, HAVING AT LEAST THREE FEED HOLES ARE REQUIRED AFTER THE LAST COMPONENT. SPLICES SHALL NOT INTERFERE WITH THE SPROCKET FEED HOLES.

# **Packing Detail**

PACKAGE	STANDARD PACK		STANDARD PACK INNER CARTON BOX		OUTER CARTON BOX		
	Details	Net Weight/Qty	Size Qty		Size	Qty	Gr Wt
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs

Notes MPS2907

MPS2907A

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## Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished in the Data Sheet and on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

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