



## NPN SILICON PLANAR EPITAXIAL TRANSISTORS



MPS8098 MPS8099

TO-92 Plastic Package

## Amplifier Transistors

#### **ABSOLUTE MAXIMUM RATING**

DESCRIPTION	SYMBOL	MPS8098	MPS8099	UNITS
Collector Base Voltage	V <sub>CBO</sub>	60	80	V
Collector Emitter Voltage	V <sub>CEO</sub>	60	80	V
Emitter Base Voltage	V <sub>EBO</sub>	6	5.0	V
Collector Current Continuous	Ι <sub>C</sub>	5	mA	
Power Dissipation T <sub>a</sub> =25 <sup>o</sup> C	PD	6	mW	
Derate Above 25°C		5	mW/⁰C	
Power Dissipation T <sub>c</sub> =25 <sup>o</sup> C	PD	1	1.5	
Derate Above 25°C		12		mW/⁰C
Operating And Storage Junction Temperature Range	T <sub>j</sub> ,T <sub>stg</sub>	- 55 t	٥C	

#### **THERMAL CHARACTERISTICS**

Junction to Case	R <sub>th(j-c)</sub>	83.3	°C/W
Junction to Ambient in free air	R <sub>th(j-a)</sub>	200	°C/W

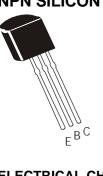
#### ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25° C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
Collector Base Voltage	V <sub>CBO</sub>	I <sub>C</sub> =100μA, I <sub>E</sub> =0				
		MPS8098	60			V
		MPS8099	80			V
Collector Emitter Voltage	V <sub>CEO</sub>	I <sub>C</sub> =10mA, I <sub>B</sub> =0				
		MPS8098	60			V
		MPS8099	80			V
Emitter Base Voltage	V <sub>EBO</sub>	I <sub>E</sub> =10μΑ, I <sub>C</sub> =0	6.0			V
Collector Cut Off Current	ceo	V <sub>CE</sub> =60V, I <sub>B</sub> =0			0.1	μA
Collector Cut Off Current	Сво	MPS8098				
		$V_{CB}$ =60V, I <sub>E</sub> =0			0.1	μΑ
		MPS8099				
		V <sub>CB</sub> =80V, I <sub>E</sub> =0			0.1	μA
Emitter Cut Off Current	ЕВО	$V_{EB}=6V, I_{C}=0$			0.1	μA

## NPN SILICON PLANAR EPITAXIAL TRANSISTOR

### MPS8098 MPS8099

TO-92 Plastic Package



### ELECTRICAL CHARACTERISTICS (T<sub>a</sub>=25° C unless specified otherwise)

DESCRIPTION	SYMBOL	TEST CONDITION	MIN	TYP	MAX	UNITS
DC Current Gain	*h <sub>FE</sub>	I <sub>C</sub> =1mA, V <sub>CE</sub> =5V	100		300	
		I <sub>C</sub> =10mA, V <sub>CE</sub> =5V	100			
		I <sub>C</sub> =100mA, V <sub>CE</sub> =5V	75			
Collector Emitter Saturation Voltage	*V <sub>CE(sat)</sub>	I <sub>C</sub> =100mA, I <sub>B</sub> =5mA			0.4	V
		I <sub>C</sub> =100mA, I <sub>B</sub> =10mA			0.3	V
Base Emitter On Voltage	*V <sub>BE(on)</sub>	MPS8098				
		I <sub>C</sub> =1mA,V <sub>CE</sub> =5V	0.5		0.7	V
		MPS8099				
		I <sub>C</sub> =10mA,V <sub>CE</sub> =5V	0.6		0.8	V

#### SMALL SIGNAL CHARACTERISTICS

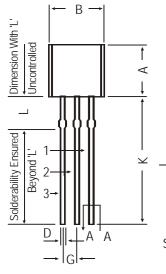
Current Gain Bandwidth Product	f <sub>⊤</sub>	I <sub>C</sub> =10mA, V <sub>CE</sub> =5V,f=100MHz	150		MHz
Output Capacitance	C <sub>obo</sub>	I <sub>E</sub> =0, V <sub>CB</sub> =5V, f=1MHz		6.0	pF
Input Capacitance	C <sub>ibo</sub>	I <sub>C</sub> =0, V <sub>EB</sub> =0.5V, f=1MHz		25	pF

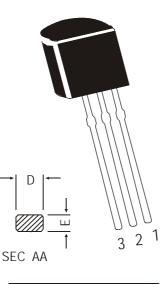
\*Pulse Test: Pulse Width < 300ms, Duty Cycle=2%

#### **MPS8098 MPS8099**

#### **TO-92 Plastic Package**

### **TO-92 Plastic Package**





MIN.

4.32

4.45

3.18

0.41

0.35

1.14

1.14

12.70

1.982

5 DEG

MAX.

5.33

5.20

4.19

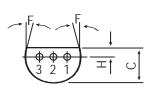
0.55

0.50

1.40

1.53

2.082



**PIN CONFIGURATION** 

1. COLLECTOR

2. BASE

3. EMITTER

All diminsions in mm.

DIM

А

В

С

D

E

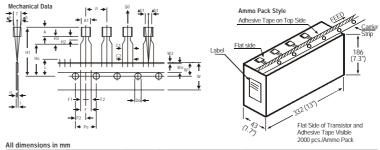
F

G

Н

Κ

L



# **TO-92 Transistors on Tape and Ammo Pack**

		SPECIFICATION		ON		
ITEM	SYMBOL	MIN.	NOM.	MAX.	TOL .	REMARKS
BODY WIDTH	A1	4.0		4.8		
BODY HEIGHT	A	4.8		5.2		
BODY THICKNESS	Т	3.9		4.2		
PITCH OF COMPONENT	Р		12.7		± 1.0	
FEED HOLE PITCH	Po		12.7		± 0.3	CUMULATIVE PITCH ERROR 1.0 mm/20 PITCH
FEED HOLE CENTRE TO						
COMPONENT CENTRE	P2		6.35		± 0.4	TO BE MEASURED AT BOTTOM OF CLINCH
DISTANCE BETWEEN OUTER					+ 0.6	
LEADS	F		5.08		- 0.2	
COMPONENT ALIGNMENT SIDE VIEW	∆h		0	1.0		AT TOP OF BODY
COMPONENT ALIGNMENT FRONT VIEW	∆h1		0	1.3		AT TOP OF BODY
TAPE WIDTH	W		18		± 0.5	
HOLD-DOWN TAPE WIDTH	Wo		6		± 0.2	
HOLE POSITION	W1		9		+ 0.7	
					- 0.5	
HOLD-DOWN TAPE POSITION	W2		0.5		± 0.2	
LEAD WIRE CLINCH HEIGHT	Ho		16		± 0.5	
COMPONENT HEIGHT	H1			23.25		
LENGTH OF SNIPPED LEADS	L			11.0		
FEED HOLE DIAMETER	Do		4		± 0.2	
TOTAL TAPE THICKNESS	t			1.2		t1 0.3-0.6
LEAD - TO - LEAD DISTANCE	F1, F2		2.54		+ 0.4	
STAND OFF	H2	0.45		1.45	- 0.1	
CLINCH HEIGHT	H3			3.0		
LEAD PARALLELISM	C1 - C2			0.22		
PULL - OUT FORCE	(P)	6N				

NOTES

NOTES
1. Maximum alignment deviation between leads will not to be greater than 0.2mm.
2. Maximum non-cumulative variation between tape feed holes shall not exceed 1 mm in 20 pitches.
3. Holddown tape will not exceed beyond the edge(s) of carrier tape and there shall be no exposure of adhesive.
4. There will be no more than three (3) consecutive missing components in a tape.
5. A tape traiter, having at least three feed holes are provided after the last component in a tape.
6. Splices should not interfere with the sprocket feed holes.

**Packing Detail** 

	· · · · · · · · · · · · · · · · · · ·								
PACKAGE	STANDARD PACK		INNER CARTO	N BOX	OUTER CARTON BOX				
	Details	ails Net Weight/Oty Size		Qty	Size	Qty	Gr Wt		
TO-92 Bulk	1K/polybag	200 gm/1K pcs	3" x 7.5" x 7.5"	5K	17" x 15" x 13.5"	80K	23 kgs		
TO-92 T&A	2K/ammo box	645 gm/2K pcs	12.5" x 8" x 1.8"	2K	17" x 15" x 13.5"	32K	12.5 kgs		

### CMBT8098 CMBT8099

TO-92 Plastic Package

### Disclaimer

The product information and the selection guides facilitate selection of the CDIL's Discrete Semiconductor Device(s) best suited for application in your product(s) as per your requirement. It is recommended that you completely review our Data Sheet(s) so as to confirm that the Device(s) meet functionality parameters for your application. The information furnished on the CDIL Web Site/CD are believed to be accurate and reliable. CDIL however, does not assume responsibility for inaccuracies or incomplete information. Furthermore, CDIL does not assume liability whatsoever, arising out of the application or use of any CDIL product; neither does it convey any license under its patent rights nor rights of others. These products are not designed for use in life saving/support appliances or systems. CDIL customers selling these products (either as individual Discrete Semiconductor Devices or incorporated in their end products), in any life saving/support appliances or systems or applications do so at their own risk and CDIL will not be responsible for any damages resulting from such sale(s).

CDIL strives for continuous improvement and reserves the right to change the specifications of its products without prior notice.



CDIL is a registered Trademark of Continental Device India Limited C-120 Naraina Industrial Area, New Delhi 110 028, India. Telephone + 91-11-2579 6150, 5141 1112 Fax + 91-11-2579 5290, 5141 1119 email@cdil.com www.cdilsemi.com

MPS8098\_8099Rev190402E

Continental Device India Limited