DC COMPONENTS CO., LTD.

## TECHNICAL SPECIFICATIONS OF FAST RECOVERY GLASS PASSIVATED RECTIFIER

VOLTAGE RANGE - 50 to 1000 Volts

## FEATURES

* High reliability
* Low leakage
* Low forward voltage drop
* High current capability
* High switching capability
* Glass passivated junction


## MECHANICAL DATA

* Case: Molded plastic
* Epoxy: UL 94V-0 rate flame retardant
* Lead: MIL-STD-202E, Method 208 guaranteed
* Polarity: Color band denotes cathode end
* Mounting position: Any
* Weight: 0.22 gram

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS
Ratings at $25^{\circ} \mathrm{C}$ ambient temperature unless otherwise specified. Single phase, half wave, 60 Hz , resistive or inductive load.
For capacitive load, derate current by $20 \%$.
CURRENT - 1.0 Ampere


|  | SYMBOL | RL101FG | RL102FG | RL103FG | RL104FG | RL105FG | RL106FG | RL107FG | UNITS |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| Maximum Recurrent Peak Reverse Voltage | VRrM | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | Volts |
| Maximum RMS Voltage | Vrms | 35 | 70 | 140 | 280 | 420 | 560 | 700 | Volts |
| Maximum DC Blocking Voltage | VDC | 50 | 100 | 200 | 400 | 600 | 800 | 1000 | Volts |
| Maximum Average Forward Rectified Current at $\mathrm{TA}=55^{\circ} \mathrm{C}$ | 10 | 1.0 |  |  |  |  |  |  | Amps |
| Peak Forward Surge Current 8.3 ms single half sine-wave superimposed on rated load (JEDEC Method) | IFSM | 30 |  |  |  |  |  |  | Amps |
| Maximum Instantaneous Forward Voltage at 1.0A DC | VF | 1.3 |  |  |  |  |  |  | Volts |
| Maximum DC Reverse Current at Rated DC Blocking Voltage TA $=25^{\circ} \mathrm{C}$ | IR | 5.0 |  |  |  |  |  |  | uAmps |
| Maximum Full Load Reverse Current Average, Full Cycle $.375^{*}(9.5 \mathrm{~mm})$ lead length at $\mathrm{L}=55^{\circ} \mathrm{C}$ |  | 100 |  |  |  |  |  |  | uAmps |
| Maximum Reverse Recovery Time (Note 1) | trr |  |  | 50 |  | 250 |  | 00 | nSec |
| Typical Junction Capacitance (Note 2) | CJ | 15 |  |  |  |  |  |  | pF |
| Operating and Storage Temperature Range | TJ, Tsta | -65 to + 150 |  |  |  |  |  |  | ${ }^{0} \mathrm{C}$ |

NOTES: 1. Test Conditions: $\mathrm{IF}=0.5 \mathrm{~A}, \mathrm{IR}=1.0 \mathrm{~A}, \mathrm{IRR}=0.25 \mathrm{~A}$
2. Measured at 1 MHz and applied reverse voltage of 4.0 volts

NEXT

FIG. 1 - TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC



FIG. 1 - TYPICAL FORWARD CURRENT DERATING CURVE


FIG. 4 - TYPICAL JUNCTION CAPACITANCE



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