

3875081 G E SOLID STATE

01E 17349 D T-35-19

General-Purpose Power Transistors

File Number 141

2N1700

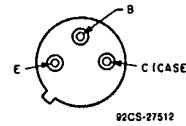
## Silicon N-P-N Power-Switching Transistor

For Switching Applications

**Features:**

- Operation at high junction temperatures

TERMINAL DESIGNATIONS



JEDEC TO-205AD

The RCA-2N1700 silicon n-p-n transistor is intended for a wide variety of uses in industrial equipment. They are particularly useful in applications such as inverters, choppers, voltage and current regulators, and relay-actuating circuits.

The 2N1700 is supplied in a JEDEC TO-205AD package.

**MAXIMUM RATINGS, Absolute-Maximum Values:**

	2N1700	
* $V_{CBO}$ .....	60	V
* $V_{CEX}$ $V_{BE} = -1.5$ V .....	60	V
* $V_{CEO(SUS)}$ .....	40	V
* $V_{EBO}$ .....	6	V
* $I_C$ .....	1	A
* $I_B$ .....	0.75	A
* $P_T$ $T_C \leq 25^\circ\text{C}$ .....	5	W
$T_C > 25^\circ\text{C}$ .....	0.029	$^\circ\text{C/W}$
* $T_{stg}, T_J$ .....	-65 to +200	$^\circ\text{C}$
* $T_L$ At distance $\geq 1/16$ in. $\pm 1/32$ in. (1.58 mm $\pm 0.8$ mm) from seating plane for 10 s max. ....	255	$^\circ\text{C}$

\*In accordance with JEDEC registration data format.

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ELECTRICAL CHARACTERISTICS,  $T_C=25^\circ\text{C}$  Unless Otherwise Specified

CHARACTERISTIC	TEST CONDITIONS				LIMITS		UNITS
	VOLTAGE V dc		CURRENT A dc		2N1700		
	$V_{CE}$	$V_{BE}$	$I_C$	$I_B$	Min.	Max.	
$I_{CBO}$ $V_{CB}=30\text{ V}$ $V_{CB}=60\text{ V}$					—	75 25	$\mu\text{A}$
$I_C$ $T_C=150^\circ\text{C}$ , $V_{CB}=30\text{ V}$					—	1	mA
$I_{EBO}$		-6	0		—	25	$\mu\text{A}$
$V_{CE0(sus)}$			0.05 <sup>a</sup>	0	40 <sup>b</sup>	—	V
$V_{CEX}$		-1.5	0.0005		60 <sup>b</sup>	—	V
$h_{FE}$	4 20		0.1 <sup>a</sup> 1 <sup>a</sup>		20 6	80 —	
$V_{BE}$	4 20		0.1 1		— —	2 12.5	V
$r_{CE(sat)}$			0.1	0.01	—	10	$\Omega$
$V_{CE(sat)}$			1 <sup>a</sup>	0.5	—	12	V
$h_{ie}$ $f=1\text{ MHz}$	4		5		40	—	
$f_{hib}$ $V_{CB}=6\text{ V}$ $V_{CB}=28\text{ V}$				0.0005	400	— 1.2 (typ.)	kHz MHz
$C_{obo}$ $V_{CB}=40\text{ V}$ , $f=1\text{ MHz}$						150 (typ.)	pF
$\tau_i$						10 (typ.)	ms
$R_{\theta JC}$					—	35	$^\circ\text{C/W}$
$R_{\theta JA}$					—	200	

<sup>a</sup>In accordance with JEDEC registration data format.

<sup>b</sup>Pulsed: pulse duration = 300  $\mu\text{s}$ , duty factor  $\leq 2\%$ .

<sup>c</sup>CAUTION: The sustaining voltages  $V_{CE0(sus)}$  and  $V_{CEX(sus)}$  MUST NOT be measured on a curve tracer.

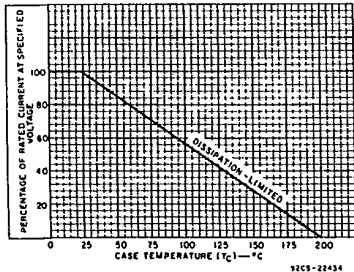


Fig. 1 - Derating curve.

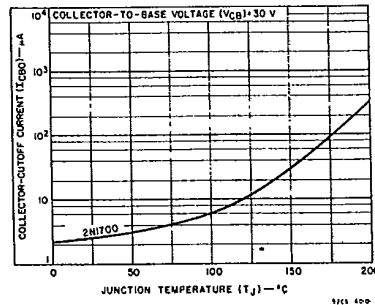


Fig. 2 - Typical collector-cutoff current characteristics.

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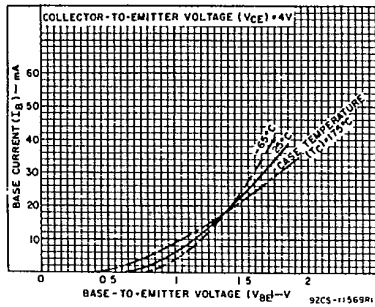


Fig. 3 - Typical input characteristics.

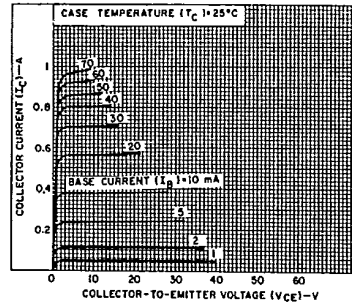


Fig. 4 - Typical output characteristics.

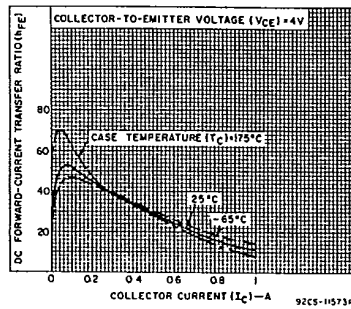
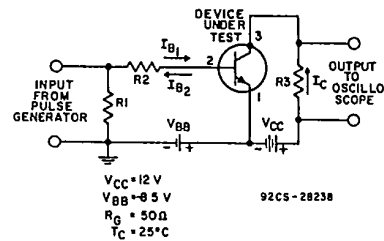
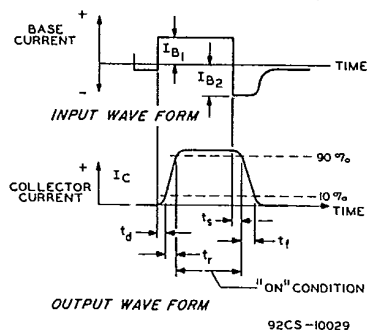


Fig. 5 - Typical dc beta characteristics.



Test Conditions:

R <sub>1</sub> .....	1 W	50	Ω
R <sub>2</sub> .....	1 W	700	Ω
R <sub>3</sub> .....	2 W	59	Ω
I <sub>C</sub> .....		200	mA
I <sub>B1</sub> .....		20	mA
I <sub>B2</sub> .....		-8.5	mA

Switching Times:

t <sub>d</sub> .....	0.2	μs
t <sub>r</sub> .....	1	μs
t <sub>s</sub> .....	0.6	μs
t <sub>f</sub> .....	1	μs

Fig. 6 - Test circuit and oscilloscope display for measurement of switching times.

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