

2N4340, 2N4341

N-Channel Silicon Junction Field-Effect Transistor

- Small Signal Amplifiers
- Current Regulators
- Voltage-Controlled Resistors

Absolute maximum ratings at $T_A = 25^\circ\text{C}$

Reverse Gate Source & Reverse Gate Drain Voltage
 Continuous Forward Gate Current
 Continuous Device Power Dissipation
 Power Derating (to 175°C)

– 50 V
 50 mA
 300 mW
 2mW/ $^\circ\text{C}$

At 25°C free air temperature:

Static Electrical Characteristics

		2N4340		2N4341		Process NJ16		
		Min	Max	Min	Max	Unit	Test Conditions	
Gate Source Breakdown Voltage	$V_{(BR)GSS}$	– 50		– 50		V	$I_G = -1\ \mu\text{A}$, $V_{DS} = 0\text{V}$	
Gate Reverse Current	I_{GSS}		– 100		– 100	pA	$V_{GS} = -30\text{V}$, $V_{DS} = 0\text{V}$	
			– 100		– 100	nA	$V_{GS} = -30\text{V}$, $V_{DS} = 0\text{V}$	$T_A = 150^\circ\text{C}$
Gate Source Cutoff Voltage	$V_{GS(OFF)}$	– 1	– 3	– 2	– 6	V	$V_{DS} = 15\text{V}$, $I_D = 0.1\ \mu\text{A}$	
Drain Saturation Current (Pulsed)	I_{DSS}	1.2	3.6	3	9	mA	$V_{DS} = 15\text{V}$, $V_{GS} = 0\text{V}$	
Drain Cutoff Current	$I_{D(OFF)}$		0.05 (– 5)		0.07 (– 10)	nA V	$V_{DS} = 15\text{V}$, $V_{GS} = ()$	

Dynamic Electrical Characteristics

Drain Source ON Resistance	$r_{ds(on)}$		1500		800	Ω	$V_{GS} = 0\text{V}$, $I_D = 0\text{A}$	$f = 1\text{ kHz}$
Common Source Forward Transconductance	g_{fs}	1300	3000	2000	4000	μS	$V_{DS} = 15\text{V}$, $V_{GS} = 0\text{V}$	$f = 1\text{ kHz}$
Common Source Output Conductance	g_{os}		30		60	μS	$V_{DS} = 15\text{V}$, $V_{GS} = 0\text{V}$	$f = 1\text{ kHz}$
Common Source Input Capacitance	C_{iss}		7		7	pF	$V_{DS} = 15\text{V}$, $V_{GS} = 0\text{V}$	$f = 1\text{ MHz}$
Common Source Reverse Transfer Capacitance	C_{rss}		3		3	pF	$V_{DS} = 15\text{V}$, $V_{GS} = 0\text{V}$	$f = 1\text{ MHz}$
Noise Figure	NF		1		1	dB	$V_{DS} = 15\text{V}$, $V_{GS} = 0\text{V}$ $R_G = 1\text{ M}\Omega$, BW = 200 Hz	$f = 1\text{ kHz}$

TO-18 Package

Dimensions in Inches (mm)

Pin Configuration

1 Source, 2 Drain, 3 Gate & Case

Surface Mount

SMP4340, SMP4341



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