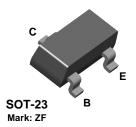


2N4126

MMBT4126





PNP General Purpose Amplifier

This device is designed for general purpose amplifier and switching applications at collector currents to 10 µA as a switch and to 100 mA as an amplifier.

Absolute Maximum Ratings* TA = 25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CEO}	Collector-Emitter Voltage	25	V	
V _{CBO}	Collector-Base Voltage	25	V	
V _{EBO}	Emitter-Base Voltage	4.0	V	
I _C	Collector Current - Continuous	200	mA	
T _J , T _{stg}	Operating and Storage Junction Temperature Range	-55 to +150	°C	

^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

- 1) These ratings are based on a maximum junction temperature of 150 degrees C.

 2) These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.
- 3) All voltages (V) and currents (A) are negative polarity for PNP transistors.

Thermal Characteristics TA= 25°C unless otherwise noted

Symbol	Characteristic	Max		Units
		2N4126	*MMBT4126	
P_D	Total Device Dissipation	625	350	mW
	Derate above 25°C	5.0	2.8	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3		°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	357	°C/W

^{*}Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06."

PNP General Purpose Amplifier (continued)

	_	
Flectrical	('harac	*+Arictice
1 150.1110.01	Callal at	

TA = 25°C unless otherwise noted

Parameter	Test Conditions	Min	Max	Units
RACTERISTICS				
Collector-Emitter Breakdown Voltage	$I_C = 1.0 \text{ mA}, I_B = 0$	25		V
Collector-Base Breakdown Voltage	$I_C = 10 \mu\text{A}, I_E = 0$	25		V
Emitter-Base Breakdown Voltage	$I_C = 10 \mu\text{A}, I_C = 0$	4.0		V
Collector Cutoff Current	$V_{CB} = 20 \text{ V}, I_{E} = 0$		50	nA
Emitter Cutoff Current	$V_{EB} = 3.0 \text{ V}, I_{C} = 0$		50	nA
	RACTERISTICS Collector-Emitter Breakdown Voltage Collector-Base Breakdown Voltage Emitter-Base Breakdown Voltage Collector Cutoff Current		$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$

ON CHARACTERISTICS*

h _{FE}	DC Current Gain	$I_C = 2.0 \text{ mA}, V_{CE} = 1.0 \text{ V}$	120	360	
		$I_C = 50 \text{ mA}, V_{CE} = 1.0 \text{ V}$	60		
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_C = 50 \text{ mA}, I_B = 5.0 \text{ mA}$		0.4	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	$I_C = 50 \text{ mA}, I_B = 5.0 \text{ mA}$		0.95	V

SMALL SIGNAL CHARACTERISTICS

f _T	Current Gain - Bandwidth Product	$I_C = 10 \text{ mA}, V_{CE} = 20 \text{ V},$ f = 100 MHz	250		MHz
C _{ibo}	Input Capacitance	$V_{EB} = 0.5 \text{ V}, I_{C} = 0,$ f = 1.0 MHz		10	pF
C _{cb}	Collector-Base Capcitance	$V_{CB} = 5.0 \text{ V}, I_{E} = 0,$ f = 100 kHz		4.5	pF
h _{fe}	Small-Signal Current Gain	$I_C = 2.0 \text{ mA}, V_{CE} = 10 \text{ V},$ f = 1.0 kHz	120	480	
NF	Noise Figure	$I_C = 100 \mu A$, $V_{CE} = 5.0 V$, $R_S=1.0 kΩ$, $f=10 Hz$ to 15.7 kHz		4.0	dB

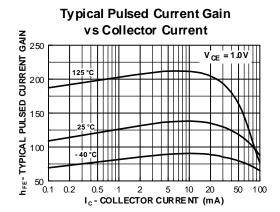
^{*}Pulse Test: Pulse Width \leq 300 μ s, Duty Cycle \leq 2.0%

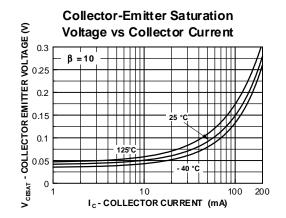
NOTE: All voltages (V) and currents (A) are negative polarity for PNP transistors.

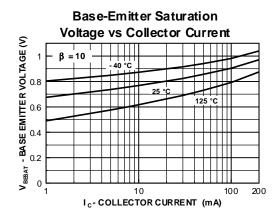
PNP General Purpose Amplifier

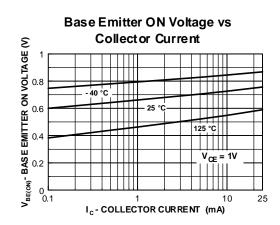
(continued)

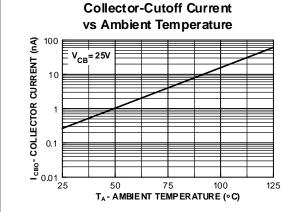
Typical Characteristics

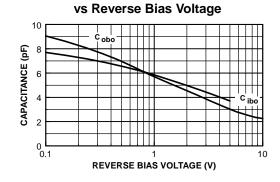












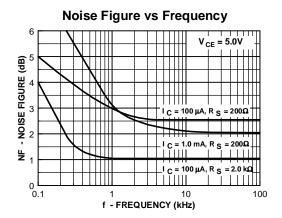
Common-Base Open Circuit

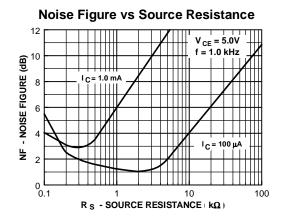
Input and Output Capacitance

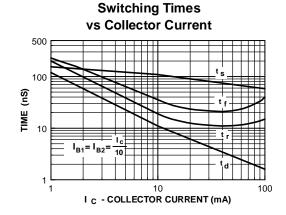
PNP General Purpose Amplifier

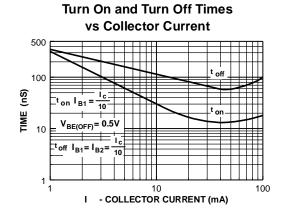
(continued)

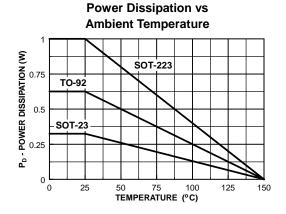
Typical Characteristics (continued)







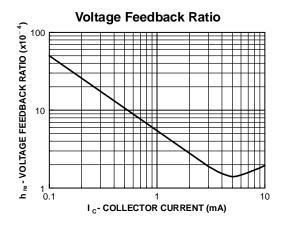


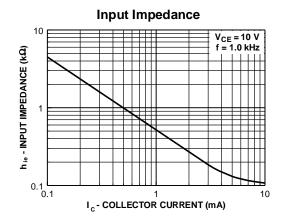


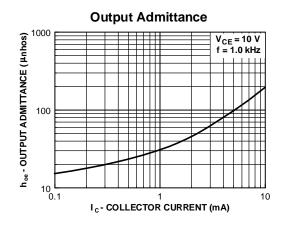
PNP General Purpose Amplifier

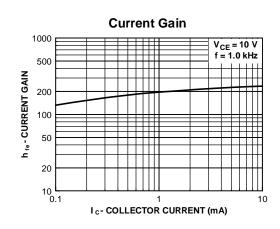
(continued)

Typical Characteristics (continued)









TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

 $ACEx^{TM}$ FASTr™ PowerTrench® SyncFET™ Bottomless™ QFET™ TinyLogic™ GlobalOptoisolator™ QSTM UHC™ CoolFET™ GTO™ **VCX**TM $CROSSVOLT^{TM}$ QT Optoelectronics™ HiSeC™

DOME™ ISOPLANAR™ Quiet Series™

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.