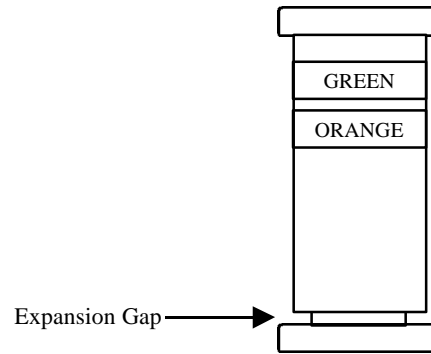


**General Description:**

A General Purpose diode that couples high forward conductance fast switching speed and high blocking voltages in a glass leadless LL-34 Surface Mount package.

Placement of the Expansion Gap has no relationship to the location of the Cathode Terminal which is indicated by the first color band.



## High Voltage, General Purpose Diode

**Absolute Maximum Ratings\*** TA = 25°C unless otherwise noted

Sym	Parameter	Value	Units
T <sub>stg</sub>	Storage Temperature	-65 to +200	°C
T <sub>J</sub>	Operating Junction Temperature	-65 to +200	°C
P <sub>D</sub>	Total Power Dissipation at T <sub>A</sub> = 25°C	500	mW
	Linear Derating Factor from T <sub>A</sub> = 25°C	3.33	mW/°C
R <sub>OJA</sub>	Thermal Resistance Junction-to-Ambient	350	°C/W
W <sub>IV</sub>	Working Inverse Voltage	200	V
I <sub>O</sub>	Average Rectified Current	200	mA
I <sub>F</sub>	DC Forward Current (IF)	500	mA
i <sub>f</sub>	Recurrent Peak Forward Current	600	mA
i <sub>F(surge)</sub>	Peak Forward Surge Current (IFSM) Pulse Width = 1.0 second	1.0	Amp
	Pulse Width = 1.0 microsecond	4.0	Amp

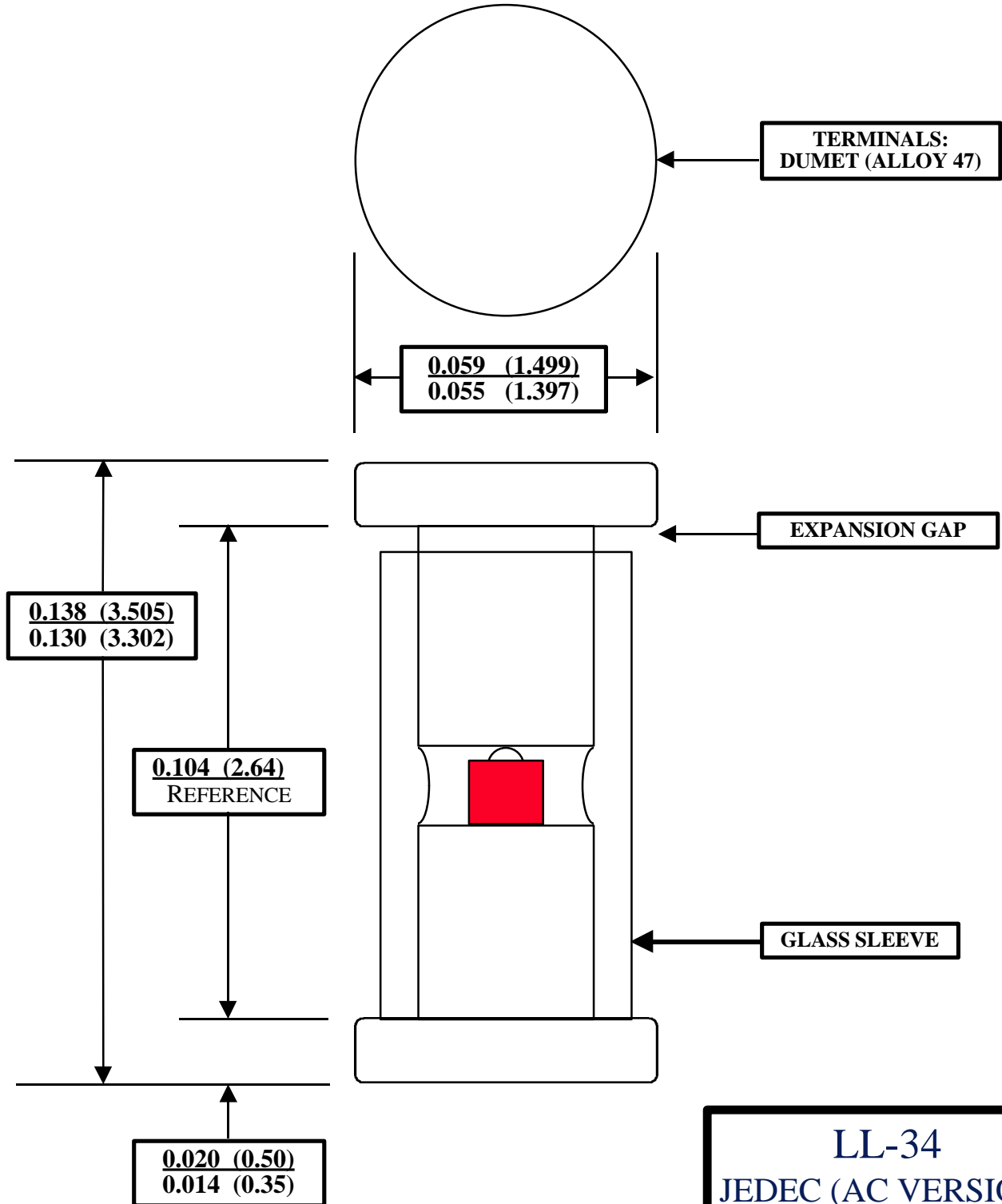
\*These ratings are limiting values above which the serviceability of any semiconductor device may be impaired

## Electrical Characteristics

TA = 25°C unless otherwise noted

SYM	CHARACTERISTICS	MIN	MAX	UNITS	TEST CONDITIONS
B <sub>V</sub>	Breakdown Voltage	250		V	I <sub>R</sub> = 100 uA
I <sub>R</sub>	Reverse Leakage		100	nA	V <sub>R</sub> = 200 V V <sub>R</sub> = 200 V T <sub>A</sub> = 150°C
			100	uA	
V <sub>F</sub>	Forward Voltage		1.00	V	I <sub>F</sub> = 100 mA I <sub>F</sub> = 200 mA
			1.25	V	
C <sub>T</sub>	Capacitance		5.0	pF	V <sub>R</sub> = 0.0 V, f = 1.0 MHz
T <sub>RR</sub>	Reverse Recovery Time		50	ns	I <sub>F</sub> = I <sub>R</sub> 30 mA I <sub>RR</sub> = 1.0 mA R <sub>L</sub> = 100 Ohms

THE PLACEMENT OF THE EXPANSION GAP HAS NO RELATIONSHIP TO THE LOCATION OF THE CATHODE TERMINAL OF THE DEVICE. THE EXPANSION GAP & CATHODE BAND CAN BE ON THE SAME TERMINAL OR AT OPPOSITE TERMINALS OF THE DIODE.



**LL-34**  
**JEDEC (AC VERSION)**  
Fairchild Semiconductor's Criteria  
5-MAR-98

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## PRODUCT STATUS DEFINITIONS

### Definition of Terms

Datasheet Identification	Product Status	Definition
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