

FDP6035AL/FDB6035AL

N-Channel Logic Level PowerTrench™ MOSFET

General Description

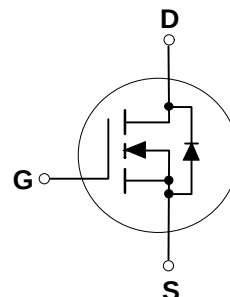
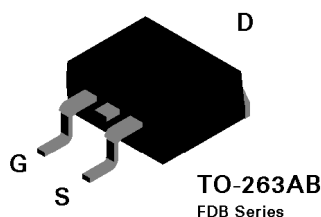
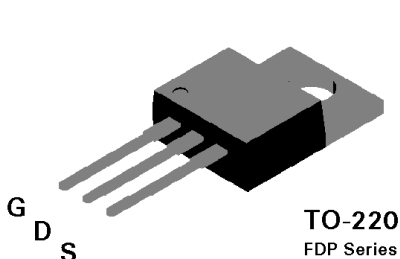
This N-Channel Logic Level MOSFET has been designed specifically to improve the overall efficiency of DC/DC converters using either synchronous or conventional switching PWM controllers.

These MOSFETs feature faster switching and lower gate charge than other MOSFETs with comparable $R_{DS(on)}$ specifications.

The result is a MOSFET that is easy and safer to drive (even at very high frequencies), and DC/DC power supply designs with higher overall efficiency.

Features

- 48A, 30 V. $R_{DS(on)} = 0.0125 \Omega @ V_{GS} = 10 \text{ V}$,
 $R_{DS(on)} = 0.017 \Omega @ V_{GS} = 4.5 \text{ V}$.
- Critical DC electrical parameters specified at elevated temperature.
- Rugged internal source-drain diode can eliminate the need for an external Zener diode transient suppressor.
- High performance trench technology for extremely low $R_{DS(on)}$.
- 175°C maximum junction temperature rating.



Absolute Maximum Ratings $T_c = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	FDP6035AL	FDB6035AL	Units
V_{DSS}	Drain-Source Voltage	30		V
V_{GSS}	Gate-Source Voltage	± 20		V
I_D	Drain Current - Continuous (Note 1)	48		A
	- Pulsed (Note 1)	150		
P_D	Total Power Dissipation @ $T_c = 25^\circ\text{C}$	58		W
	Derate above 25°C	0.4		W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-65 to 175		$^\circ\text{C}$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	275		$^\circ\text{C}$
THERMAL CHARACTERISTICS				
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	2.6		$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5		$^\circ\text{C/W}$

Electrical Characteristics ($T_c = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Unit
DRAIN-SOURCE AVALANCHE RATINGS (Note 1)						
W _{DSS}	Single Pulse Drain-Source Avalanche Energy	V _{DD} = 15 V, I _D = 48 A			130	mJ
I _{AR}	Maximum Drain-Source Avalanche Current				48	A
OFF CHARACTERISTICS						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	30			V
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	I _D = 250 μA, Referenced to 25 °C		22		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V			1	μA
I _{GSSF}	Gate - Body Leakage, Forward	V _{GS} = 20 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate - Body Leakage, Reverse	V _{GS} = -20 V, V _{DS} = 0 V			-100	nA
ON CHARACTERISTICS (Note 2)						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	1	1.5	3	V
ΔV _{GS(th)} /ΔT _J	Gate Threshold Voltage Temp.Coefficient	I _D = 250 μA, Referenced to 25 °C		-5		mV/°C
R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 24 A		0.011	0.0125	Ω
		T _J = 125°C		0.017	0.021	
		V _{GS} = 4.5 V, I _D = 20 A		0.015	0.017	
I _{D(on)}	On-State Drain Current	V _{GS} = 10 V, V _{DS} = 10 V	48			A
g _{FS}	Forward Transconductance	V _{DS} = 10 V, I _D = 24 A		33		S
DYNAMIC CHARACTERISTICS						
C _{iss}	Input Capacitance	V _{DS} = 15 V, V _{GS} = 0 V, f = 1.0 MHz		1650		pF
C _{oss}	Output Capacitance			365		pF
C _{rss}	Reverse Transfer Capacitance			170		pF
SWITCHING CHARACTERISTICS (Note 1)						
t _{D(on)}	Turn - On Delay Time	V _{DD} = 15 V, I _D = 1 A, V _{GS} = 10 V, R _{GEN} = 6 Ω		10	18	nS
t _r	Turn - On Rise Time			12	22	nS
t _{D(off)}	Turn - Off Delay Time			35	56	nS
t _f	Turn - Off Fall Time			10	18	nS
Q _g	Total Gate Charge	V _{DS} = 15 V, I _D = 48 A V _{GS} = 5 V		17	23	nC
Q _{gs}	Gate-Source Charge			6.2		nC
Q _{gd}	Gate-Drain Charge			6.8		nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
I _S	Maximum Continuous Drain-Source Diode Forward Current (Note 1)				48	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current (Note 1)				150	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 24 A (Note1)		1.05	1.3	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _F = 30 A		23	40	ns
I _{rr}	Reverse Recovery Current	dI _F /dt = 100 A/μs		0.74	1.3	A

Notes

1. Pulse Test: Pulse Width $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 2.0\%$.

Typical Electrical Characteristics

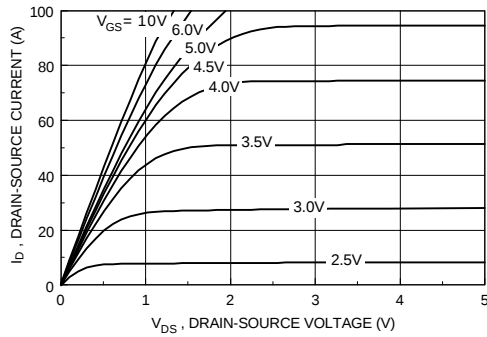


Figure 1. On-Region Characteristics.

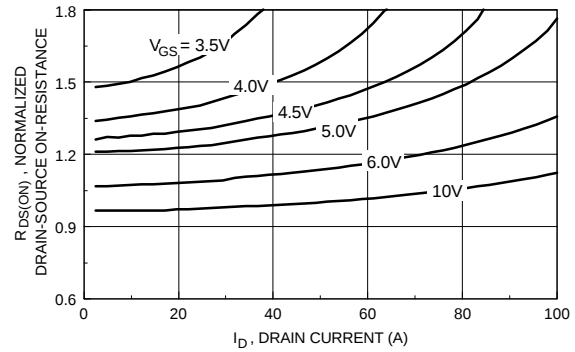


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

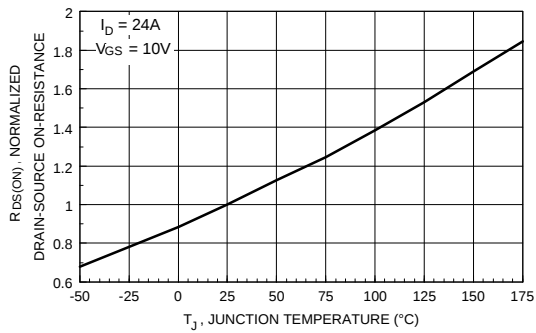


Figure 3. On-Resistance Variation with Temperature.

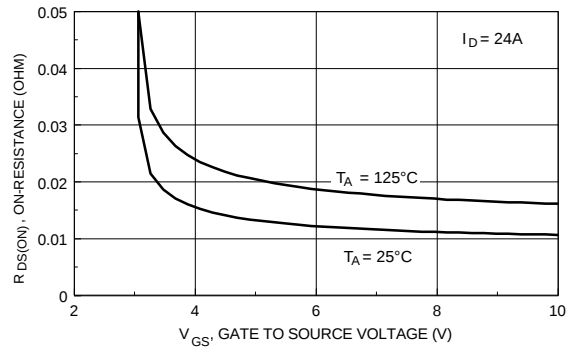


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

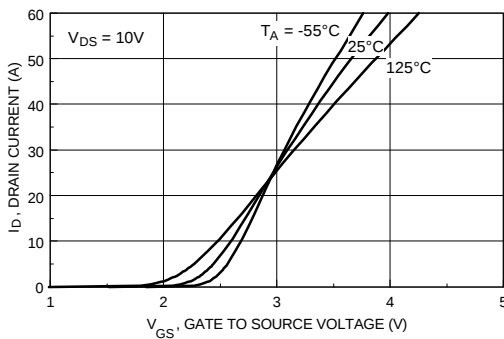


Figure 5. Transfer Characteristics.

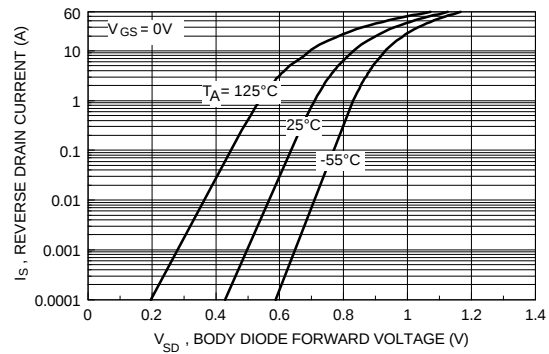


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Electrical Characteristics (continued)

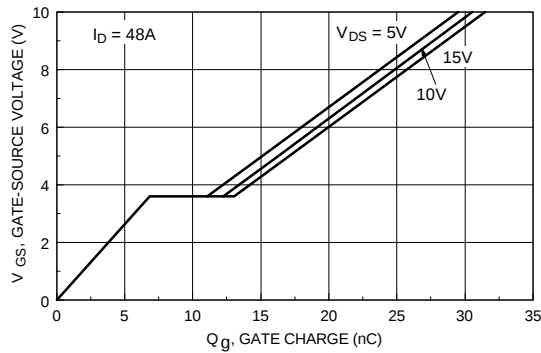


Figure 7. Gate Charge Characteristics.

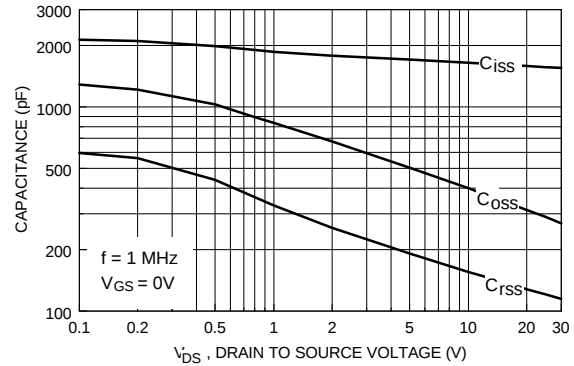


Figure 8. Capacitance Characteristics.

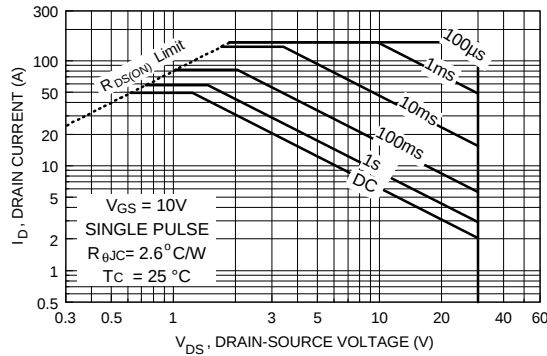


Figure 9. Maximum Safe Operating Area.

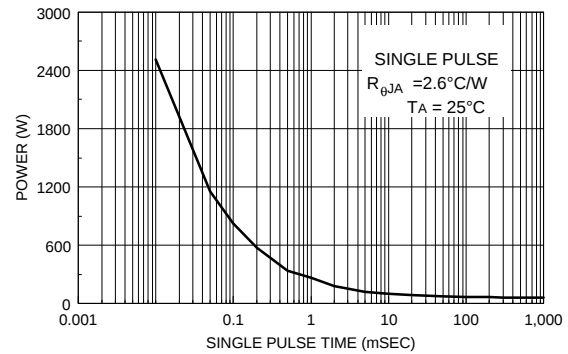


Figure 10. Single Pulse Maximum Power Dissipation.

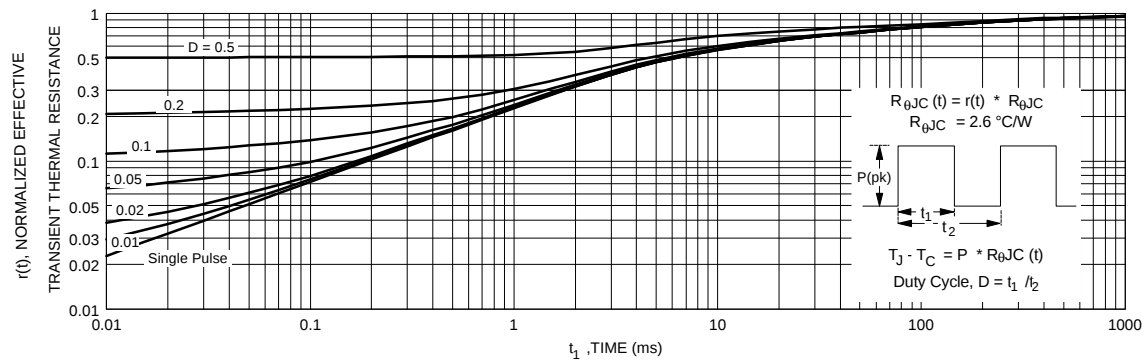


Figure 11. Transient Thermal Response Curve.

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