

ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted)

Symbol	Parameter	Conditions	Min	Typ	Max	Units
OFF CHARACTERISTICS						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	30			V
ΔBV _{DSS} /ΔT _J	Breakdown Voltage Temp. Coefficient	I _D = 250 μA, Referenced to 25 °C		42		mV/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 24 V, V _{GS} = 0 V T _J = 55 °C			1 10	μA μA
I _{GSSF}	Gate - Body Leakage, Forward	V _{GS} = 8 V, V _{DS} = 0 V			100	nA
I _{GSSR}	Gate - Body Leakage, Reverse	V _{GS} = -8 V, V _{DS} = 0 V			-100	nA
ON CHARACTERISTICS (Note 2)						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	0.4	0.67	1	V
ΔV _{GS(th)} /ΔT _J	Gate Threshold VoltageTemp.Coefficient	I _D = 250 μA, Referenced to 25 °C		-2.4		mV/°C
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 4.5 V, I _D = 5.2 A T _J = 125 °C V _{GS} = 2.5 V, I _D = 4.5 A		0.033 0.051 0.043	0.042 0.07 0.054	Ω
I _{D(on)}	On-State Drain Current	V _{GS} = 4.5 V, V _{DS} = 5 V	11			A
g _{FS}	Forward Transconductance	V _{DS} = 10 V, I _D = 5.2 A		15		S
DYNAMIC CHARACTERISTICS						
C _{iss}	Input Capacitance	V _{DS} = 10 V, V _{GS} = 0 V, f = 1.0 MHz		538		pF
C _{oss}	Output Capacitance			226		pF
C _{rss}	Reverse Transfer Capacitance			51		pF
SWITCHING CHARACTERISTICS (Note 2)						
t _{D(on)}	Turn - On Delay Time	V _{DD} = 5 V, I _D = 1 A, V _{GS} = 4.5 V, R _{GEN} = 6 Ω		5	12	ns
t _r	Turn - On Rise Time			17	27	ns
t _{D(off)}	Turn - Off Delay Time			25	40	ns
t _f	Turn - Off Fall Time			5.3	11	ns
Q _g	Total Gate Charge	V _{DS} = 10 V, I _D = 5.2 A, V _{GS} = 4.5 V		11	16	nC
Q _{gs}	Gate-Source Charge			2		nC
Q _{gd}	Gate-Drain Charge			2.4		nC
DRAIN-SOURCE DIODE CHARACTERISTICS						
I _S	Continuous Source Diode Current				1.3	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 1.3 A (Note 2) T _J = 125°C		0.7 0.57	1.2 1	V

Notes:

1. $R_{\theta JA}$ is the sum of the junction-to-case and case-to-ambient thermal resistance where the case thermal reference is defined as the solder mounting surface of the drain pins. $R_{\theta JC}$ is guaranteed by design while $R_{\theta CA}$ is determined by the user's board design.

a. 78°C/W when mounted on a 1 in^2 pad of 2oz Cu on FR-4 board.

b. 156°C/W when mounted on a minimum pad of 2oz Cu on FR-4 board.

2. Pulse Test: Pulse Width $\leq 300\text{ }\mu\text{s}$, Duty Cycle $\leq 2.0\%$.

Typical Electrical Characteristics

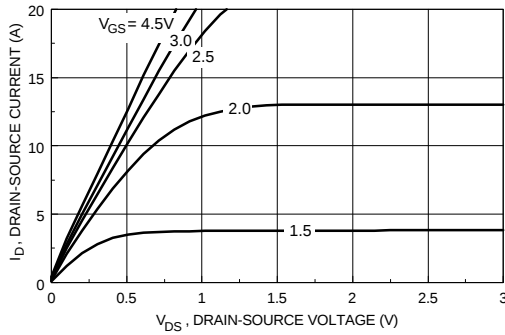


Figure 1. On-Region Characteristics.

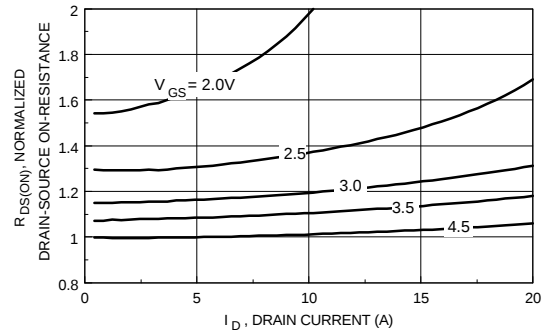


Figure 2. On-Resistance Variation with Drain Current and Gate Voltage.

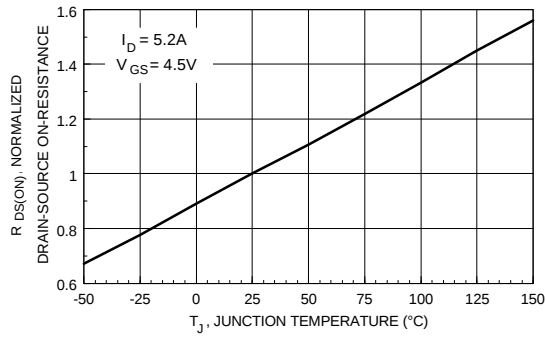


Figure 3. On-Resistance Variation with Temperature.

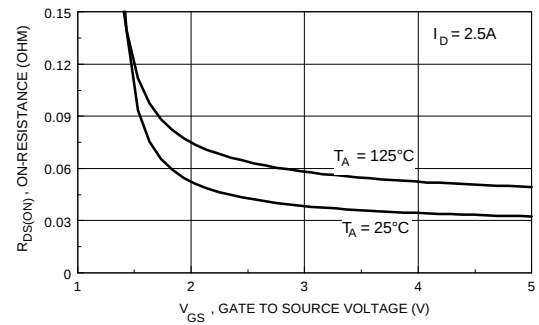


Figure 4. On-Resistance Variation with Gate-to-Source Voltage.

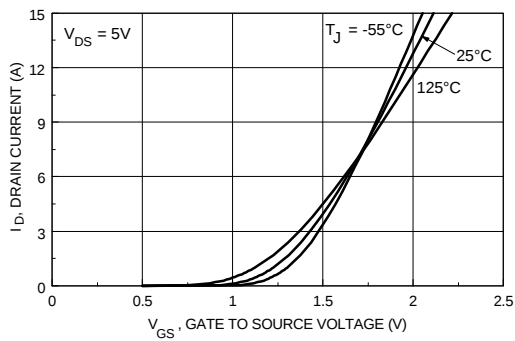


Figure 5. Transfer Characteristics.

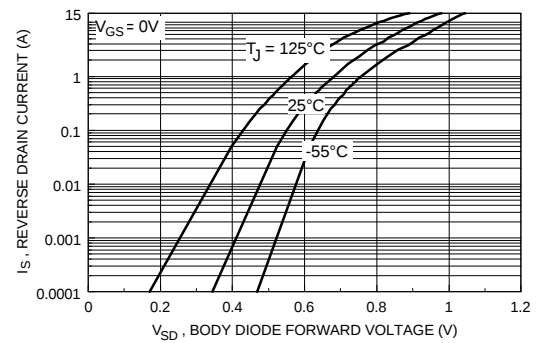


Figure 6. Body Diode Forward Voltage Variation with Source Current and Temperature.

Typical Electrical Characteristics (continued)

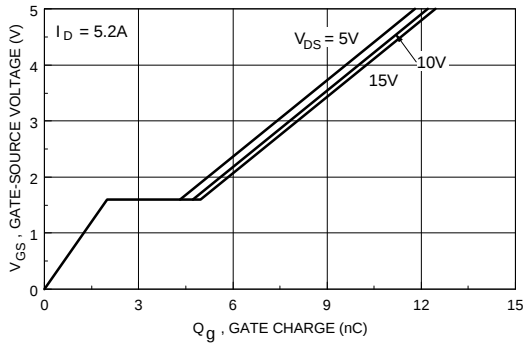


Figure 7. Gate Charge Characteristics.

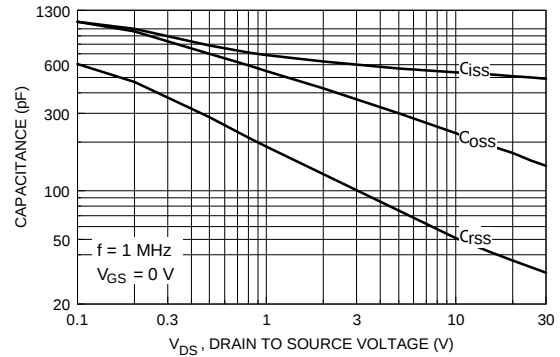


Figure 8. Capacitance Characteristics.

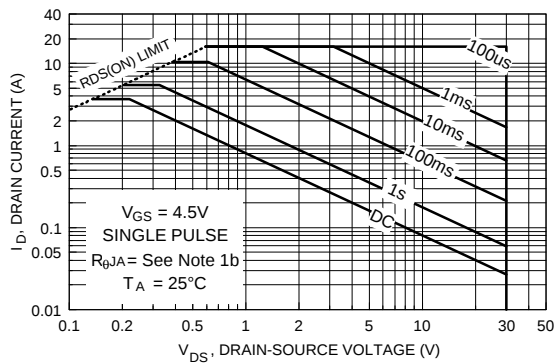


Figure 9. Maximum Safe Operating Area.

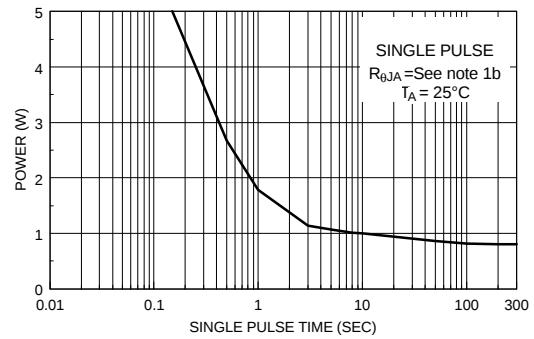


Figure 10. Single Pulse Maximum Power Dissipation.

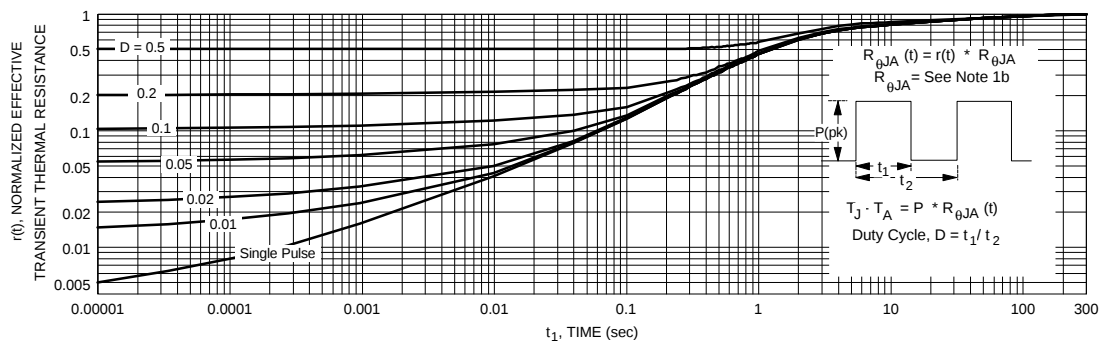


Figure 11. Transient Thermal Response Curve.

Thermal characterization performed using the conditions described in note 1b.
Transient thermal response will change depending on the circuit board design.

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