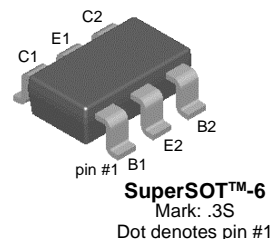


FMB5551

NPN General Purpose Amplifier SuperSOT-6 Surface Mount Package

- This device is designed for general purpose high voltage amplifiers and gas discharge display driving.
- Sourced from process 16.
- See MMBT5551 for characteristics.



Absolute Maximum Ratings $T_a=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Value	Units
V_{CEO}	Collector-Emitter Voltage	160	V
V_{CBO}	Collector-Base Voltage	180	V
V_{EBO}	Emitter-Base Voltage	6	V
I_C	Collector Current (DC)	600	mA
P_C	Collector Dissipation ($T_a=25^\circ\text{C}$) *	0.7	W
T_J	Junction Temperature	150	$^\circ\text{C}$
T_{STG}	Storage Temperature Range	- 55 ~ 150	$^\circ\text{C}$
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	180	$^\circ\text{C/W}$

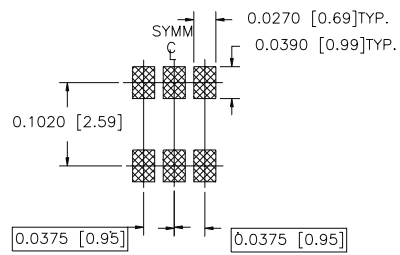
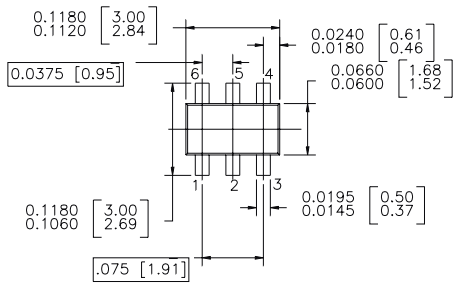
* Pd total, for both transistors. For each transistor, Pd = 350mW.

Electrical Characteristics $T_a=25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Typ.	Max.	Units
Off Characteristics						
BV_{CEO}	Collector-Emitter Voltage	$I_C = 1\text{mA}$	160			V
BV_{CBO}	Collector-Base Voltage	$I_C = 10\mu\text{A}$	180			V
BV_{EBO}	Emitter-Base Voltage	$I_E = 10\mu\text{A}$	6			V
I_{CBO}	Collector Cut-off Current	$V_{CB} = 120\text{V}$ $V_{CB} = 120\text{V}, T = 100^\circ\text{C}$			50 50	nA μA
I_{EBO}	Emitter Cut-off Current	$V_{EB} = 4\text{V}$			50	nA
On Characteristics						
h_{FE}	DC Current Gain	$V_{CE} = 5\text{V}, I_C = 1\text{mA}$ $V_{CE} = 5\text{V}, I_C = 10\text{mA}$ $V_{CE} = 5\text{V}, I_C = 50\text{mA}$	80 80 30		250	
$V_{CE(sat)}$	Collector-Emitter Saturation Voltage	$I_C = 10\text{mA}, I_B = 1\text{mA}$ $I_C = 50\text{mA}, I_B = 5\text{mA}$			0.15 0.2	V
$V_{BE(sat)}$	Base-Emitter Saturation Voltage	$I_C = 10\text{mA}, I_B = 1\text{mA}$ $I_C = 50\text{mA}, I_B = 5\text{mA}$			1 1	V
Small Signal Characteristics						
TYPICAL						
C_{ob}	Output Capacitance	$V_{CB} = 10\text{V}, f = 1\text{MHz}$			6	pF
C_{ib}	Input Capacitance	$V_{CB} = 0.5\text{V}, f = 1\text{MHz}$			20	pF
f_T	Current gain Bandwidth Product	$V_{CE} = 10\text{V}, I_C = 10\text{mA}$ $f = 100\text{MHz}$	100		300	MHz
NF	Noise Figure	$V_{CE} = 5\text{V}, I_C = 200\mu\text{A}$ $f = 1\text{MHz}, R_S = 2\text{k}\Omega, B = 200\text{Hz}$			8	dB
h_{FE}	Small Signal Current Gain	$V_{CE} = 10\text{V}, I_C = 1\text{mA}$ $f = 1\text{KHz}$	50		250	

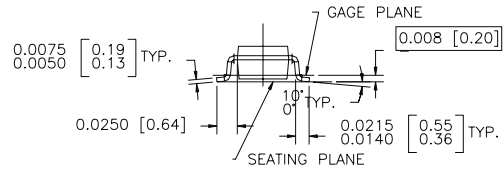
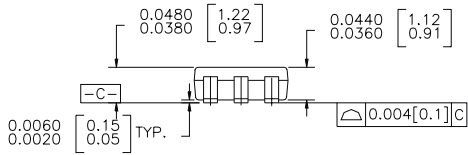
Package Dimensions

SuperSOT™-6



LAND PATTERN RECOMMENDATION

CONTROLLING DIMENSION IS INCH
VALUES IN [] ARE MILLIMETERS



SUPER SOT 6 LEADS

- NOTES : UNLESS OTHERWISE SPECIFIED
- 1.0 STANDARD LEAD FINISH : 150 MICROINCHES 93.81 MICROMETERS)
MINIMUM TIN / LEAD (SOLDER) ON COPPER.
 - 2.0 NO JEDEC REGISTRATION AS OF JULY 1996

Dimensions in Millimeters

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACE _x [™]	FAST [®]	ISOPLANAR [™]	Power247 [™]	SuperFET [™]
ActiveArray [™]	FAST _r [™]	LittleFET [™]	PowerSaver [™]	SuperSOT [™] -3
Bottomless [™]	FPS [™]	MICROCOUPLER [™]	PowerTrench [®]	SuperSOT [™] -6
CoolFET [™]	FRFET [™]	MicroFET [™]	QFET [®]	SuperSOT [™] -8
CROSSVOLT [™]	GlobalOptoisolator [™]	MicroPak [™]	QS [™]	SyncFET [™]
DOME [™]	GTO [™]	MICROWIRE [™]	QT Optoelectronics [™]	TinyLogic [®]
EcoSPARK [™]	HiSeC [™]	MSX [™]	Quiet Series [™]	TINYOPTO [™]
E ² CMOS [™]	I ² C [™]	MSXPro [™]	RapidConfigure [™]	TruTranslation [™]
EnSigna [™]	<i>i-Lo</i> [™]	OCX [™]	RapidConnect [™]	UHC [™]
FACT [™]	ImpliedDisconnect [™]	OCXPro [™]	μSerDes [™]	UltraFET [®]
FACT Quiet Series [™]		OPTOLOGIC [®]	SILENT SWITCHER [®]	VCX [™]
Across the board. Around the world. [™]		OPTOPLANAR [™]	SMART START [™]	
The Power Franchise [®]		PACMAN [™]	SPM [™]	
Programmable Active Droop [™]		POP [™]	Stealth [™]	

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION.

As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.