

KA5x03xx-SERIES

KA5H0365R, KA5M0365R, KA5L0365R

KA5H0380R, KA5M0380R, KA5L0380R

Fairchild Power Switch(FPS)

Features

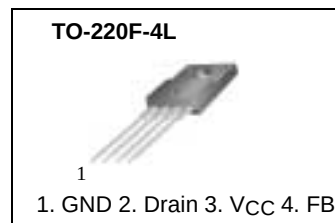
- Precision fixed operating frequency (100/67/50kHz)
- Low start-up current(typ. 100uA)
- Pulse by pulse current limiting
- Over current protection
- Over voltage protection (Min. 25V)
- Internal thermal shutdown function
- Under voltage lockout
- Internal high voltage sense FET
- Auto-restart mode

Applications

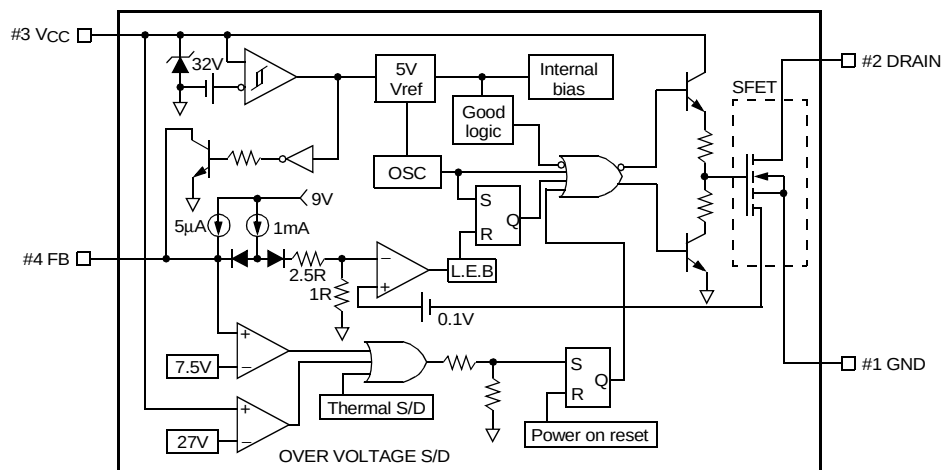
- SMPS for VCR, SVR, STB, DVD & DVCD
- SMPS for Printer, Facsimile & Scanner
- Adaptor for Camcorder

Description

The Fairchild Power Switch(FPS) product family is specially designed for an off-line SMPS with minimal external components. The Fairchild Power Switch(FPS) consist of high voltage power SenseFET and current mode PWM IC. Included PWM controller features integrated fixed frequency oscillator, under voltage lock-out, leading edge blanking, optimized gate turn-on/turn-off driver, thermal shutdown protection, over voltage protection, and temperature compensated precision current sources for loopcompensation and fault protection circuitry. compared to discrete MOSFET and PWM controller or RCC solution, a Fairchild Power Switch(FPS) can reduce total component count, design size, weight and at the same time increase efficiency, productivity, and system reliability. It has a basic platform well suited for cost effective design in either a flyback converter or a forward converter.



Internal Block Diagram



Absolute Maximum Ratings

(Ta=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
KA5x0365R			
Maximum Drain Voltage	V _{D,MAX}	650	V
Drain-Gate Voltage (R _{GS} =1MΩ)	V _{DGR}	650	V
Gate-Source (GND) Voltage	V _{GS}	±30	V
Drain Current Pulsed ⁽¹⁾	I _{DM}	12.0	ADC
Continuous Drain Current (T _C =25°C)	I _D	3.0	ADC
Continuous Drain Current (T _C =100°C)	I _D	2.4	ADC
Single Pulsed Avalanche Energy ⁽²⁾	E _{AS}	358	mJ
Maximum Supply Voltage	V _{CC,MAX}	30	V
Analog Input Voltage Range	V _{FB}	−0.3 to V _{SD}	V
Total Power Dissipation	P _D	75	W
	Derating	0.6	W/°C
Operating Junction Temperature.	T _J	+160	°C
Operating Ambient Temperature.	T _A	−25 to +85	°C
Storage Temperature Range.	T _{STG}	−55 to +150	°C
KA5x0380R			
Maximum Drain Voltage	V _{D,MAX}	800	V
Drain-Gate Voltage (R _{GS} =1MΩ)	V _{DGR}	800	V
Gate-Source (GND) Voltage	V _{GS}	±30	V
Drain Current Pulsed ⁽¹⁾	I _{DM}	12.0	ADC
Continuous Drain Current (T _C =25°C)	I _D	3.0	ADC
Continuous Drain Current (T _C =100°C)	I _D	2.1	ADC
Single Pulsed Avalanche Energy ⁽²⁾	E _{AS}	95	mJ
Maximum Supply Voltage	V _{CC,MAX}	30	V
Analog Input Voltage Range	V _{FB}	−0.3 to V _{SD}	V
Total Power Dissipation	P _D	75	W
	Derating	0.6	W/°C
Operating Junction Temperature.	T _J	+160	°C
Operating Ambient Temperature.	T _A	−25 to +85	°C
Storage Temperature Range.	T _{STG}	−55 to +150	°C

Note:

1. Repetitive rating: Pulse width limited by maximum junction temperature
2. L=51mH, starting T_J=25°C
3. L=13μH, starting T_J=25°C

Electrical Characteristics (SFET part)

(Ta = 25°C unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
KA5x0365R						
Drain-source breakdown voltage	BVDSS	VGS=0V, ID=50μA	650	-	-	V
Zero gate voltage drain current	IDSS	VDS=Max. Rating, VGS=0V	-	-	50	μA
		VDS=0.8Max. Rating, VGS=0V, TC=125°C	-	-	200	μA
Static drain-source on resistance ^(note)	RDS(ON)	VGS=10V, ID=0.5A	-	3.6	4.5	Ω
Forward transconductance ^(note)	gfs	VDS=50V, ID=0.5A	2.0	-	-	S
Input capacitance	Ciss	VGS=0V, VDS=25V, f=1MHz	-	720	-	pF
Output capacitance	Coss		-	40	-	
Reverse transfer capacitance	Crss		-	40	-	
Turn on delay time	td(on)	VDD=0.5BVDSS, ID=1.0A (MOSFET switching time are essentially independent of operating temperature)	-	150	-	nS
Rise time	tr		-	100	-	
Turn off delay time	td(off)		-	150	-	
Fall time	tf		-	42	-	
Total gate charge (gate-source+gate-drain)	Qg	VGS=10V, ID=1.0A, VDS=0.5BVDSS (MOSFET switching time are essentially independent of operating temperature)	-	-	34	nC
Gate-source charge	Qgs		-	7.3	-	
Gate-drain (Miller) charge	Qgd		-	13.3	-	
KA5x0380R						
Drain-source breakdown voltage	BVDSS	VGS=0V, ID=50μA	800	-	-	V
Zero gate voltage drain current	IDSS	VDS=Max. Rating, VGS=0V	-	-	250	μA
		VDS=0.8Max. Rating, VGS=0V, TC=125°C	-	-	1000	μA
Static drain-source on resistance ^(note)	RDS(ON)	VGS=10V, ID=0.5A	-	4.0	5.0	Ω
Forward transconductance ^(note)	gfs	VDS=50V, ID=0.5A	1.5	2.5	-	S
Input capacitance	Ciss	VGS=0V, VDS=25V, f=1MHz	-	779	-	pF
Output capacitance	Coss		-	75.6	-	
Reverse transfer capacitance	Crss		-	24.9	-	
Turn on delay time	td(on)	VDD=0.5BVDSS, ID=1.0A (MOSFET switching time are essentially independent of operating temperature)	-	40	-	nS
Rise time	tr		-	95	-	
Turn off delay time	td(off)		-	150	-	
Fall time	tf		-	60	-	
Total gate charge (gate-source+gate-drain)	Qg	VGS=10V, ID=1.0A, VDS=0.5BVDSS (MOSFET switching time are essentially independent of operating temperature)	-	-	34	nC
Gate-source charge	Qgs		-	7.2	-	
Gate-drain (Miller) charge	Qgd		-	12.1	-	

Note:

Pulse test: Pulse width ≤ 300μS, duty ≤ 2%

$$S = \frac{1}{R}$$

Electrical Characteristics (CONTROL part)

(Ta = 25°C unless otherwise specified)

Characteristic	Symbol	Test condition	Min.	Typ.	Max.	Unit
UVLO SECTION						
Start Threshold Voltage	VSTART	VFB=GND	14	15	16	V
Stop Threshold Voltage	VSTOP	VFB=GND	8.4	9	9.6	V
OSCILLATOR SECTION						
Initial accuracy	FOSC	KA5H03xxR	90	100	110	kHz
Initial accuracy	FOSC	KA5M03xxR	61	67	73	kHz
Initial accuracy	FOSC	KA5L03xxR	45	50	55	kHz
Frequency change with temperature ⁽²⁾	-	-25°C≤Ta≤+85°C	-	±5	±10	%
Maximum duty cycle	Dmax	KA5H03xxR	62	67	72	%
Maximum duty cycle	Dmax	KA5M03xxR KA5L03xxR	72	77	82	%
FEEDBACK SECTION						
Feedback source current	IFB	Ta=25°C, 0V≤Vfb≤3V	0.7	0.9	1.1	mA
Shutdown Feedback voltage	VSD	Vfb≥6.5V	6.9	7.5	8.1	V
Shutdown delay current	Idelay	Ta=25°C, 5V≤Vfb≤VSD	4	5	6	μA
REFERENCE SECTION						
Output voltage ⁽¹⁾	Vref	Ta=25°C	4.80	5.00	5.20	V
Temperature Stability ⁽¹⁾⁽²⁾	Vref/ΔT	-25°C≤Ta≤+85°C	-	0.3	0.6	mV/°C
CURRENT LIMIT(SELF-PROTECTION)SECTION						
Peak Current Limit	IOVER	Max. inductor current	1.89	2.15	2.41	A
PROTECTION SECTION						
Over voltage protection	VOVP	VCC≥24V	25	27	29	V
Thermal shutdown temperature (Tj) ⁽¹⁾	TSD	-	140	160	-	°C
TOTAL STANDBY CURRENT SECTION						
Start Up Current	ISTART	VCC=14V	-	100	170	μA
Operating supply current (control part only)	IOP	VCC≤28	-	7	12	mA

Note:

1. These parameters, although guaranteed, are not 100% tested in production
2. These parameters, although guaranteed, are tested in EDS(water test) process

Typical Performance Characteristics(SFET part)

(KA5X0365R)

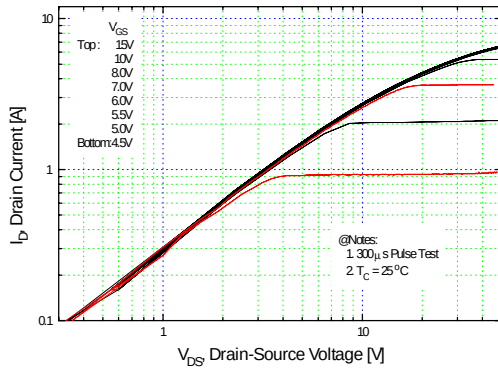


Figure 1. Output Characteristics

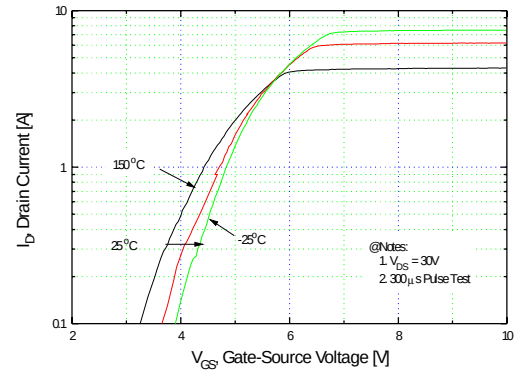


Figure 2. Transfer Characteristics

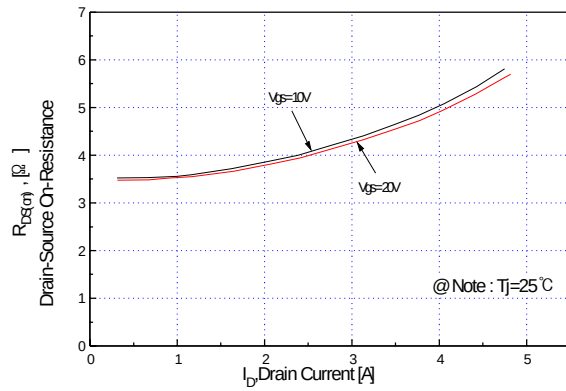


Figure 3. On-Resistance vs. Drain Current

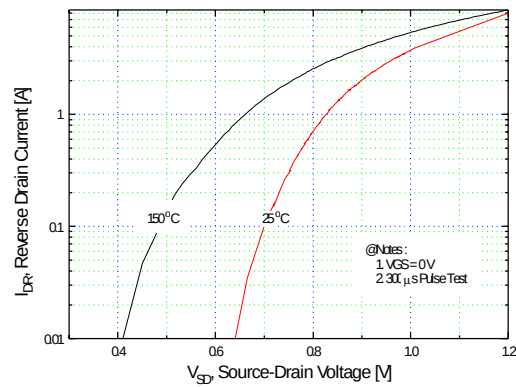


Figure 4. Source-Drain Diode Forward Voltage

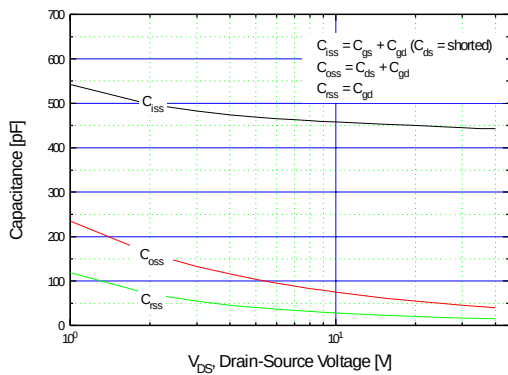


Figure 5. Capacitance vs. Drain-Source Voltage

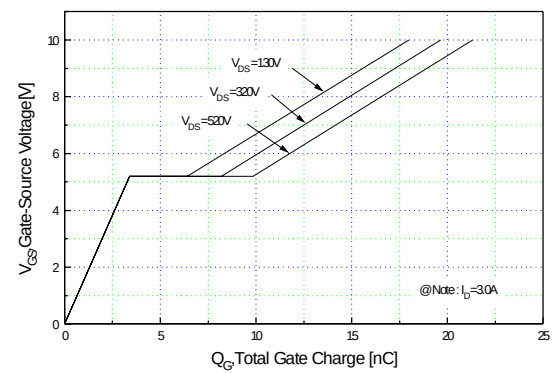


Figure 5. Gate Charge vs. Gate-Source Voltage

Typical Performance Characteristics (Continued)

KA5X0365R

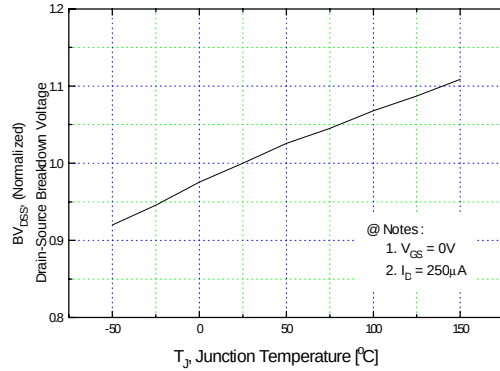


Figure 7. Breakdown Voltage vs. Temperature

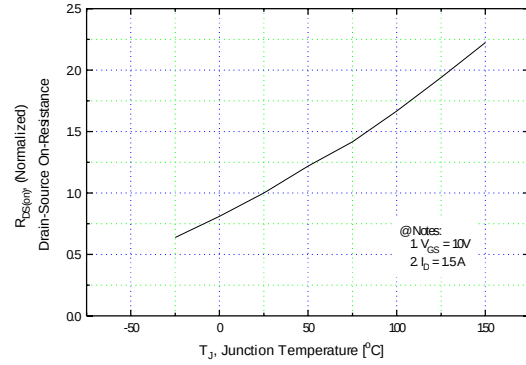


Figure 8. On-Resistance vs. Temperature

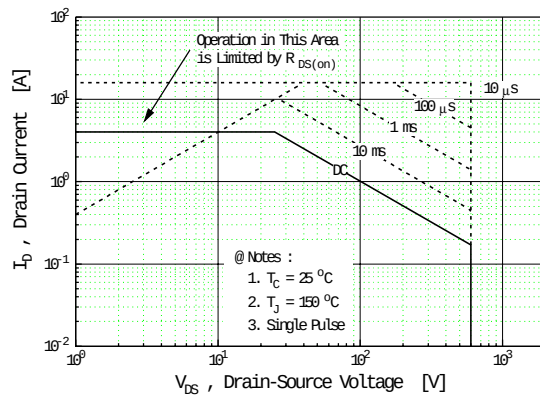


Figure 9. Max. Safe Operating Area

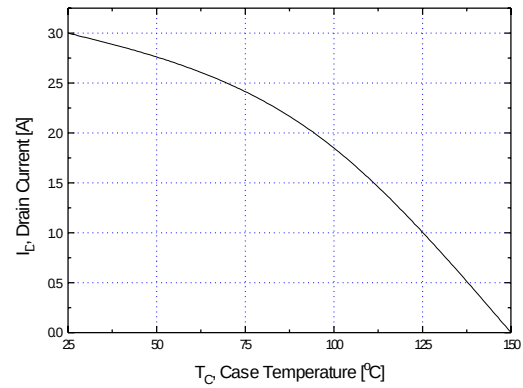


Figure 10. Max. Drain Current vs. Case Temperature

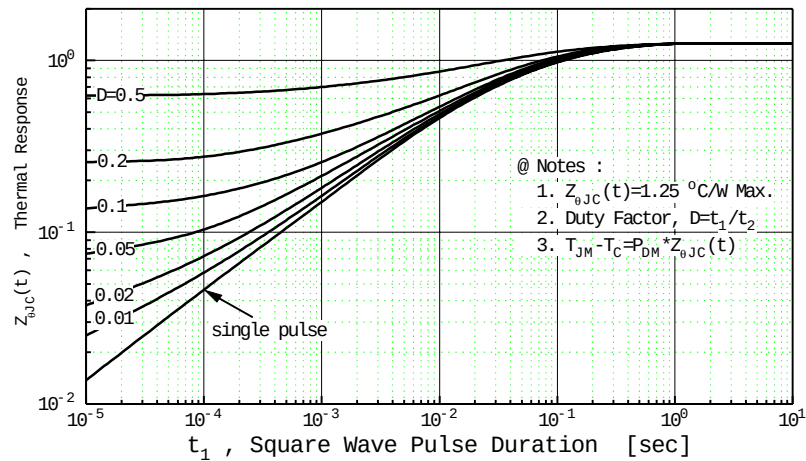


Figure 11. Thermal Response

Typical Performance Characteristics

(KA5X0380R)

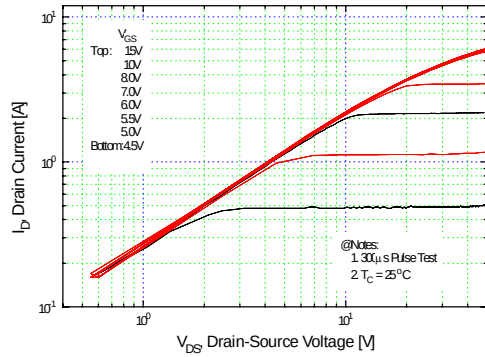


Figure 1. Output Characteristics

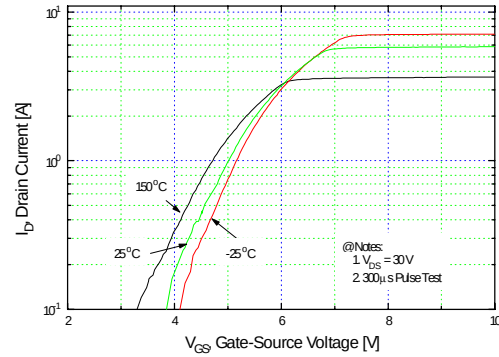


Figure 2. Transfer Characteristics

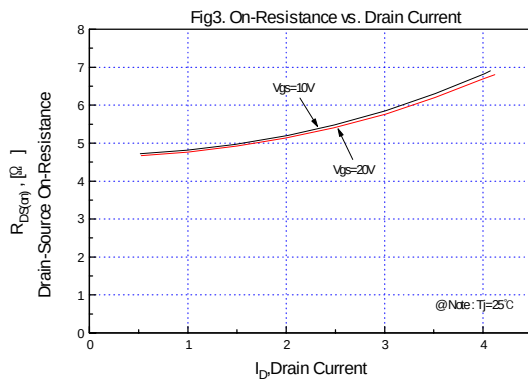


Figure 3. On-Resistance vs. Drain Current

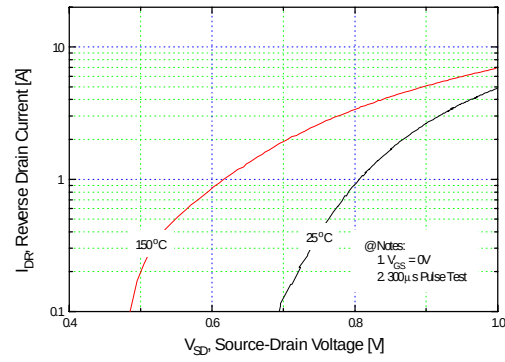


Figure 4. Source-Drain Diode Forward Voltage

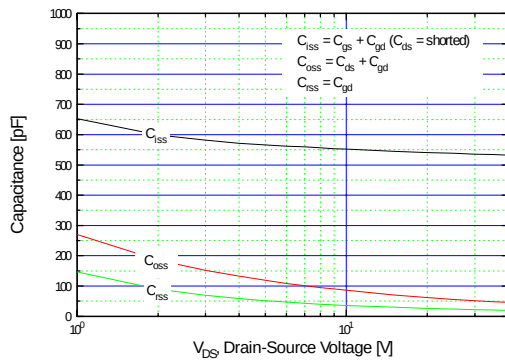


Figure 5. Capacitance vs. Drain-Source Voltage

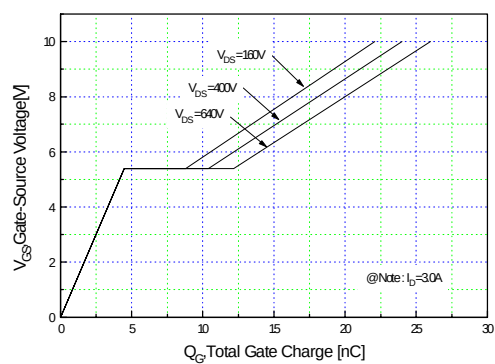


Figure 6. Gate Charge vs. Gate-Source Voltage

Typical Performance Characteristics (Continued)

(KA5X0380R)

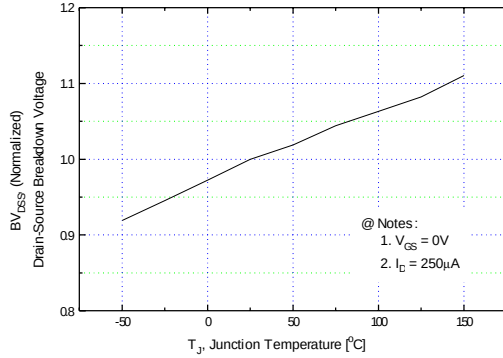


Figure 7. Breakdown Voltage vs. Temperature

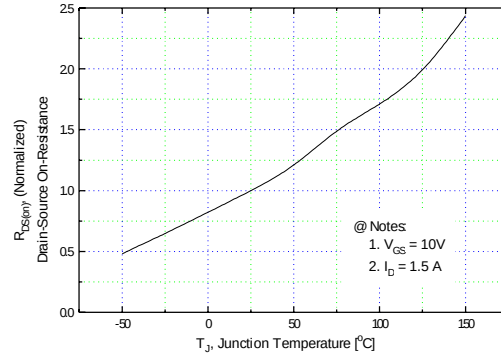


Figure 8. On-Resistance vs. Temperature

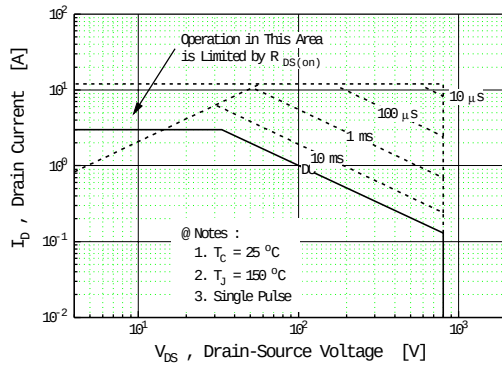


Figure 9. Max. Safe Operating Area

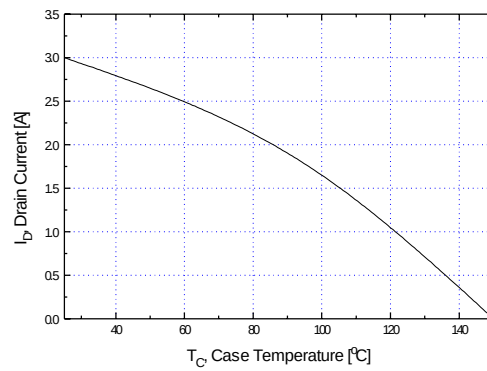


Figure 10. Max. Drain Current vs. Case Temperature

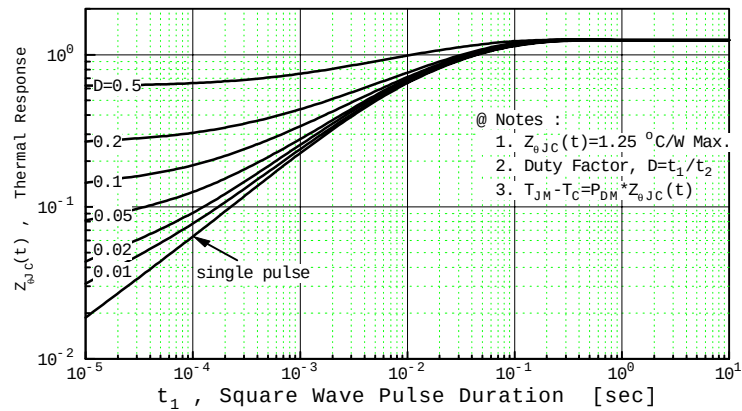


Figure 11. Thermal Response

Typical Performance Characteristics (Control Part)

(These characteristic graphs are normalized at $T_a = 25^\circ\text{C}$)

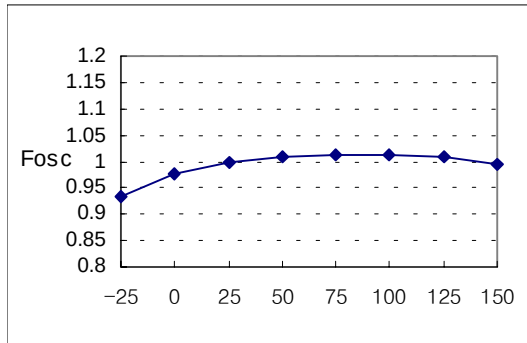


Figure 1. Operating Frequency

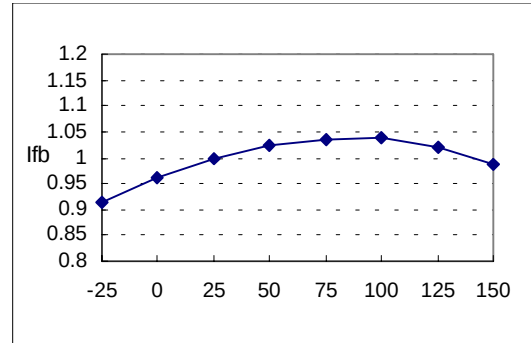


Figure 2. Feedback Source Current

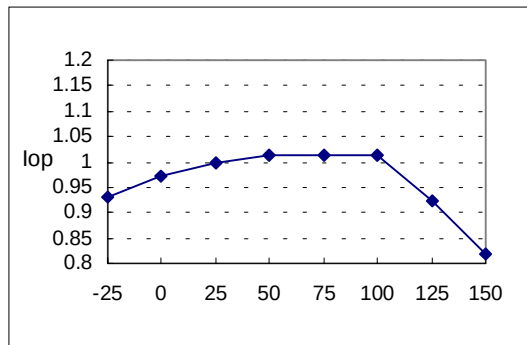


Figure 3. Operating Supply Current

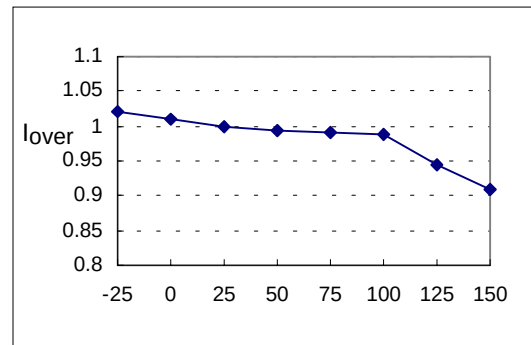


Figure 4. Peak Current Limit

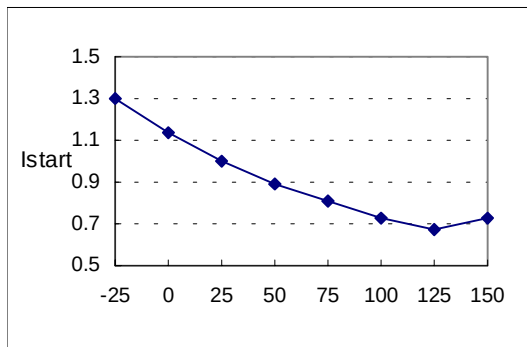


Figure 5. Start up Current

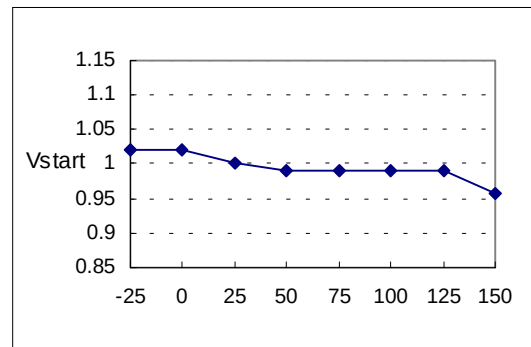


Figure 6. Start Threshold Voltage

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at Ta = 25°C)

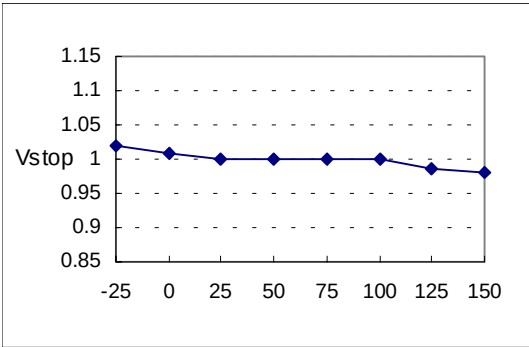


Figure 7. Stop Threshold Voltage

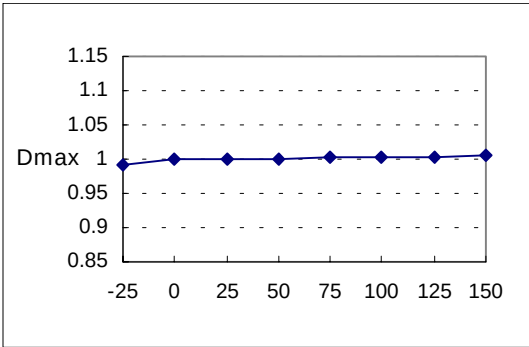


Figure 8. Maximum Duty Cycle

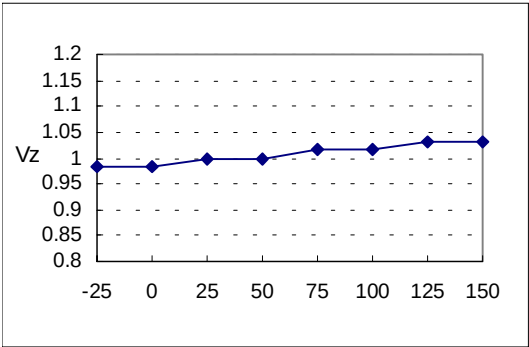


Figure 9. VCC Zener Voltage

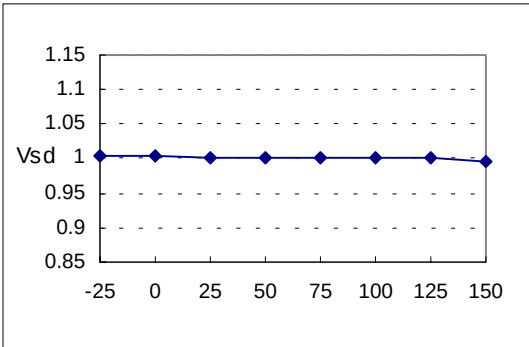


Figure 10. Shutdown Feedback Voltage

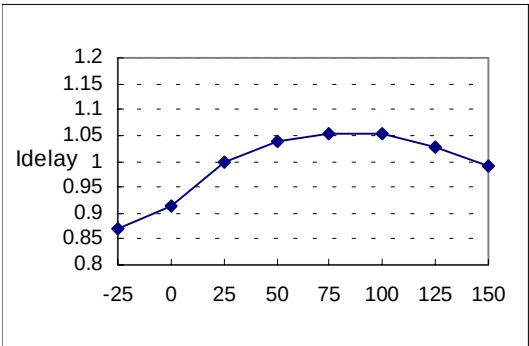


Figure 11. Shutdown Delay Current

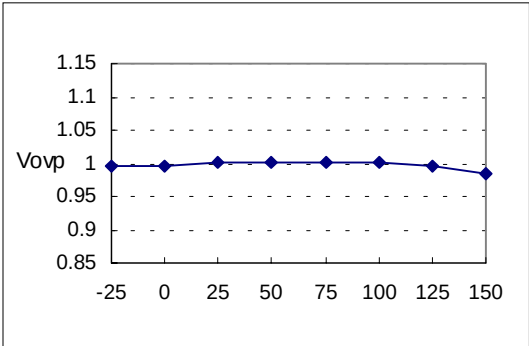


Figure 12. Over Voltage Protection

Typical Performance Characteristics (Continued)

(These characteristic graphs are normalized at $T_a = 25^\circ\text{C}$)

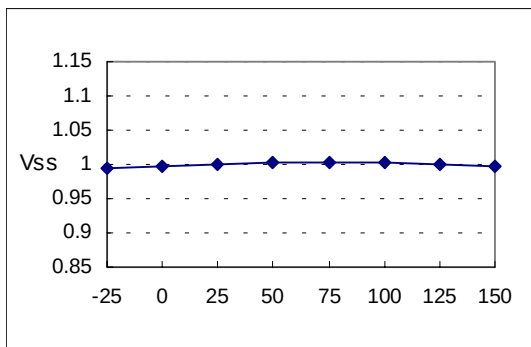


Figure13. Soft Start Voltage

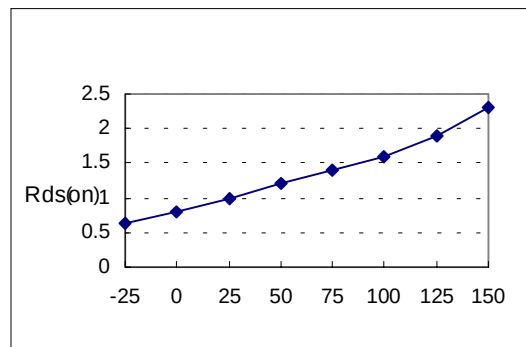
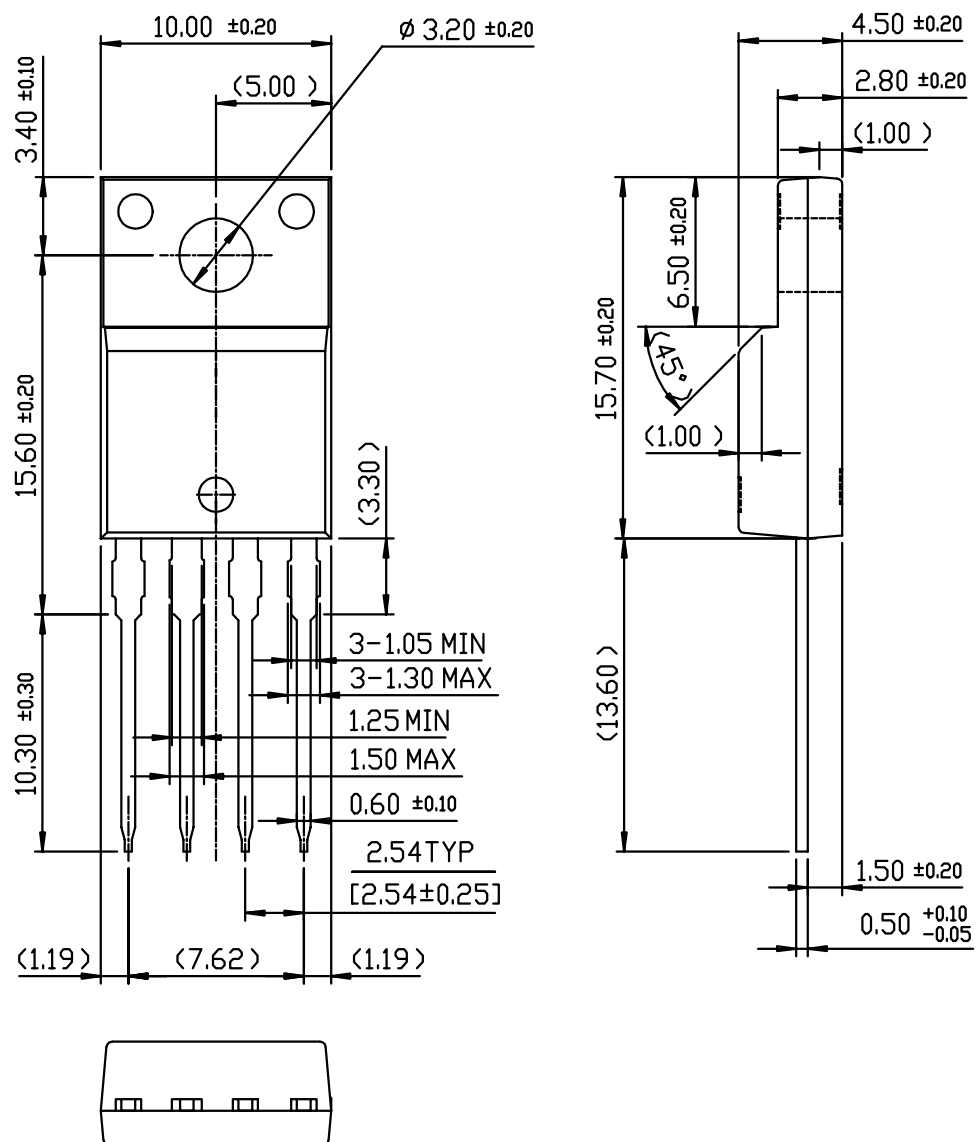


Figure 14. Static Drain-Source on Resistance

Package Dimensions

TO-220F-4L



Technical drawing of the 254TYP connector, showing dimensions in millimeters (mm). The drawing includes a front view, a side view, and a top view.

Front View Dimensions:

- Overall width: 10.00 ± 0.20
- Top section width: 3.40 ± 0.10
- Top section hole diameter: $\phi 3.20 \pm 0.20$
- Top section hole offset: (5.00)
- Bottom section height: 15.60 ± 0.20
- Bottom section hole diameter: $\phi 3.30$
- Bottom section hole offset: (1.80)
- Pin height: 5.00 ± 0.30
- Pin diameter: $3-1.05 \text{ MIN}$ to $3-1.30 \text{ MAX}$
- Pin pitch: 1.25 MIN to 1.50 MAX
- Pin width: 0.60 ± 0.10
- Pin positions: #1, #2, #3, #4
- Overall height: 26.00 ± 0.80
- Top flange width: 4.50 ± 0.20
- Top flange hole diameter: 2.80 ± 0.20
- Top flange hole offset: (1.00)
- Top flange thickness: 6.50 ± 0.20
- Top flange hole diameter: $\phi 4.50$
- Top flange hole offset: (1.00)
- Top flange hole diameter: $\phi 3.50$
- Top flange hole offset: (1.00)
- Top flange hole diameter: $\phi 3.18 \pm 0.30$
- Top flange hole offset: (1.19)
- Top flange hole diameter: $\phi 1.50 \pm 0.10$
- Top flange hole offset: (7.62)
- Top flange hole diameter: $\phi 0.50^{+0.10}_{-0.05}$

Side View Dimensions:

- Overall width: 10.00 ± 0.20
- Top section width: 3.40 ± 0.10
- Top section hole diameter: $\phi 3.20 \pm 0.20$
- Top section hole offset: (5.00)
- Bottom section height: 15.60 ± 0.20
- Bottom section hole diameter: $\phi 3.30$
- Bottom section hole offset: (1.80)
- Pin height: 5.00 ± 0.30
- Pin diameter: $3-1.05 \text{ MIN}$ to $3-1.30 \text{ MAX}$
- Pin pitch: 1.25 MIN to 1.50 MAX
- Pin width: 0.60 ± 0.10
- Pin positions: #1, #2, #3, #4
- Overall height: 26.00 ± 0.80
- Top flange width: 4.50 ± 0.20
- Top flange hole diameter: 2.80 ± 0.20
- Top flange hole offset: (1.00)
- Top flange thickness: 6.50 ± 0.20
- Top flange hole diameter: $\phi 4.50$
- Top flange hole offset: (1.00)
- Top flange hole diameter: $\phi 3.50$
- Top flange hole offset: (1.00)
- Top flange hole diameter: $\phi 3.18 \pm 0.30$
- Top flange hole offset: (1.19)
- Top flange hole diameter: $\phi 1.50 \pm 0.10$
- Top flange hole offset: (7.62)
- Top flange hole diameter: $\phi 0.50^{+0.10}_{-0.05}$

Top View Dimensions:

- Overall width: 10.00 ± 0.20
- Top section width: 3.40 ± 0.10
- Top section hole diameter: $\phi 3.20 \pm 0.20$
- Top section hole offset: (5.00)
- Bottom section height: 15.60 ± 0.20
- Bottom section hole diameter: $\phi 3.30$
- Bottom section hole offset: (1.80)
- Pin height: 5.00 ± 0.30
- Pin diameter: $3-1.05 \text{ MIN}$ to $3-1.30 \text{ MAX}$
- Pin pitch: 1.25 MIN to 1.50 MAX
- Pin width: 0.60 ± 0.10
- Pin positions: #1, #2, #3, #4
- Overall height: 26.00 ± 0.80
- Top flange width: 4.50 ± 0.20
- Top flange hole diameter: 2.80 ± 0.20
- Top flange hole offset: (1.00)
- Top flange thickness: 6.50 ± 0.20
- Top flange hole diameter: $\phi 4.50$
- Top flange hole offset: (1.00)
- Top flange hole diameter: $\phi 3.50$
- Top flange hole offset: (1.00)
- Top flange hole diameter: $\phi 3.18 \pm 0.30$
- Top flange hole offset: (1.19)
- Top flange hole diameter: $\phi 1.50 \pm 0.10$
- Top flange hole offset: (7.62)
- Top flange hole diameter: $\phi 0.50^{+0.10}_{-0.05}$

Ordering Information

Product Number	Package	Marking Code	BVDSS	FOSC	RDS(on)
KA5H0365R-TU	TO-220F-4L	5H0365R	650V	100kHz	3.6Ω
KA5H0365R-YDTU	TO-220F-4L(Forming)				
KA5M0365R-TU	TO-220F-4L	5M0365R	650V	67kHz	3.6Ω
KA5M0365R-YDTU	TO-220F-4L(Forming)				
KA5L0365R-TU	TO-220F-4L	5L0365R	650V	50kHz	3.6Ω
KA5L0365R-YDTU	TO-220F-4L(Forming)				
Product Number	Package	Marking Code	BVDSS	FOSC	RDS(on)
KA5H0380R-TU	TO-220F-4L	5H0380R	800V	100kHz	4.6Ω
KA5H0380R-YDTU	TO-220F-4L(Forming)				
KA5M0380R-TU	TO-220F-4L	5M0380R	800V	67kHz	4.6Ω
KA5M0380R-YDTU	TO-220F-4L(Forming)				
KA5L0380R-TU	TO-220F-4L	5L0380R	800V	50kHz	4.6Ω
KA5L0380R-YDTU	TO-220F-4L(Forming)				

TU :Non Forming Type

YDTU : Forming type

DISCLAIMER

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1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.