

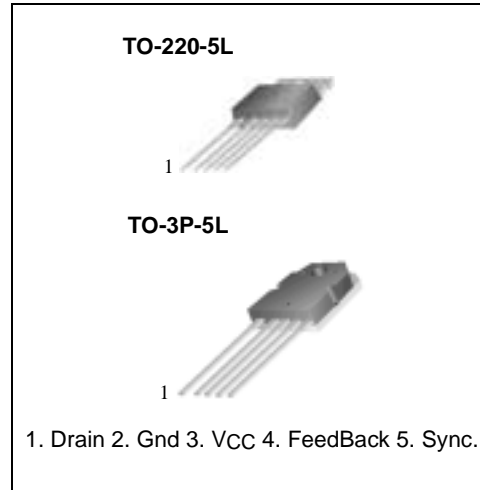
# KA5S-SERIES

KA5S0765C/KA5S0965/KA5S12656/KA5S1265

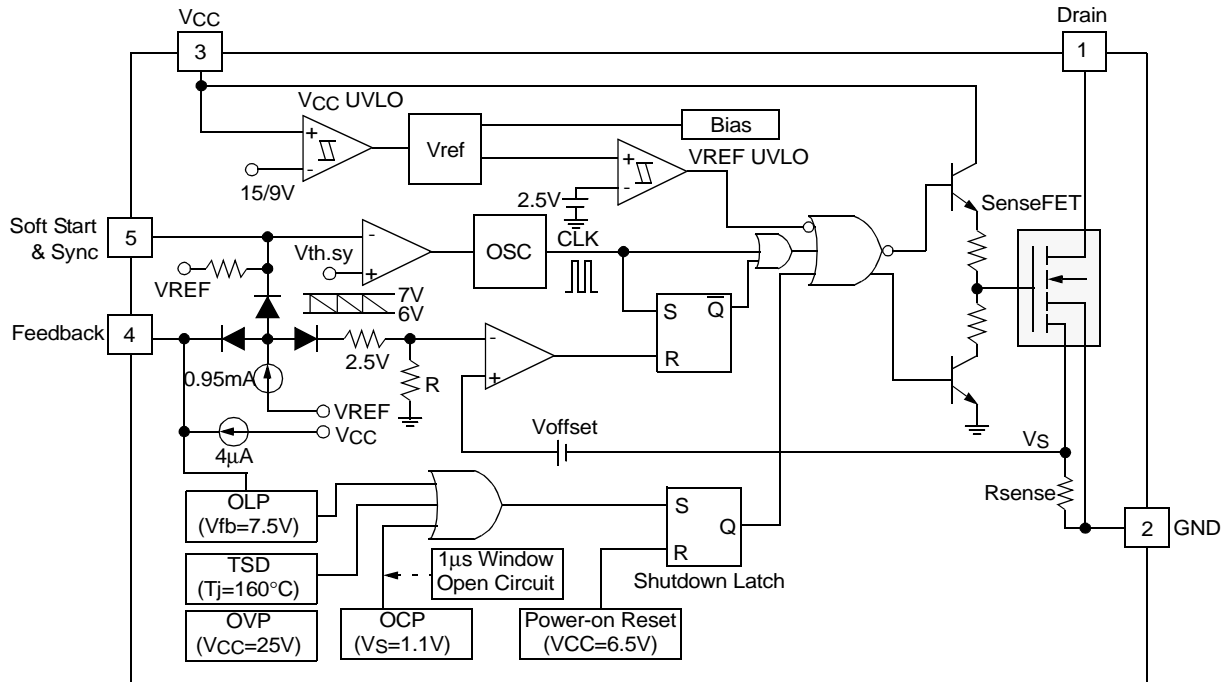
Fairchild Power Switch(FPS)

## Features

- Wide Operating Frequency Range Up to 150kHz
- Lowest Cost SMPS Solution
- Lowest External Components
- Low Start-up Current (Max:170 $\mu$ A)
- Low Operating Current (Max:12mA)
- Internal High Voltage SenseFET
- Over Voltage Protection With Latch Mode (Min23V)
- Over Load Protection With Latch Mode
- Over Current Protection With Latch Mode
- Internal Thermal Protection With Latch Mode
- Pulse By Pulse Over Current Limiting
- Under Voltage Lockout With Hysteresis
- External Sync. Terminal



## Internal Block Diagram



## Absolute Maximum Ratings

(Ta=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
<b>KA5S0765C</b>			
Drain-Gate Voltage(RGS=1MΩ)	VDGR	650	V
Gate-Source(GND) Voltage	VGS	±30	V
Drain Current Pulsed <sup>(1)</sup>	IDM	28	ADC
Continuous Drain Current (Tc = 25°C)	ID	7.0	ADC
Continuous Drain Current (Tc = 100°C)	ID	5.6	ADC
Single Pulsed Avalanch Current <sup>(3)</sup> (Energy <sup>(2)</sup> )	IAS(EAS)	27(570)	A(mJ)
Maximum Supply Voltage	VCC,MAX	30	V
Input Voltage Range	VFB	-0.3 to VCC	V
	VSS	-0.3 to 8	V
Total Power Dissipation	PD (Watt H/S)	140	W
	Darting	1.11	W/°C
Operating Junction Temperature.	TJ	+160	°C
Operating Ambient Temperature.	TA	-25 to +85	°C
Storage Temperature Range.	TSTG	-55 to +150	°C
<b>KA5S0965</b>			
Drain-Gate Voltage(RGS=1MΩ)	VDGR	650	V
Gate-Source(GND) Voltage	VGS	±30	V
Drain Current Pulsed <sup>(1)</sup>	IDM	36	ADC
Continuous Drain Current (Tc = 25°C)	ID	9.0	ADC
Continuous Drain Current (Tc = 100°C)	ID	5.8	ADC
Single Pulsed Avalanch Current <sup>(3)</sup> (Energy <sup>(2)</sup> )	IAS(EAS)	25(950)	A(mJ)
Maximum Supply Voltage	VCC,MAX	30	V
Input Voltage Range	VFB	-0.3 to VCC	V
	VSS	-0.3 to 8	V
Total Power Dissipation	PD (Watt H/S)	170	W
	Darting	1.33	W/°C
Operating Junction Temperature.	TJ	+160	°C
Operating Ambient Temperature.	TA	-25 to +85	°C
Storage Temperature Range.	TSTG	-55 to +150	°C

**Absolute Maximum Ratings** (Continued)

(Ta=25°C, unless otherwise specified)

Characteristic	Symbol	Value	Unit
<b>KA5S12656</b>			
Drain-Gate Voltage(RGS=1MΩ)	VDGR	650	V
Gate-Source(GND) Voltage	VGS	±30	V
Drain Current Pulsed <sup>(1)</sup>	IDM	48	ADC
Continuous Drain Current (Tc = 25°C)	ID	12	ADC
Continuous Drain Current (Tc = 100°C)	ID	8.4	ADC
Single Pulsed Avalanch Current <sup>(3)</sup> (Energy <sup>(2)</sup> )	IAS(EAS)	25(785)	A(mJ)
Maximum Supply Voltage	VCC,MAX	30	V
Input Voltage Range	VFB	-0.3 to VCC	V
	VSS	-0.3 to 8	V
Total Power Dissipation	PD (Watt H/S)	160	W
	Darting	1.28	W/°C
Operating Junction Temperature.	TJ	+160	°C
Operating Ambient Temperature.	TA	-25 to +85	°C
Storage Temperature Range.	TSTG	-55 to +150	°C
<b>KA5S1265</b>			
Drain-Gate Voltage(RGS=1MΩ)	VDGR	650	V
Gate-Source(GND) Voltage	VGS	±30	V
Drain Current Pulsed <sup>(1)</sup>	IDM	48	ADC
Continuous Drain Current (Tc = 25°C)	ID	12	ADC
Continuous Drain Current (Tc = 100°C)	ID	8.4	ADC
Single Pulsed Avalanch Current <sup>(3)</sup> (Energy <sup>(2)</sup> )	IAS(EAS)	42(785)	A(mJ)
Maximum Supply Voltage	VCC,MAX	30	V
Input Voltage Range	VFB	-0.3 to VCC	V
	VSS	-0.3 to 8	V
Total Power Dissipation	PD (Watt H/S)	160	W
	Darting	1.28	W/°C
Operating Junction Temperature.	TJ	+160	°C
Operating Ambient Temperature.	TA	-25 to +85	°C
Storage Temperature Range.	TSTG	-55 to +150	°C

**Note:**

1. Repetitive rating : Pulse width limited by maximum junction temperature
2. L = 10mH, VDD =50V, RG = 27Ω, starting Tj = 25°C
3. L = 13μH, starting Tj = 25°C

## Electrical Characteristics (SFET Part)

(Ta = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>KA5S0765C</b>						
Drain-Source Breakdown Voltage	BVDSS	VGS=0V, ID=50μA	650	-	-	V
Zero Gate Voltage Drain Current	IDSS	VDS=Max., Rating, VGS=0V	-	-	50	μA
		VDS=0.8Max., Rating, VGS=0V, TC=125°C	-	-	200	μA
Static Drain-Source on Resistance <sup>(1)</sup>	RDS(on)	VGS=10V, ID=4.0A	-	1.25	1.6	Ω
Forward Transconductance <sup>(1)</sup>	gfs	VDS=15V, ID=4.0A	3.0	-	-	S
Input Capacitance	Ciss	VGS=0V, VDS=25V, f = 1MHz	-	1600	-	pF
Output Capacitance	Coss		-	310	-	
Reverse Transfer Capacitance	Crss		-	120	-	
Turn on Delay Time	td(on)	VDD=0.5B VDS, ID=7.0A (MOSFET switching time is essentially independent of operating temperature)	-	25	-	nS
Rise Time	tr		-	55	-	
Turn Off Delay Time	td(off)		-	80	-	
Fall Time	tf		-	50	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	VGS=10V, ID=7.0A, VDS=0.5B VDS(MOSFET switching time is essentially independent of operating temperature)	-	-	72	nC
Gate-Source Charge	Qgs		-	9.3	-	
Gate-Drain (Miller) Charge	Qgd		-	29.3	-	
<b>KA5S0965</b>						
Drain-Source Breakdown Voltage	BVDSS	VGS=0V, ID=50μA	650	-	-	V
Zero Gate Voltage Drain Current	IDSS	VDS=Max., Rating, VGS=0V	-	-	50	μA
		VDS=0.8Max., Rating, VGS=0V, TC=125°C	-	-	200	μA
Static Drain-Source on Resistance <sup>(1)</sup>	RDS(on)	VGS=10V, ID=4.5A	-	0.96	1.2	Ω
Forward Transconductance <sup>(1)</sup>	gfs	VDS=50V, ID=4.5A	5.0	-	-	S
Input Capacitance	Ciss	VGS=0V, VDS=25V, f = 1MHz	-	1750	-	pF
Output Capacitance	Coss		-	190	-	
Reverse Transfer Capacitance	Crss		-	78	-	
Turn on Delay Time	td(on)	VDD=0.5B VDS, ID=9.0A (MOSFET switching time is essentially independent of operating temperature)	-	20	50	nS
Rise Time	tr		-	23	55	
Turn Off Delay Time	td(off)		-	85	180	
Fall Time	tf		-	30	70	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	VGS=10V, ID=9.0A, VDS=0.5B VDS(MOSFET switching time is essentially independent of operating temperature)	-	74	95	nC
Gate-Source Charge	Qgs		-	12	-	
Gate-Drain (Miller) Charge	Qgd		-	35	-	

**Electrical Characteristics (SFET Part)** (Continued)

(Ta = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>KA5S12656</b>						
Drain-Source Breakdown Voltage	BVDSS	VGS=0V, ID=50μA	650	-	-	V
Zero Gate Voltage Drain Current	IDSS	VDS=Max., Rating, VGS=0V	-	-	50	μA
		VDS=0.8Max., Rating, VGS=0V, TC=125°C	-	-	200	μA
Static Drain-Source On Resistance <sup>(1)</sup>	RDS(on)	VGS=10V, ID=6.0A	-	0.72	0.9	Ω
Forward Transconductance <sup>(1)</sup>	gfs	VDS=50V, ID=4.0A	5.7	-	-	S
Input Capacitance	Ciss	VGS=0V, VDS=25V, f = 1MHz	-	2700	-	pF
Output Capacitance	Coss		-	300	-	
Reverse Transfer Capacitance	Crss		-	61	-	
Turn on Delay Time	td(on)	VDD=0.5B VDS, ID=12.0A (MOSFET switching time is essentially independent of operating temperature)	-	18	-	nS
Rise Time	tr		-	37	-	
Turn Off Delay Time	td(off)		-	88	-	
Fall Time	tf		-	36	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	VGS=10V, ID=12.0A, VDS=0.5B VDS(MOSFET switching time is essentially independent of operating temperature)	-	-	140	nC
Gate-Source Charge	Qgs		-	20	-	
Gate-Drain (Miller) Charge	Qgd		-	69	-	
<b>KA5S1265</b>						
Drain-Source Breakdown Voltage	BVDSS	VGS=0V, ID=50μA	650	-	-	V
Zero Gate Voltage Drain Current	IDSS	VDS=Max., Rating, VGS=0V	-	-	50	μA
		VDS=0.8Max., Rating, VGS=0V, TC=125°C	-	-	200	μA
Static Drain-Source on Resistance <sup>(1)</sup>	RDS(on)	VGS=10V, ID=6.0A	-	0.72	0.9	Ω
Forward Transconductance <sup>(1)</sup>	gfs	VDS=50V, ID=4.0A	5.7	-	-	S
Input Capacitance	Ciss	VGS=0V, VDS=25V, f = 1MHz	-	2700	-	pF
Output Capacitance	Coss		-	300	-	
Reverse Transfer Capacitance	Crss		-	61	-	
Turn on Delay Time	td(on)	VDD=0.5BVDSS, ID=12.0A (MOSFET switching time is essentially independent of operating temperature)	-	18	-	nS
Rise Time	tr		-	37	-	
Turn Off Delay Time	td(off)		-	88	-	
Fall Time	tf		-	36	-	
Total Gate Charge (Gate-Source+Gate-Drain)	Qg	VGS=10V, ID=12.0A, VDS=0.5B VDS(MOSFET switching time is essentially independent of operating temperature)	-	-	140	nC
Gate-Source Charge	Qgs		-	20	-	
Gate-Drain (Miller) Charge	Qgd		-	69	-	

**Note:**

1. Pulse Test : Pulse width ≤ 300μS, Duty Cycle ≤ 2%
2. MOSFET switching time is essentially independent of operating temperature

$$3. S = \frac{1}{R}$$

**Electrical Characteristics (Control Part)** (Continued)(V<sub>CC</sub>=16V, T<sub>amb</sub> = 25°C unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Unit
<b>UVLO SECTION</b>						
Start Threshold Voltage	VSTART	V <sub>FB</sub> =GND	14	15	16	V
Stop Threshold Voltage	VSTOP	V <sub>FB</sub> =GND	8	9	10	V
<b>OSCILLATOR SECTION</b>						
Initial Frequency	FOSC	-	18	20	22	kHz
Voltage Stability	FSTABLE	12V ≤ V <sub>CC</sub> ≤ 23V	0	1	3	%
Temperature Stability (Note2)	ΔFOSC	-25°C ≤ T <sub>a</sub> ≤ 85°C	0	±5	±10	%
Maximum Duty Cycle	DMAX	-	92	95	98	%
Minimum Duty Cycle	DMIN	-	-	-	0	%
<b>FEEDBACK SECTION</b>						
Feedback Source Current	I <sub>FB</sub>	V <sub>FB</sub> =GND	0.7	0.9	1.1	mA
Shutdown Feedback Voltage	V <sub>SD</sub>	V <sub>FB</sub> ≥ 6.9V	6.9	7.5	8.1	V
Shutdown Delay Current	I <sub>DELAY</sub>	V <sub>FB</sub> =5V	3.0	4.0	5.0	μA
<b>SYNC. &amp; SOFTSTART SECTION</b>						
Softstart Voltage	V <sub>SS</sub>	V <sub>FB</sub> =2V	4.7	5.0	5.3	V
Softstart Current	I <sub>SS</sub>	V <sub>SS</sub> =0V	0.75	0.95	1.15	mA
Sync High Threshold Voltage(Note3)	V <sub>SYNCH</sub>	V <sub>CC</sub> =16V, V <sub>FB</sub> =5V	-	7.0	-	V
Sync Low Threshold Voltage(Note3)	V <sub>SYNCL</sub>	V <sub>CC</sub> =16V, V <sub>FB</sub> =5V	-	6.0	-	V
<b>CURRENT LIMIT(SELF-PROTECTION)SECTION</b>						
Peak Current Limit (Note4)	I <sub>OVER</sub>	<b>KA5S0765C</b>	3.52	4.0	4.48	A
		<b>KA5S0965</b>	5.28	6.0	6.72	
		<b>KA5S12656</b>	5.28	6.0	6.72	
		<b>KA5S1265</b>	7.04	8.0	8.96	
<b>PROTECTION SECTION</b>						
Over Voltage Protection	V <sub>OVP</sub>	V <sub>CC</sub> ≥ 24V	23	25	28	V
Over Current Latch voltage(Note3)	V <sub>OCL</sub>	-	0.9	1.0	1.1	V
Thermal Shutdown Temperature(Note2)	T <sub>SD</sub>	-	140	160	-	°C
<b>TOTAL DEVICE SECTION</b>						
Start Up Current	I <sub>START</sub>	V <sub>FB</sub> =GND, V <sub>CC</sub> =14V	-	0.1	0.17	mA
Operating Supply Current(Note1)	I <sub>OP</sub>	V <sub>FB</sub> =GND, V <sub>CC</sub> =16V	-	7	12	mA
	I <sub>OP(MIN)</sub>	V <sub>FB</sub> =GND, V <sub>CC</sub> =12V				
	I <sub>OP(MAX)</sub>	V <sub>FB</sub> =GND, V <sub>CC</sub> =30V				

**Note:**

1. These parameters are the current flowing in the control IC.
2. These parameters, although guaranteed, are not 100% tested in mass production
3. These parameters, although guaranteed, are tested in EDS(wafer test) process
4. These parameters are indicated Inductor current.

## Typical Performance Characteristics

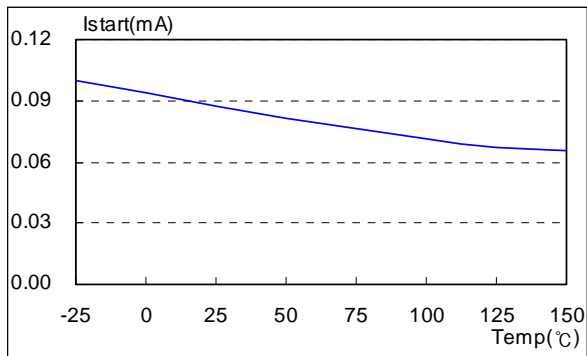


Figure 1. Start Up Current vs. Temp.

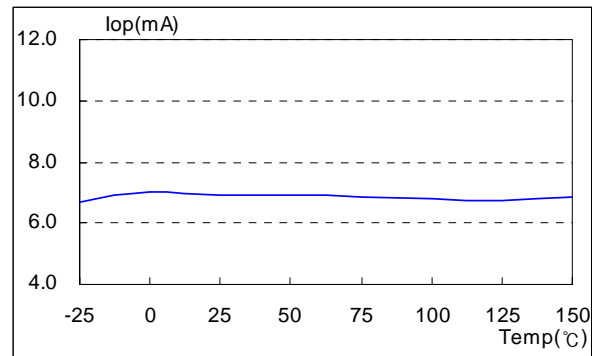


Figure 2. Operating Supply Current vs. Temp.

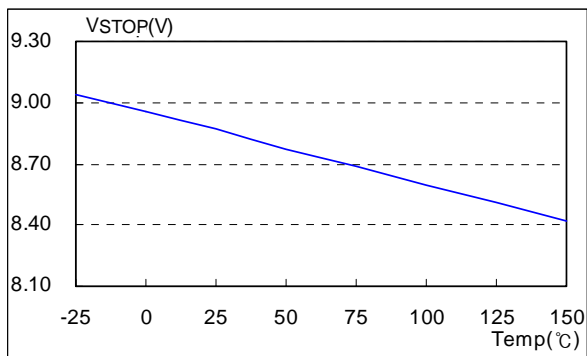


Figure 3. Stop Threshold Voltage vs. Temp

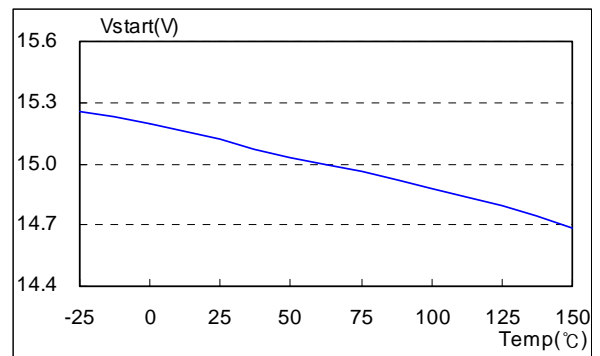


Figure 4. Start Threshold Voltage vs. Temp

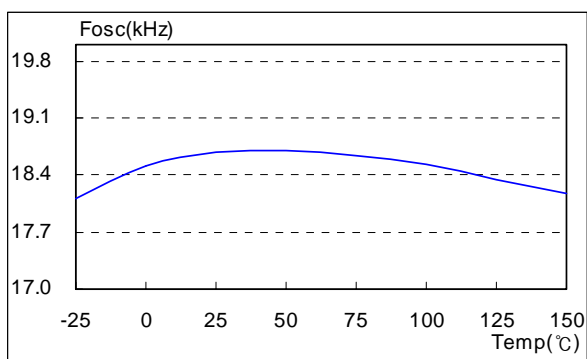


Figure 5. Initial Frequency VS. Temp

Typical Performance Characteristics (Continued)

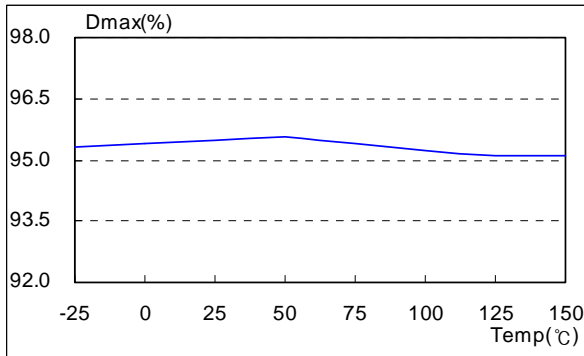


Figure 6. Maximum Duty vs. Temp.

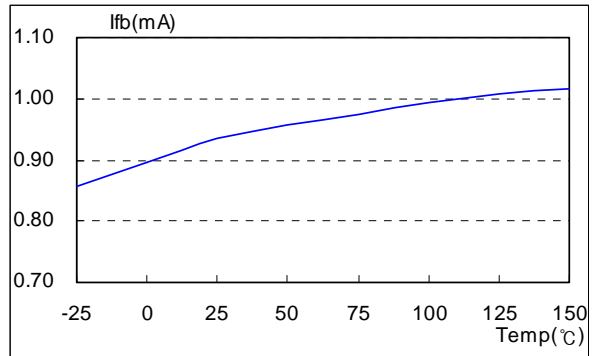


Figure 7. Feedback Source Current vs. Temp.

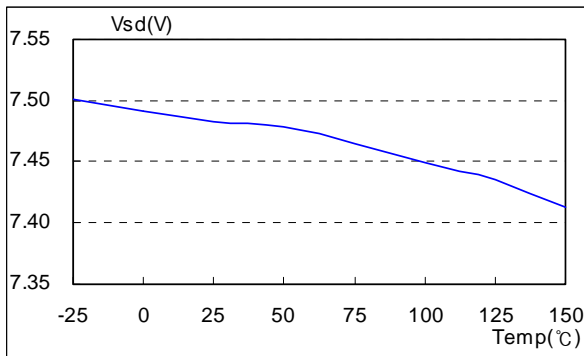


Figure 8. Shutdown Feedback Voltage vs. Temp.

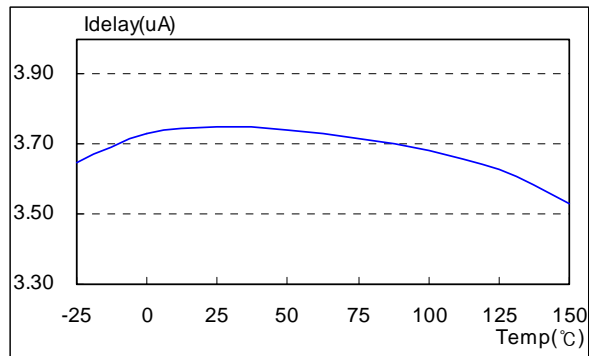


Figure 9. Shutdown Delay Current vs. Temp.

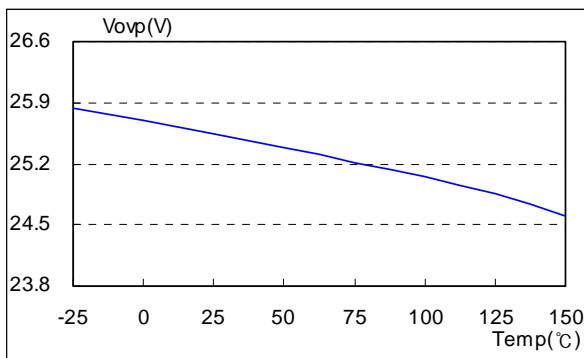


Figure 10. Over Voltage Protection vs. Temp.

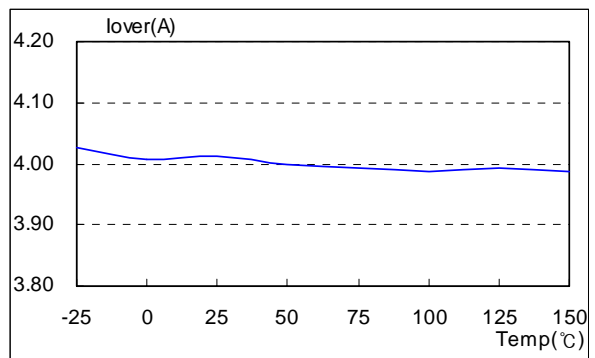
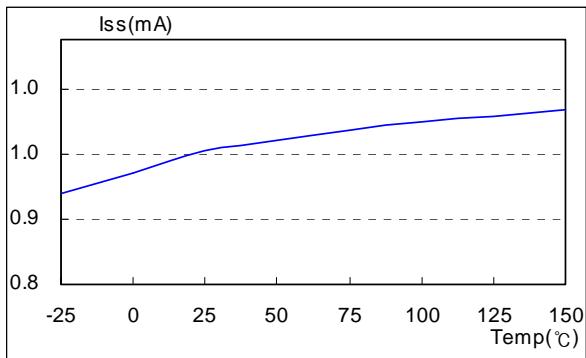
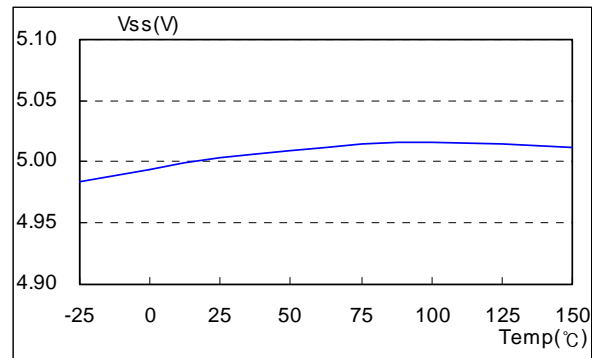


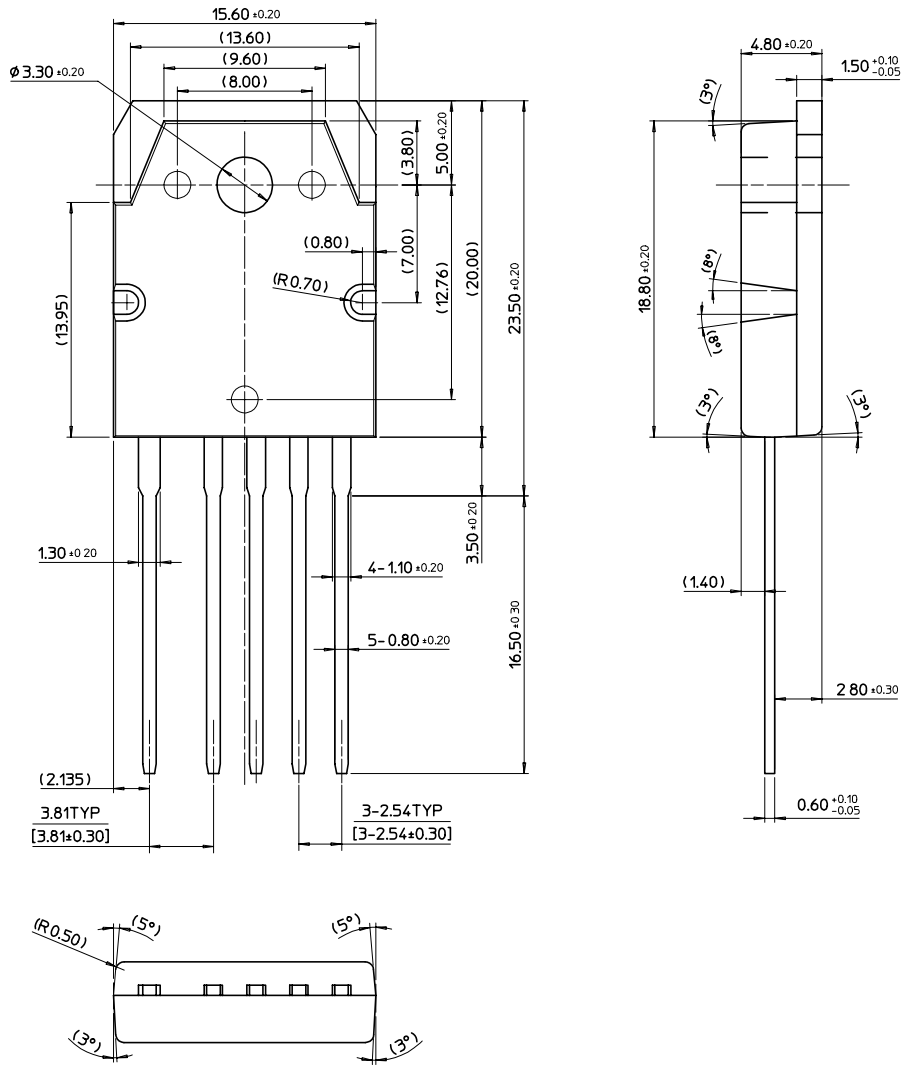
Figure 11. Peak Current Limit vs. Temp



**Typical Performance Characteristics** (Continued)**Figure 12. Soft Start Current vs. Temp.****Figure 13. Soft Start Voltage vs. Temp.**

Package Dimensions

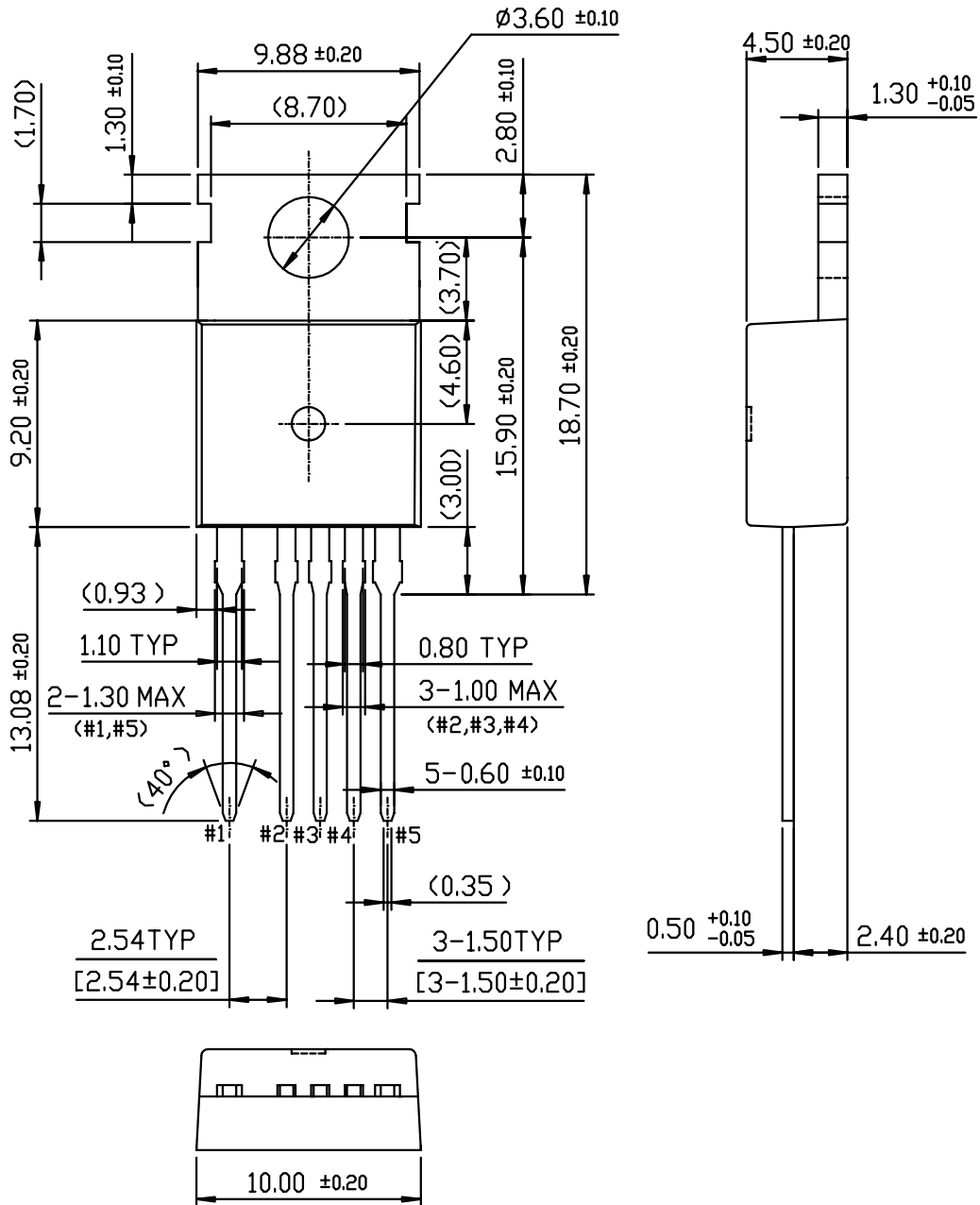
TO-3P-5L





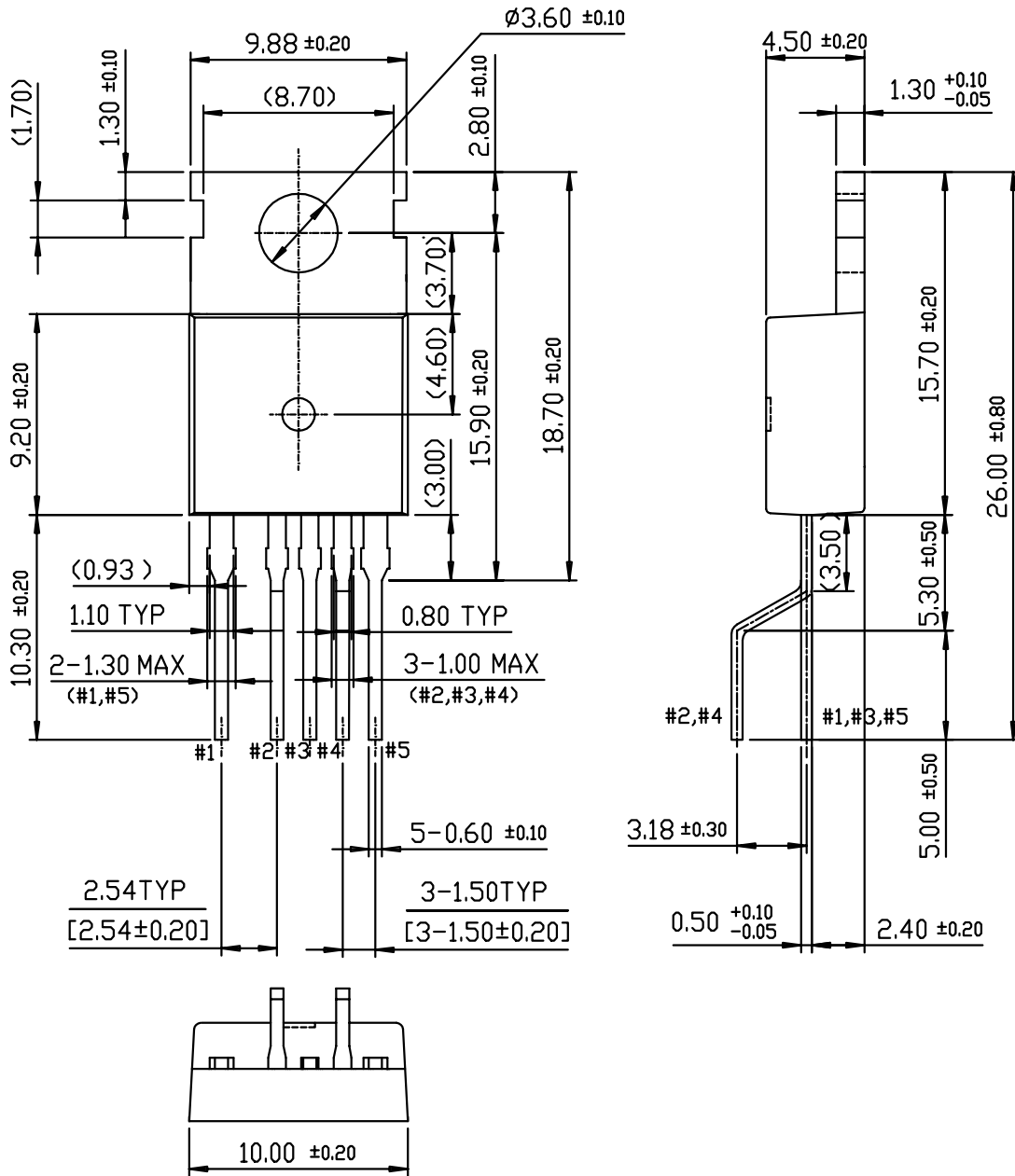
Package Dimensions (Continued)

TO-220-5L

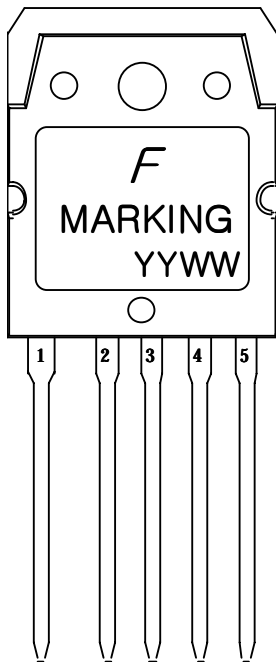


Package Dimensions (Continued)

TO-220-5L(Forming)



## TOP Mark and Pinout Information



Pin No.	Symbol	Description
1	Drain	SenseFET Drain
2	GND	Ground (Source)
3	VCC	Control Part Supply Input
4	F/B	PWM Non Inverting Input
5	S/S	Soft start & External Sync.

Device	Marking
KA5S0765C	5S0765C
KA5S0965	5S0965
KA5S12656	5S12656
KA5S1265	5S1265

### Notes ;

- (1) F ; Fairchild Semiconductor
- (2) 5S0765C, 5S0965, 5S12656, 5S1265; Device Marking Name
- (3) YY: Last Two Digit of Calender Year
- (4) WW: Patweek Based on Fairchild Semiconductor Work Month Calender

## Ordering Information

Product Number	Package	Marking Code	BVdss	Rds(on)
KA5S0765CTU	TO-220-5L	5S0765C	650V	1.6Ω
KA5S0765CYDTU	TO-220-5L(Forming)			
KA5S0965TU	TO-3P-5L	5S0965	650V	1.2Ω
KA5S0965YDTU	TO-3P-5L(Forming)			
KA5S12656TU	TO-3P-5L	5S12656	650V	0.9Ω
KA5S12656YDTU	TO-3P-5L(Forming)			
KA5S1265TU	TO-3P-5L	5S1265	650V	0.9Ω
KA5S1265YDTU	TO-3P-5L(Forming)			

TU : Non Forming Type

YDTU : Forming Type

**DISCLAIMER**

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

**LIFE SUPPORT POLICY**

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF THE PRESIDENT OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, and (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in a significant injury of the user.
2. A critical component in any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.