

September 2007

MPSA93 PNP High Voltage Amplifier

- This device is designed for high voltage driver applications.
- · Sourced from Process 76.



Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Value	Units	
V _{CEO}	Collector-Emitter Voltage	200	V	
V _{CBO}	Collector-Base Voltage	200	V	
V_{EBO}	Emitter-Base Voltage	5	V	
I _C	Collector Current (DC)	500	mA	
T _J , T _{STG}	Operating and Storage Junction Temperature Range	-55 ~ +150	°C	

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

NOTES

Thermal Characteristics $T_a=25$ °C unless otherwise noted

Symbol	Parameter	Max.	Units
P _D	Total Device Dissipation	625	mW
	Derate above 25°C	5.0	mW/°C
$R_{\theta JC}$	Thermal Resistance, Junction to Case	83.3	°C/W
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	200	°C/W
	DAPORA CILVA	200	U

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¹⁾ These ratings are based on a maximum junction temperature of 150 degrees C.

²⁾ These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

^{*} Device mounted on FR-4 PCB 1.6" X 1.6" X 0.06".

Electrical Characteristics $\rm T_{C} = 25\,^{\circ}C$ unless otherwise noted

Parameter

Off Characteristics					
V _{(BR)CBO}	Collector-Base Breakdown Voltage	$I_C = 100 \mu A, I_E = 0$	200		V
V _{(BR)CEO}	Collector-Emitter Breakdown Voltage*	I _C = 1 mA, I _B = 0	200		V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	$I_E = 100 \mu A, I_C = 0$	5		V
Irno	Emitter Cut-off Current	V _{ED} = 3V 1 ₀ = 0		0.1	пΔ

 $V_{CB} = 200V$, $I_{E} = 0$

Test Condition

Min.

Тур.

Max.

0.25

Units

μΑ

On Characteristics

Symbol

 I_{CBO}

h _{FE}	DC Current Gain	$V_{CE} = 10V, I_{C} = 1mA$ $V_{CE} = 10V, I_{C} = 10mA$ $V_{CE} = 10V, I_{C} = 30mA$	25 40 25		
V _{CE} (sat)	Collector-Emitter Saturation Voltage	$I_C = 20 \text{ mA}, I_B = 2 \text{ mA}$		0.4	V
V _{BE} (sat)	Base-Emitter Saturation Voltage	$I_C = 20 \text{ mA}, I_B = 2 \text{ mA}$		0.9	V

Small Signal Characteristics

Ccb	Collector-Base Capacitance	$V_{CB} = 20 \text{ V}, I_{E} = 0, f = 1.0 \text{ MHz}$		8	pF
f _T	Current Gain Bandwidth Product	$V_{CE} = 5.0V, I_{C} = 10mA, f = 100MHz$	50		MHz

^{*} Pulse Test: Pulse Width 300 s, Duty Cycle 2.0%

Notes

Collector Cut-off Current

¹⁾ All voltages (V) and currents (A) are negative polarity for PNP transistors.





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