



## SILICON PLASTIC POWER TRANSISTOR

**NPN 2N6292**

**7A 40W**

### Technical Data

...designed for use in general-purpose switching and amplifier applications.

- ☞ DC Current Gain -  $h_{FE} = 30-150 @ I_C = 2.0A_{dc}$
- ☞ Collector-Emitter Sustaining Voltage –  $V_{CEO(sus)} = 70 V_{dc}$  (Min)
- ☞ TO-220 Package

### MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector- Emitter Voltage	$V_{CEO}$	70	Vdc
Collector – Base Voltage	$V_{CB}$	80	Vdc
Emitter Base Voltage	$V_{EB}$	5	Vdc
Collector Current – Continuous	$I_C$	7	Adc
Peak		10	
Base Current	$I_B$	3	Adc
Total Power Dissipation @ $T_C = 25^\circ C$	PD	40	Watts
Derate above $25^\circ C$		0.32	W/ $^\circ C$
Operating and Storage junction Temperature Range	$T_j, T_{stg}$	-65 to +150	$^\circ C$

### THERMAL CHARACTERISTICS

Characteristic	Symbol	Max.	Unit
Thermal resistance junction to case	$R_{thjc}$	3.125	$^\circ C/W$



**ELECTRICAL CHARACTERISTICS : [ Tc = 25 °C unless otherwise noted ]**

Characteristic	Symbol	Min	Typ	Max	Unit
<b>* OFF CHARACTERISTICS :</b>					
Collector–Emitter Sustaining Voltage (1) [ Ic =100 mAdc, IB = 0 ]	V <sub>CEO(sus)</sub>	70			Vdc
Collector Cutoff Current [ V <sub>CE</sub> = 60 Vdc, IB = 0 ]	I <sub>CEO</sub>			1	mAdc
Collector Cutoff Current [ V <sub>CE</sub> = 80 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc ] [ V <sub>CE</sub> =70 Vdc, V <sub>BE(off)</sub> = 1.5 Vdc , Tc = 150 °C ]	I <sub>CEX</sub>			100 2	⊛Adc mAdc
Emitter Cutoff Current [ V <sub>BE</sub> =5.0 Vdc , Ic = 0 ]	I <sub>EBO</sub>			1	mAdc
<b>* ON CHARACTERISTICS (1):</b>					
DC Current Gain [ Ic = 2.0 Adc , V <sub>CE</sub> = 4.0 Vdc ] [ Ic = 7 Adc , V <sub>CE</sub> = 4.0 Vdc ]	h <sub>FE</sub>	30 2.3		150	
Collector-Emitter Saturation Voltage [ Ic = 7Adc , IB = 3 Adc )	V <sub>CE(sat)</sub>			3.5	Vdc
Base-Emitter on Voltage [ Ic =7.0 Adc , V <sub>CE</sub> = 4.0. V <sub>DC</sub> ]	V <sub>BE(on)</sub>			3.0	Vdc
<b>DYNAMIC CHARACTERISTICS :</b>					
Current Gain – Bandwidth Product [Ic=0.5Adc,V <sub>CE</sub> =4Vdc,f <sub>test</sub> =1.0 MHz ]	f <sub>T</sub>	10			MHz
Small-Signal Current Gain [ IC= 0.5 Adc, V <sub>CE</sub> =4.0 Vdc, f=50kHz]	h <sub>fe</sub>	20			
Output Capacitance (V <sub>CB</sub> =10V,I <sub>E</sub> =0,f=1.0MHz)	C <sub>OB</sub>			250	pF

- Indicates within JEDEC Registration Data.
- (1) Pulse Test : Pulse Width <300µs , Duty Cycle < 2.0%