

HORIZONTAL DEFLECTION TRANSISTOR NPN BU407, 7A 60W

Technical Data

... for Horizontal deflection output stages of TV's and CRT's.

- ☞ Collector-Emitter Voltage- $V_{CES}=330Vdc$
- ☞ Low Saturation Voltage: $V_{CE(sat)}=1V(max)@5A$
- ☞ TO-220 Package
- ☞ Fast Switching Speed: $t_f=750 ns(max)$

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector- Emitter Voltage	V_{CEO}	150	Vdc
Collector- Emitter Voltage	V_{CBO}	330	Vdc
Collector- Emitter Voltage	V_{CEV}	330	Vdc
Emitter Base Voltage	V_{EB}	6	Vdc
Collector Current – Continuous	I_C	7	Adc
Peak(1)	I{CM}	10	
Base Current – continuous	I_B	4	Adc
<u>Total Power Dissipation @</u> <u>TC = 25° C</u> Derate above 25°C	PD	60 0.48	Watts W/°C
Operating and Storage junction Temperature Range	T_j, T_{stg}	-65 to +150	°C

THERMAL CHARACTERISTICS

Characteristic	Symbol	Max.	Unit
Thermal resistance junction to case	R_{thjc}	2.08	°C/W



ELECTRICAL CHARACTERISTICS : [Tc = 25 °C unless otherwise noted]

Characteristic	Symbol	Min	Typ	Max	Unit
* OFF CHARACTERISTICS :					
Collector–Emitter Sustaining Voltage (1) [Ic =100 mAdc, IB = 0]	V _{CEO(sus)}	150			Vdc
Collector Cutoff Current [V _{CE} = 400 Vdc, V _{BE} = 0]	I _{CES}			5	mAdc
Emitter Cutoff Current [V _{EB} = 6V, Ic = 0]	I _{EBO}			1	mAdc
* ON CHARACTERISTICS (1):					
DC Current Gain [Ic = 5 Adc , V _{CE} = 5.0 Vdc]	h _{FE}		28		
Collector-Emitter Saturation Voltage [Ic = 5 Adc , IB = 0.5Adc)	V _{CE(sat)}			1	Vdc
Base-Emitter Saturation Voltage [Ic = 5 Adc , IB = 0.5Adc]	V _{BE(sat)}			1.2	Vdc
DYNAMIC CHARACTERISTICS:					
Current Gain – Bandwidth Product [Ic = 0.5Adc, V _{CE} =10 Vdc, ftest=20 MHz]	f _T	10		--	MHz
Output Capacitance (V _{CB} =10Vdc, IE=0, f=1MHz)	C _{OB}	--	80	--	pF
SWITCHING CHARACTERISTICS					
Inductive Load Crossover Time [vcc=40Vdc, IC=5Adc, IB1=IB2=0.5Adc, L=150μH]	t _c	---		0.75	μs

(1) Pulse Test : Pulse Width =300μs , Duty Cycle < 2.0%