

DATA SHEET

BFQ135

NPN 6.5 GHz wideband transistor

Product specification
Supersedes data of September 1995
File under Discrete Semiconductors, SC14

1997 Nov 07

NPN 6.5 GHz wideband transistor

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FEATURES

- Optimum temperature profile and excellent reliability properties ensured by emitter-ballasting resistors and application of gold sandwich metallization.

APPLICATIONS

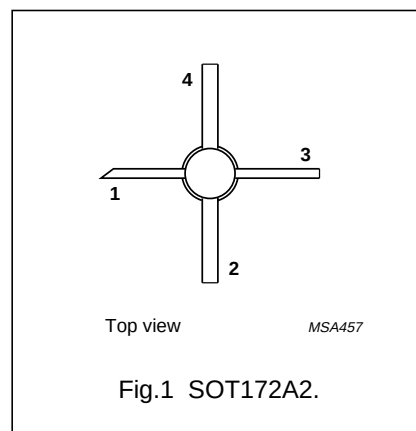
- MATV and microwave amplifiers, such as in aerial amplifiers, radar systems, oscilloscopes, spectrum analysers, etc.

DESCRIPTION

NPN wideband transistor in a 4-lead dual-emitter SOT172A2 package with a ceramic cap. All leads are isolated from the mounting base.

PINNING

PIN	DESCRIPTION
1	collector
2, 4	emitter
3	base



QUICK REFERENCE DATA

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
V_{CEO}	collector-emitter voltage	open base	—	—	19	V
I_C	collector current (DC)		—	—	150	mA
P_{tot}	total power dissipation	$T_c \leq 145\text{ }^{\circ}\text{C}$	—	—	2.7	W
h_{FE}	DC current gain	$I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	55	—	—	
f_T	transition frequency	$I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $f = 1\text{ GHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	—	6.5	—	GHz
G_{UM}	maximum unilateral power gain	$I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $f = 500\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	—	17	—	dB
		$I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $f = 800\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	—	13.5	—	dB
V_O	output voltage	$d_{im} = -60\text{ dB}$; $I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $R_L = 75\text{ }\Omega$; $f_p + f_q - f_r = 793.25\text{ MHz}$; $T_{amb} = 25\text{ }^{\circ}\text{C}$	—	1.2	—	V

WARNING

Product and environmental safety - toxic materials

This product contains beryllium oxide. The product is entirely safe provided that the BeO disc is not damaged. All persons who handle, use or dispose of this product should be aware of its nature and of the necessary safety precautions. After use, dispose of as chemical or special waste according to the regulations applying at the location of the user. It must never be thrown out with the general or domestic waste.

NPN 6.5 GHz wideband transistor**BFQ135****LIMITING VALUES**

In accordance with the Absolute Maximum Rating System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.	UNIT
V _{CBO}	collector-base voltage	open emitter	–	25	V
V _{CEO}	collector-emitter voltage	open base	–	19	V
V _{EBO}	emitter-base voltage	open collector	–	2	V
I _C	collector current (DC)		–	150	mA
P _{tot}	total power dissipation	T _c ≤ 145 °C	–	2.7	W
T _{stg}	storage temperature		–65	+150	°C
T _j	junction temperature		–	200	°C

THERMAL CHARACTERISTICS

SYMBOL	PARAMETER	VALUE	UNIT
R _{th j-c}	thermal resistance from junction to case	20	K/W

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CHARACTERISTICS

$T_j = 25\text{ °C}$ unless otherwise specified.

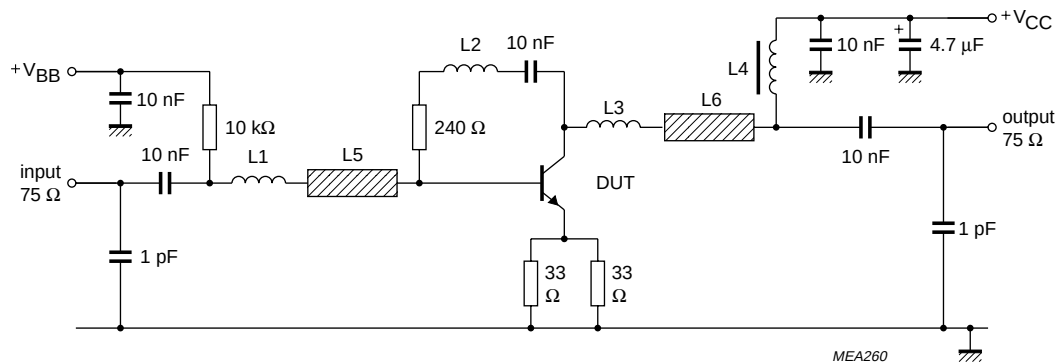
SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{CBO}	collector cut-off current	$I_E = 0$; $V_{CB} = 18\text{ V}$	–	–	50	μA
h_{FE}	DC current gain	$I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $T_{amb} = 25\text{ °C}$	55	–	–	
C_c	collector capacitance	$I_E = i_e = 0$; $V_{CB} = 18\text{ V}$; $f = 1\text{ MHz}$	–	1.8	–	pF
C_e	emitter capacitance	$I_C = i_c = 0$; $V_{EB} = 0.5\text{ V}$; $f = 1\text{ MHz}$	–	5.5	–	pF
C_{re}	feedback capacitance	$I_C = 0$; $V_{CE} = 18\text{ V}$; $f = 1\text{ MHz}$	–	1	1.2	pF
f_T	transition frequency	$I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $f = 1\text{ GHz}$; $T_{amb} = 25\text{ °C}$	–	6.5	–	GHz
G_{UM}	maximum unilateral power gain (note 1)	$I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $f = 500\text{ MHz}$; $T_{amb} = 25\text{ °C}$	–	17	–	dB
		$I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $f = 800\text{ MHz}$; $T_{amb} = 25\text{ °C}$	–	13.5	–	dB
V_O	output voltage	note 2	–	1.35	–	V
		note 3	–	1.2	–	V
d_2	second order intermodulation distortion	note 4	–	–70	–	dB
		note 5	–	–70	–	dB

Notes

- G_{UM} is the maximum unilateral power gain, assuming S_{12} is zero and $G_{UM} = 10 \log \frac{|S_{21}|^2}{(1 - |S_{11}|^2)(1 - |S_{22}|^2)} \text{ dB}$.
- $d_{im} = -60\text{ dB}$ (DIN 45004B); $I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $R_L = 75\text{ }\Omega$; $T_{amb} = 25\text{ °C}$;
 $V_p = V_O$ at $d_{im} = -60\text{ dB}$; $f_p = 445.25\text{ MHz}$;
 $V_q = V_O - 6\text{ dB}$; $f_q = 453.25\text{ MHz}$;
 $V_r = V_O - 6\text{ dB}$; $f_r = 455.25\text{ MHz}$;
measured at $f_p + f_q - f_r = 443.25\text{ MHz}$.
- $d_{im} = -60\text{ dB}$ (DIN 45004B); $I_C = 120\text{ mA}$; $V_{CE} = 18\text{ V}$; $R_L = 75\text{ }\Omega$; $T_{amb} = 25\text{ °C}$;
 $V_p = V_O$ at $d_{im} = -60\text{ dB}$; $f_p = 795.25\text{ MHz}$;
 $V_q = V_O - 6\text{ dB}$; $f_q = 803.25\text{ MHz}$;
 $V_r = V_O - 6\text{ dB}$; $f_r = 805.25\text{ MHz}$;
measured at $f_p + f_q - f_r = 793.25\text{ MHz}$.
- $I_C = 90\text{ mA}$; $V_{CE} = 18\text{ V}$; $V_O = 50\text{ dBmV}$; $T_{amb} = 25\text{ °C}$;
 $f_p = 50\text{ MHz}$; $f_q = 400\text{ MHz}$;
measured at $f_p + f_q = 450\text{ MHz}$.
- $I_C = 90\text{ mA}$; $V_{CE} = 18\text{ V}$; $V_O = 50\text{ dBmV}$; $T_{amb} = 25\text{ °C}$;
 $f_p = 250\text{ MHz}$; $f_q = 560\text{ MHz}$;
measured at $f_p + f_q = 810\text{ MHz}$.

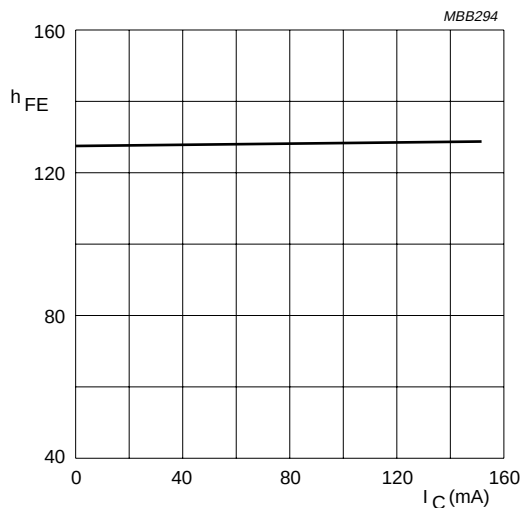
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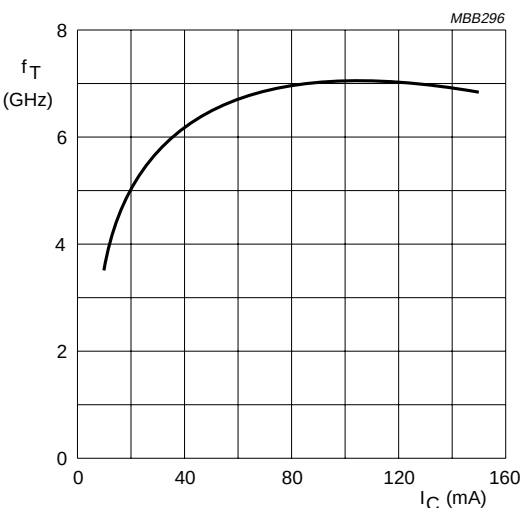
L1 = 8 nH.
L2 = 15 nH (2 turns copper wire, internal diameter 2 mm).
L3 = 10 nH (2 turns copper wire, internal diameter 1.5 mm).
L5: Lp = 21 mm; Rc = 75 Ω.
L6: Lp = 16 mm; Rc = 75 Ω.

Fig.2 Intermodulation distortion test circuit.



VCE = 18 V; Tamb = 25 °C.

Fig.3 DC current gain as a function of collector current; typical values.

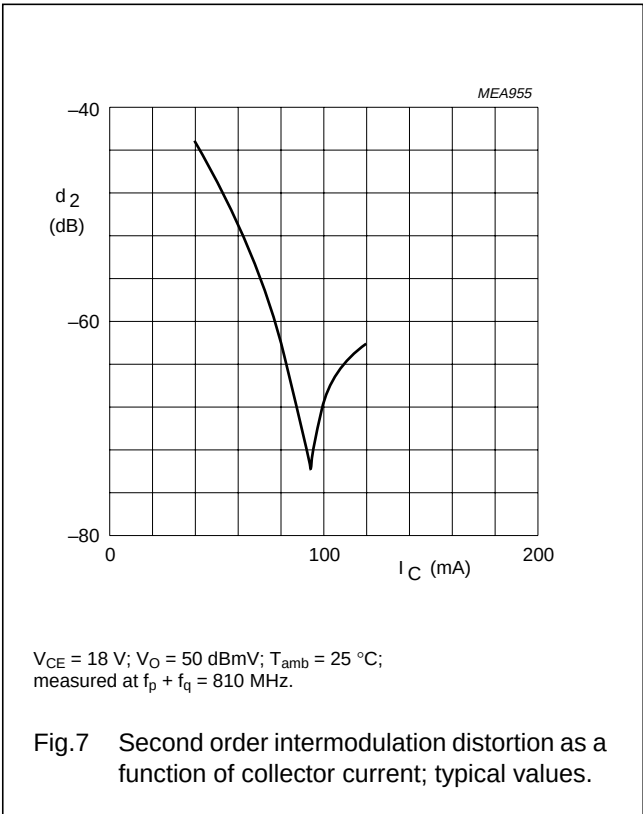
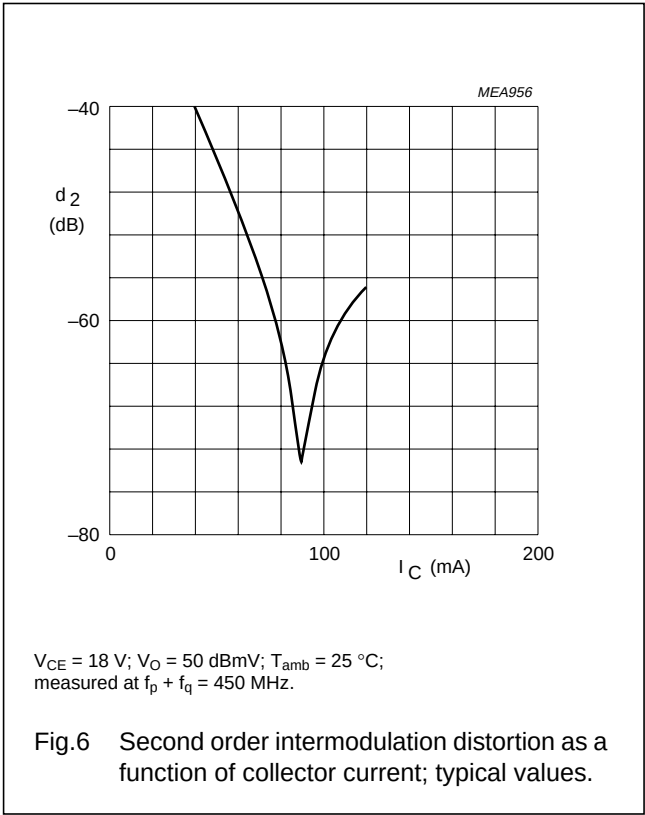
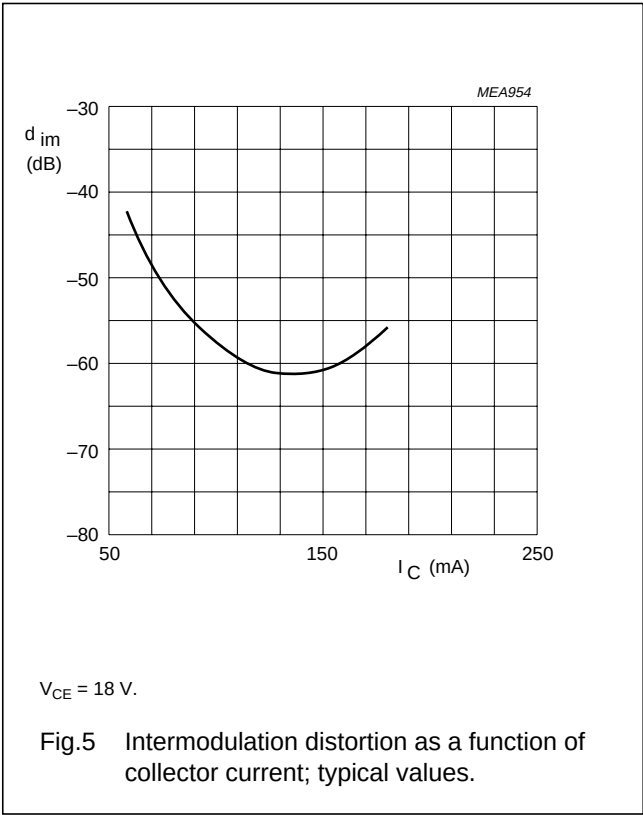


VCE = 18 V; f = 1 GHz; Tamb = 25 °C.

Fig.4 Transition frequency as a function of collector current; typical values.

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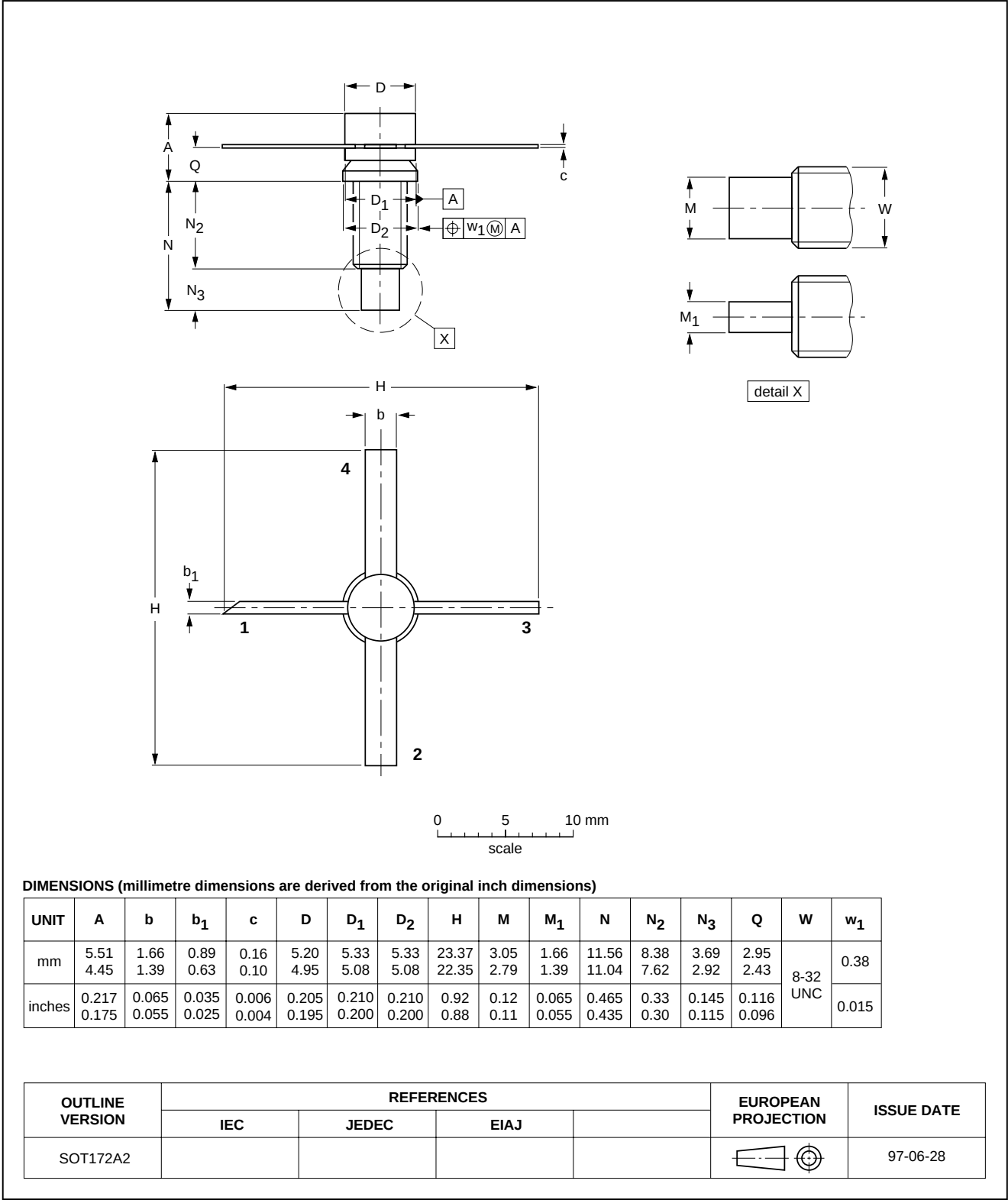
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PACKAGE OUTLINE

Studded ceramic package; 4 leads

SOT172A2



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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Short-form specification	The data in this specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.
Limiting values	
Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	

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NOTES

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Printed in The Netherlands

127127/00/02/pp12

Date of release: 1997 Nov 07

Document order number: 9397 750 02763

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