

Triacs

high noise immunity

BT139B series H

GENERAL DESCRIPTION

Glass passivated triacs in a plastic envelope suitable for surface mounting, intended for use in applications requiring high noise immunity in addition to high, bidirectional blocking voltage capability and thermal cycling performance. Typical applications include motor control, industrial lighting, heating and static switching.

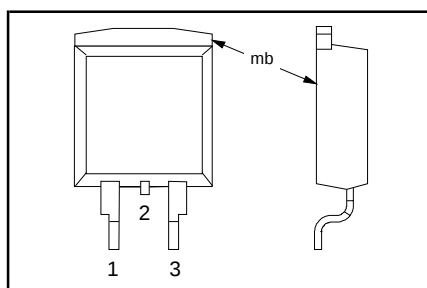
QUICK REFERENCE DATA

SYMBOL	PARAMETER	MAX.	MAX.	MAX.	UNIT
V_{DRM}	BT139B- Repetitive peak off-state voltages	500H 500	600H 600	800H 800	V
$I_{\text{T(RMS)}}$	RMS on-state current	16	16	16	A
I_{TSM}	Non-repetitive peak on-state current	140	140	140	A

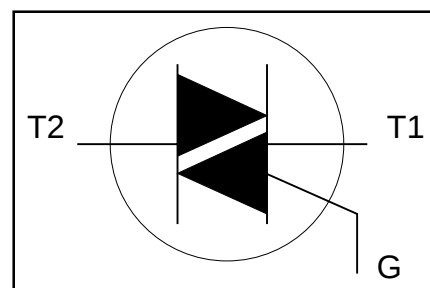
PINNING - SOT404

PIN	DESCRIPTION
1	main terminal 1
2	main terminal 2
3	gate
mb	main terminal 2

PIN CONFIGURATION



SYMBOL



LIMITING VALUES

Limiting values in accordance with the Absolute Maximum System (IEC 134).

SYMBOL	PARAMETER	CONDITIONS	MIN.	MAX.			UNIT
V_{DRM}	Repetitive peak off-state voltages		-	-500 500 ¹	-600 600 ¹	-800 800	V
$I_{\text{T(RMS)}}$	RMS on-state current	full sine wave; $T_{\text{mb}} \leq 99^\circ\text{C}$	-	16			A
I_{TSM}	Non-repetitive peak on-state current	full sine wave; $T_{\text{j}} = 25^\circ\text{C}$ prior to surge	-	140			A
		$t = 20\text{ ms}$	-	140			A
		$t = 16.7\text{ ms}$	-	150			A
		$t = 10\text{ ms}$	-	98			A ² s
I^2t	I^2t for fusing	$I_{\text{TM}} = 20\text{ A}$; $I_{\text{G}} = 0.2\text{ A}$;	-	50			A/μs
dl_{T}/dt	Repetitive rate of rise of on-state current after triggering	$dl_{\text{G}}/dt = 0.2\text{ A}/\mu\text{s}$	-	50			A/μs
		T2+ G+	-	50			A/μs
		T2+ G-	-	50			A/μs
		T2- G-	-	10			A/μs
		T2- G+	-	2			A
I_{GM}	Peak gate current		-	5			V
V_{GM}	Peak gate voltage		-	5			W
P_{GM}	Peak gate power		-	0.5			W
$P_{\text{G(AV)}}$	Average gate power	over any 20 ms period	-	150			°C
T_{stg}	Storage temperature		-40	125			°C
T_{j}	Operating junction temperature		-				

¹ Although not recommended, off-state voltages up to 800V may be applied without damage, but the triac may switch to the on-state. The rate of rise of current should not exceed 15 A/μs.

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THERMAL RESISTANCES

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
$R_{th\ j-mb}$	Thermal resistance junction to mounting base	full cycle	-	-	1.2	K/W
$R_{th\ j-a}$	Thermal resistance junction to ambient	half cycle	-	-	1.7	K/W
		minimum footprint, FR4 board	-	55	-	K/W

STATIC CHARACTERISTICS

$T_j = 25\ ^\circ\text{C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
I_{GT}	Gate trigger current	$V_D = 12\ \text{V}$; $I_T = 0.1\ \text{A}$				
		T2+ G+	10	14	50	mA
		T2+ G-	10	17	50	mA
		T2- G-	10	18	50	mA
		T2- G+	10	40	100	mA
I_L	Latching current	$V_D = 12\ \text{V}$; $I_{GT} = 0.1\ \text{A}$				
		T2+ G+	-	10	60	mA
		T2+ G-	-	25	90	mA
		T2- G-	-	12	60	mA
		T2- G+	-	14	90	mA
I_H	Holding current	$V_D = 12\ \text{V}$; $I_{GT} = 0.1\ \text{A}$	-	8	60	mA
V_T	On-state voltage	$I_T = 20\ \text{A}$	-	1.2	1.6	V
V_{GT}	Gate trigger voltage	$V_D = 12\ \text{V}$; $I_T = 0.1\ \text{A}$	-	0.7	1.5	V
		$V_D = 400\ \text{V}$; $I_T = 0.1\ \text{A}$; $T_j = 125\ ^\circ\text{C}$	0.25	0.4	-	V
I_D	Off-state leakage current	$V_D = V_{DRM(max)}$; $T_j = 125\ ^\circ\text{C}$	-	0.1	0.5	mA

DYNAMIC CHARACTERISTICS

$T_j = 25\ ^\circ\text{C}$ unless otherwise stated

SYMBOL	PARAMETER	CONDITIONS	MIN.	TYP.	MAX.	UNIT
dV_D/dt	Critical rate of rise of off-state voltage	$V_{DM} = 67\% V_{DRM(max)}$; $T_j = 125\ ^\circ\text{C}$; exponential waveform; gate open circuit	200	500	-	V/ μs
dV_{com}/dt	Critical rate of change of commutating voltage	$V_{DM} = 400\ \text{V}$; $T_j = 95\ ^\circ\text{C}$; $I_{T(RMS)} = 16\ \text{A}$; $di_{com}/dt = 7.2\ \text{A/ms}$; gate open circuit	10	20	-	V/ μs
t_{gt}	Gate controlled turn-on time	$I_{TM} = 20\ \text{A}$; $V_D = V_{DRM(max)}$; $I_G = 0.1\ \text{A}$; $di_G/dt = 5\ \text{A}/\mu\text{s}$	-	2	-	μs

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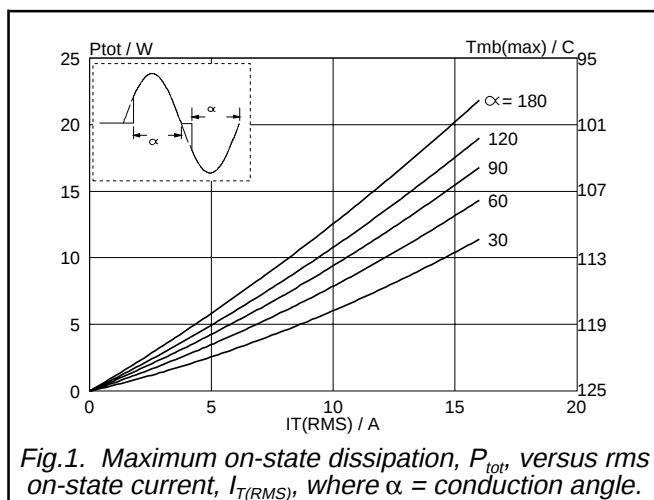


Fig.1. Maximum on-state dissipation, P_{tot} , versus rms on-state current, $I_{T(RMS)}$, where α = conduction angle.

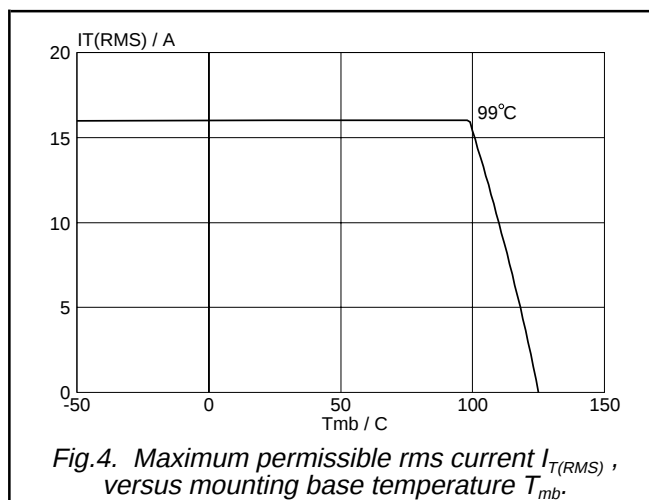


Fig.4. Maximum permissible rms current $I_{T(RMS)}$, versus mounting base temperature T_{mb} .

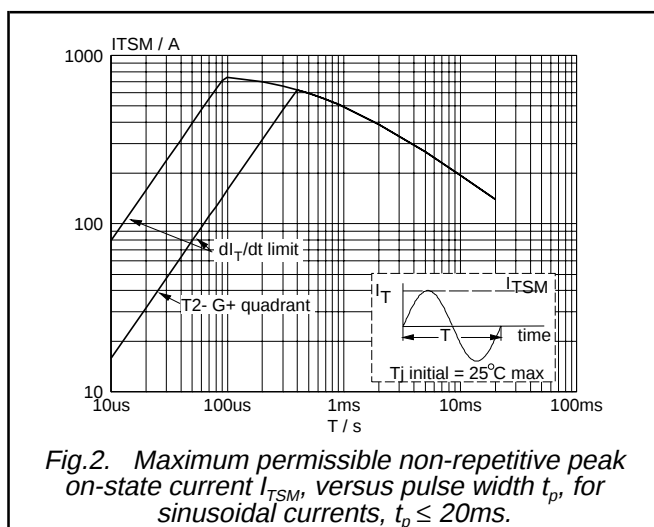


Fig.2. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus pulse width t_p , for sinusoidal currents, $t_p \leq 20\text{ms}$.

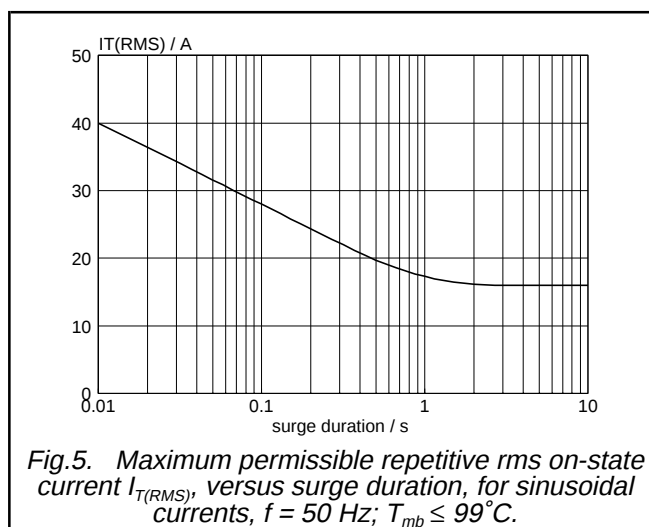


Fig.5. Maximum permissible repetitive rms on-state current $I_{T(RMS)}$, versus surge duration, for sinusoidal currents, $f = 50\text{ Hz}$; $T_{mb} \leq 99^\circ\text{C}$.

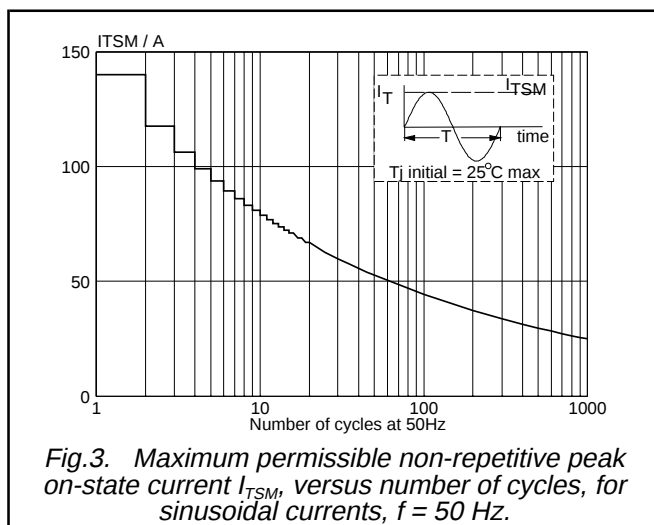


Fig.3. Maximum permissible non-repetitive peak on-state current I_{TSM} , versus number of cycles, for sinusoidal currents, $f = 50\text{ Hz}$.

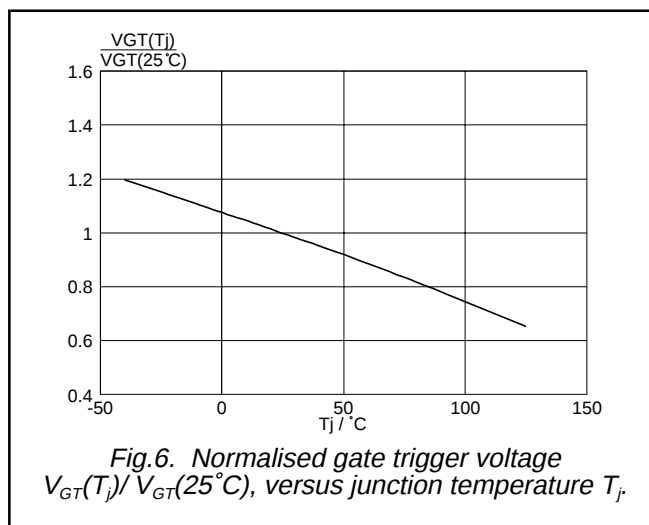
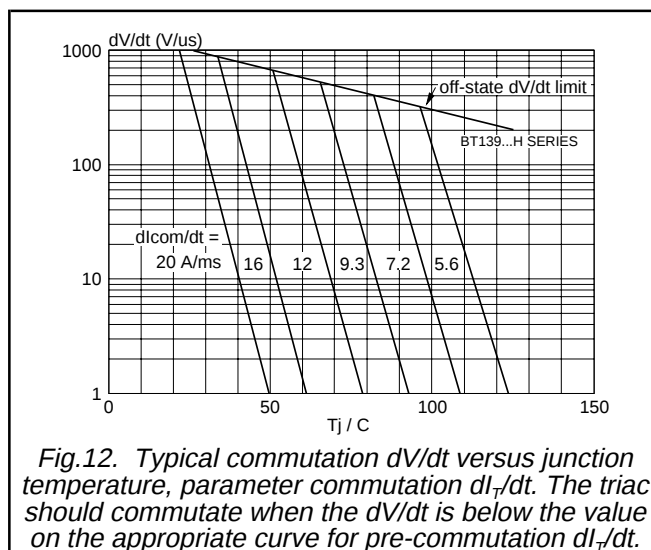
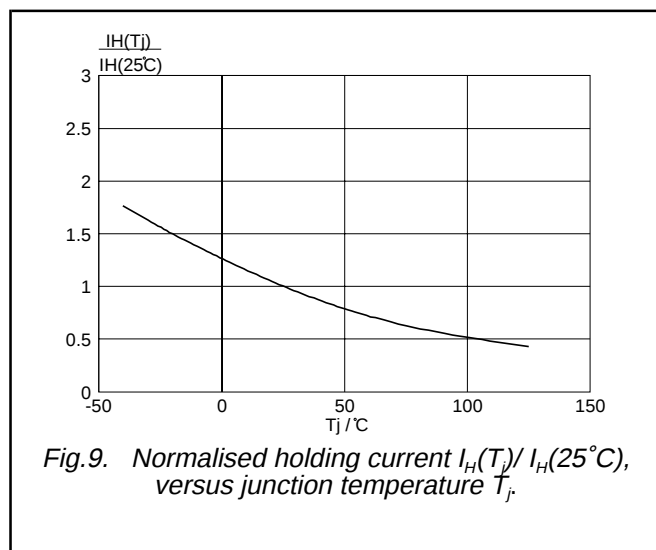
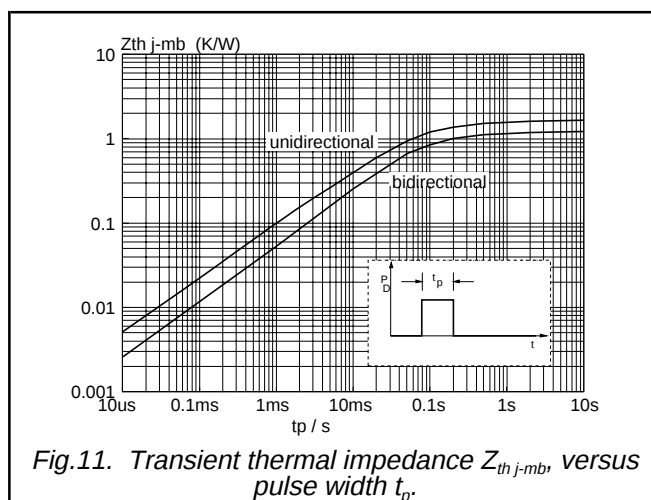
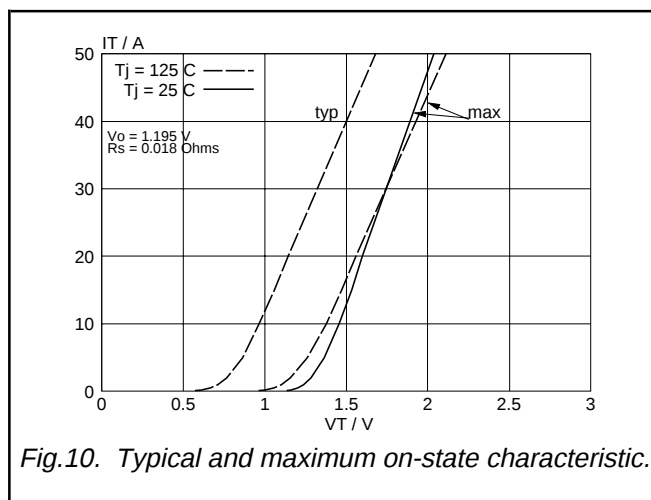
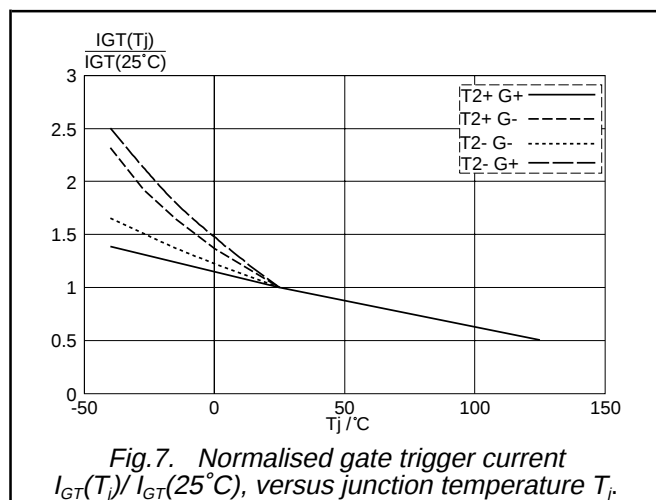


Fig.6. Normalised gate trigger voltage $V_{GT}(T_j)/V_{GT}(25^\circ\text{C})$, versus junction temperature T_j .

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MECHANICAL DATA

Dimensions in mm

Net Mass: 1.4 g

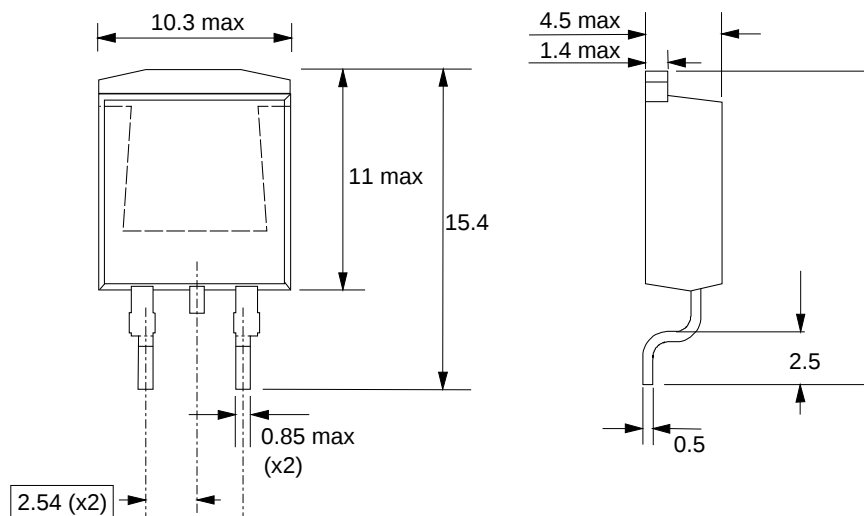


Fig.13. SOT404 : centre pin connected to mounting base.

Notes

1. Epoxy meets UL94 V0 at 1/8".

MOUNTING INSTRUCTIONS

Dimensions in mm

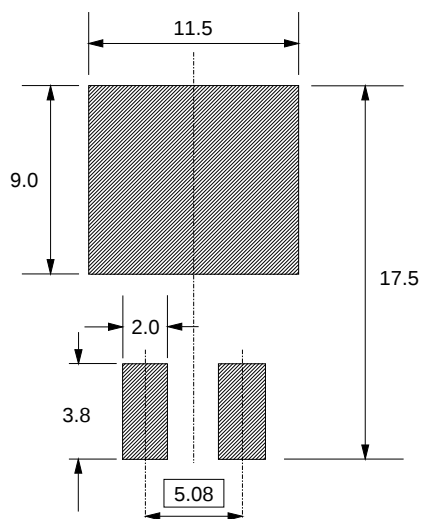


Fig.14. SOT404 : minimum pad sizes for surface mounting.

Notes

1. Plastic meets UL94 V0 at 1/8".

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DEFINITIONS

Data sheet status	
Objective specification	This data sheet contains target or goal specifications for product development.
Preliminary specification	This data sheet contains preliminary data; supplementary data may be published later.
Product specification	This data sheet contains final product specifications.
Limiting values	
Limiting values are given in accordance with the Absolute Maximum Rating System (IEC 134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of this specification is not implied. Exposure to limiting values for extended periods may affect device reliability.	
Application information	
Where application information is given, it is advisory and does not form part of the specification.	
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