

DATA SHEET

CBT3244A

Octal bus switch with quad output enables

Product data sheet

2004 May 26

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CBT3244A

FEATURES

- Standard '244-type pinout
- 5 Ω switch connection between two ports
- TTL compatible control input levels
- Package options include plastic small outline (D), shrink small outline (DB), QSOP (DS), and thin shrink small outline (TSSOP)
- Latch-up protection exceeds 500 mA per JESD78
- ESD protection exceeds 1000 V HBM per JESD22-A114, 200 V MM per JESD22-A115 and 1000 V CDM per JESD22-C101

DESCRIPTION

The CBT3244A provides eight bits of high-speed TTL-compatible bus switching in a standard '244 device pinout. The low on-state resistance of the switch allows connections to be made with minimal propagation delay.

The CBT3244A device is organized as two 4-bit low-impedance switches with separate output-enable ($\overline{OE}$) inputs. When $\overline{OE}$ is LOW, the switch is on and data can flow from port A to port B, or vice versa. When $\overline{OE}$ is HIGH, the switch is open and high-impedance state exists between the two ports.

The CBT3244A is characterized for operation from $-40\text{ }^{\circ}\text{C}$ to $85\text{ }^{\circ}\text{C}$.

PIN CONFIGURATION — SO, SSOP, QSOP, and TSSOP

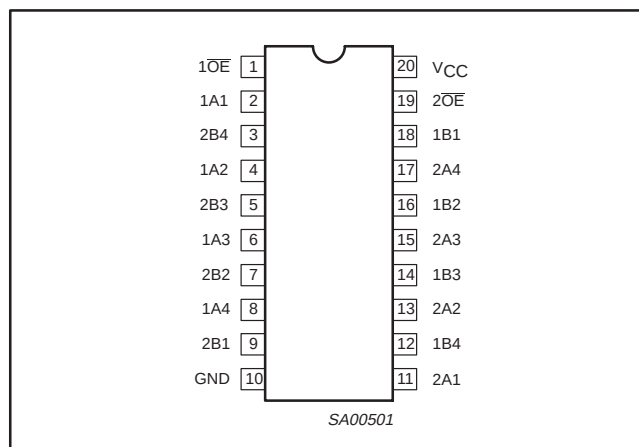


Figure 1. Pin configuration — SO, SSOP, QSOP, and TSSOP

PIN DESCRIPTION

PIN NUMBER	SYMBOL	NAME AND FUNCTION
1, 19	$1\overline{OE}$, $2\overline{OE}$	Output enable
2, 4, 6, 8	1A1–1A4	Inputs
11, 13, 15, 17	2A1–2A4	Inputs
18, 16, 14, 12	1B1–1B4	Outputs
9, 7, 5, 3	2B1–2B4	Outputs
10	GND	Ground (0V)
20	V_{CC}	Positive supply voltage

ORDERING INFORMATION

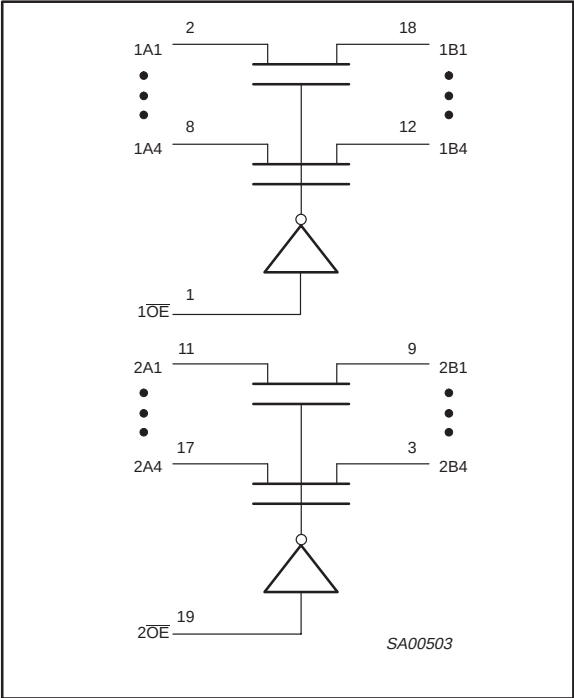
PACKAGES	TEMPERATURE RANGE	ORDER CODE	TOPSIDE MARK	DWG NUMBER
20-Pin Plastic TSSOP	$-40\text{ }^{\circ}\text{C}$ to $85\text{ }^{\circ}\text{C}$	CBT3244APW	CT3244A	SOT360-1
20-Pin Plastic SSOP (QSOP)	$-40\text{ }^{\circ}\text{C}$ to $85\text{ }^{\circ}\text{C}$	CBT3244ADS	CT3244ADS	SOT724-1
20-Pin Plastic SSOP	$-40\text{ }^{\circ}\text{C}$ to $85\text{ }^{\circ}\text{C}$	CBT3244ADB	CT3244A	SOT339-1
20-Pin Plastic SO	$-40\text{ }^{\circ}\text{C}$ to $85\text{ }^{\circ}\text{C}$	CBT3244AD	CBT3244AD	SOT163-1

Standard packing quantities and other packaging data is available at www.philipslogic.com/packaging.

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LOGIC SYMBOL



FUNCTION TABLE

INPUTS		OUTPUTS	
1OE	2OE	1A, 1B	2A, 2B
L	L	1A = 1B	2A = 2B
L	H	1A = 1B	Z
H	L	Z	2A = 2B
H	H	Z	Z

H = High voltage level
L = Low voltage level
Z = High-impedance "off" state

ABSOLUTE MAXIMUM RATINGS^{1, 2}

SYMBOL	PARAMETER	CONDITIONS	RATING	UNIT
V _{CC}	DC supply voltage		−0.5 to +7.0	V
I _{IK}	DC input diode current	V _I < 0 V	−18	mA
V _I	DC input voltage ³		−1.2 to +7.0	V
I _{OK}	DC output diode current	V _O < 0 V	−50	mA
V _{OUT}	DC output voltage ³	output in Off or HIGH state	−0.5 to +7	V
I _{OUT}	DC output current	output in LOW state	128	mA
T _{stg}	Storage temperature range		−65 to 150	°C

NOTES:

1. Stresses beyond those listed may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions beyond those indicated under "recommended operating conditions" is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.
2. The performance capability of a high-performance integrated circuit in conjunction with its thermal environment can create junction temperatures which are detrimental to reliability. The maximum junction temperature of this integrated circuit should not exceed 150°C.
3. The input and output voltage ratings may be exceeded if the input and output current ratings are observed.

RECOMMENDED OPERATING CONDITIONS

SYMBOL	PARAMETER	LIMITS		UNIT
		Min	Max	
V _{CC}	DC supply voltage	4.5	5.5	V
V _{IH}	HIGH-level input voltage	2.0	—	V
V _{IL}	LOW-level Input voltage	—	0.8	V
T _{amb}	Operating free-air temperature range	−40	+85	°C

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DC ELECTRICAL CHARACTERISTICS

SYMBOL	PARAMETER	TEST CONDITIONS	LIMITS			UNIT
			T _{amb} = −40 °C to +85 °C			
			Min	Typ ¹	Max	
V _{IK}	Input clamp voltage	V _{CC} = 4.5 V; I _I = −18 mA	—	—	−1.2	V
I _I	Input leakage current	V _{CC} = 5.5 V; V _I = GND or 5.5 V	—	—	±1	μA
I _{CC}	Quiescent supply current	V _{CC} = 5.5 V; I _O = 0, V _I = V _{CC} or GND	—	1	3	μA
ΔI _{CC}	Additional supply current per input pin ²	V _{CC} = 5.5 V, one input at 3.4 V, other inputs at V _{CC} or GND	—	—	2.5	mA
C _I	Control pins input capacitance	V _I = 3 V or 0 V, $\overline{OE}$ = V _{CC}	—	3	—	pF
C _{IO(OFF)}	Input/output capacitance	$\overline{OE}$ = V _{CC} = 5.0 V	—	3	—	pF
r _{on} ³	On-resistance	V _{CC} = 4.5 V; V _I = 0 V; I _I = 64 mA	—	4	7	Ω
		V _{CC} = 4.5 V; V _I = 0 V; I _I = 30 mA	—	4	7	
		V _{CC} = 4.5 V; V _I = 2.4 V; I _I = 15 mA	—	8	15	

NOTES:

1. All typical values are at $V_{CC} = 5\text{ V}$, $T_{amb} = 25\text{ }^{\circ}\text{C}$
2. This is the increase in supply current for each input that is at the specified TTL voltage level rather than V_{CC} or GND.
3. Measured by the voltage drop between the A and the B terminals at the indicated current through the switch.
On-state resistance is determined by the lowest voltage of the two (A or B) terminals.

AC CHARACTERISTICS

GND = 0 V; t_R ; $C_L = 50\text{ pF}$

SYMBOL	PARAMETER	FROM (INPUT)	TO (OUTPUT)	LIMITS		UNIT
				$T_{amb} = -40\text{ }^{\circ}\text{C to }+85\text{ }^{\circ}\text{C}$ $V_{CC} = +5.0\text{ V} \pm 0.5\text{ V}$		
				Min	Max	
t_{pd}	Propagation delay ¹	A or B	B or A	—	.25	ns
t_{en}	Output enable time to HIGH and LOW level	OE	A or B	1.0	5.6	ns
t_{dis}	Output disable time from HIGH and LOW level	OE	A or B	1.0	6.0	ns

NOTE:

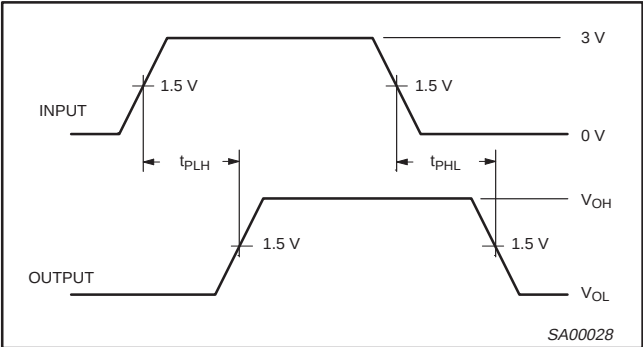
1. This parameter is warranted but not production tested. The propagation delay is based on the RC time constant of the typical on-state resistance of the switch and a load capacitance of 50 pF, when driven by an ideal voltage source (zero output impedance).

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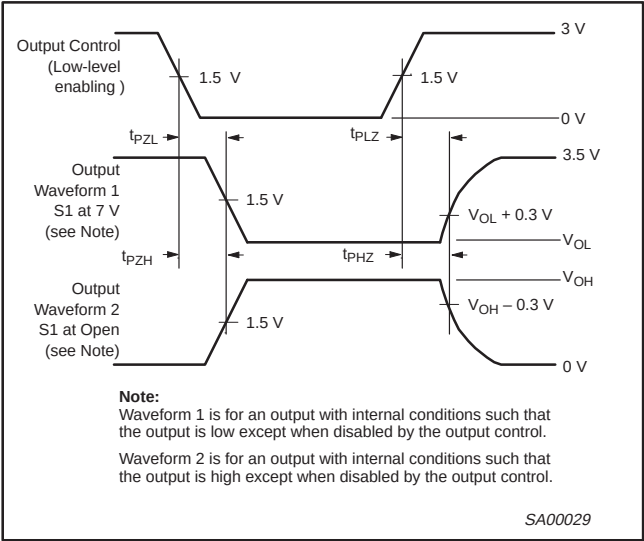
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AC WAVEFORMS

$V_M = 1.5\text{ V}$, $V_{IN} = \text{GND to } 3.0\text{ V}$



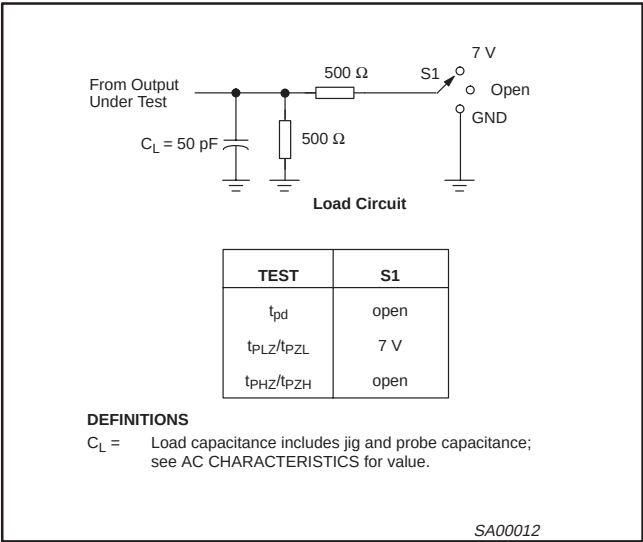
Waveform 1. Input to Output Propagation Delays



Note:
Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control.
Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.

Waveform 2. 3-State Output Enable and Disable Times

TEST CIRCUIT AND WAVEFORMS



NOTES:

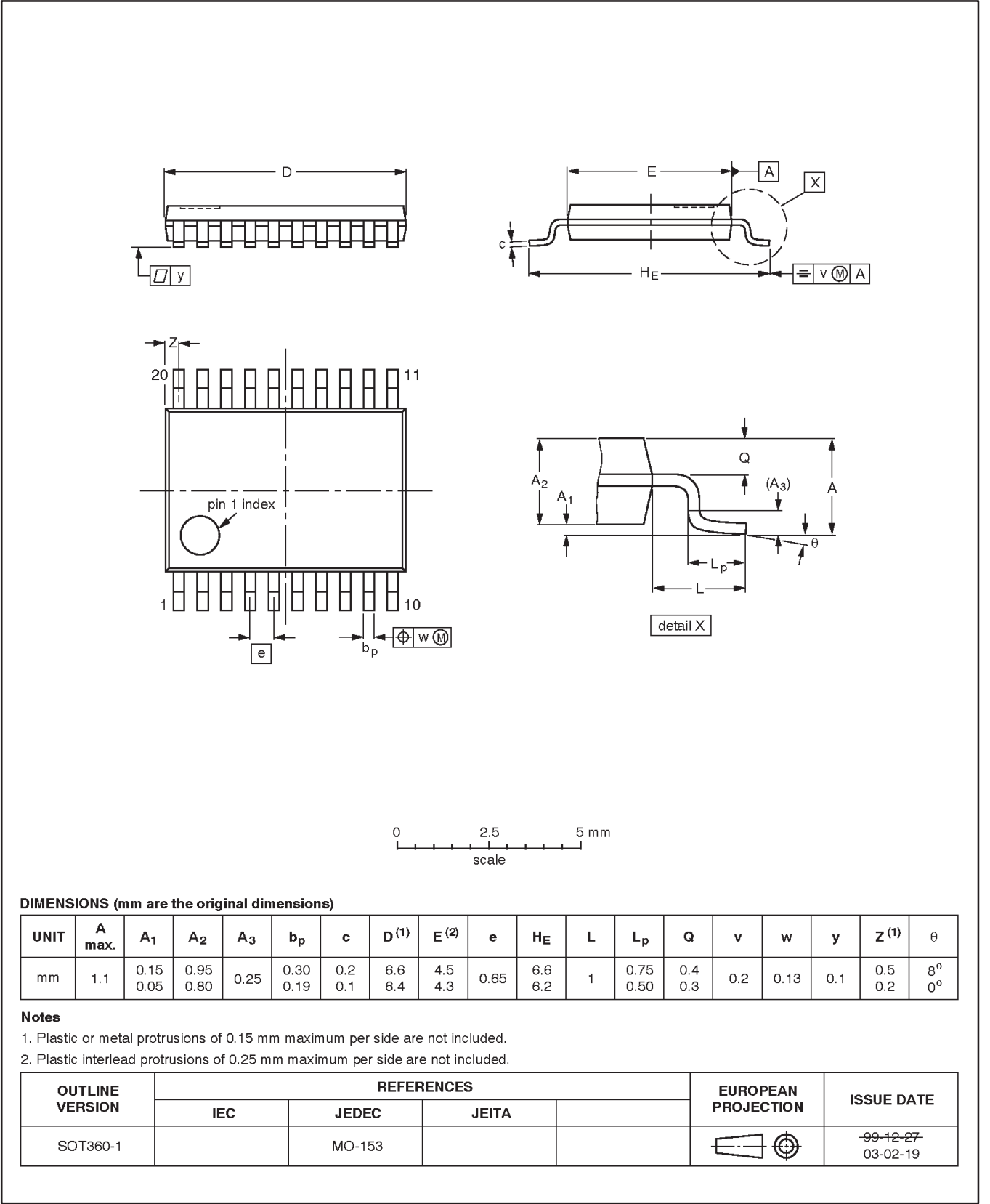
1. All input pulses are supplied by generators having the following characteristics: $PRR \leq 10\text{ MHz}$, $Z_O = 50\ \Omega$, $t_r \leq 2.5\text{ ns}$, $t_f \leq 2.5\text{ ns}$.
2. The outputs are measured one at a time with one transition per measurement.

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TSSOP20: plastic thin shrink small outline package; 20 leads; body width 4.4 mm

SOT360-1

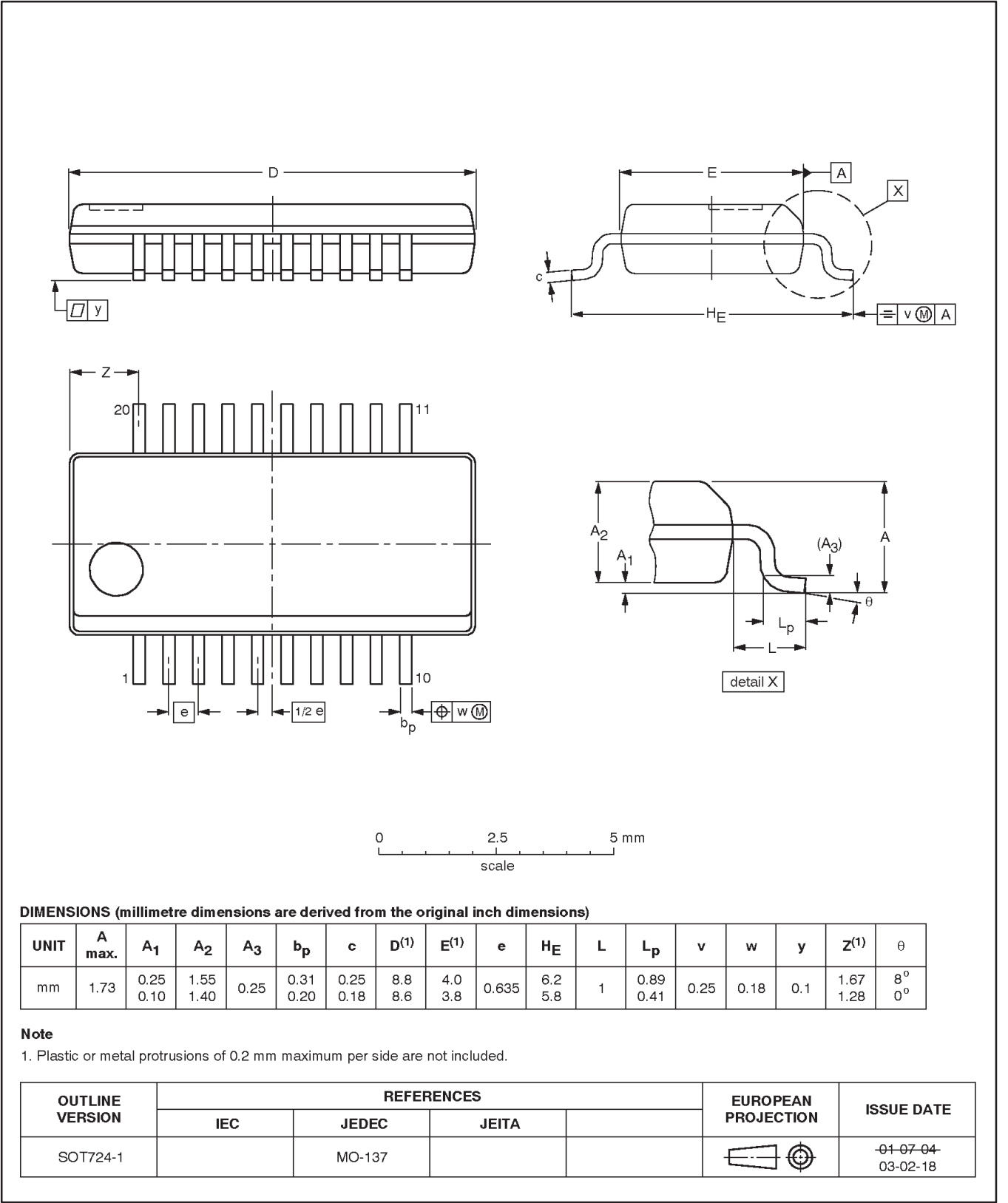


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SSOP20: plastic shrink small outline package; 20 leads;
body width 3.9 mm; lead pitch 0.635 mm

SOT724-1

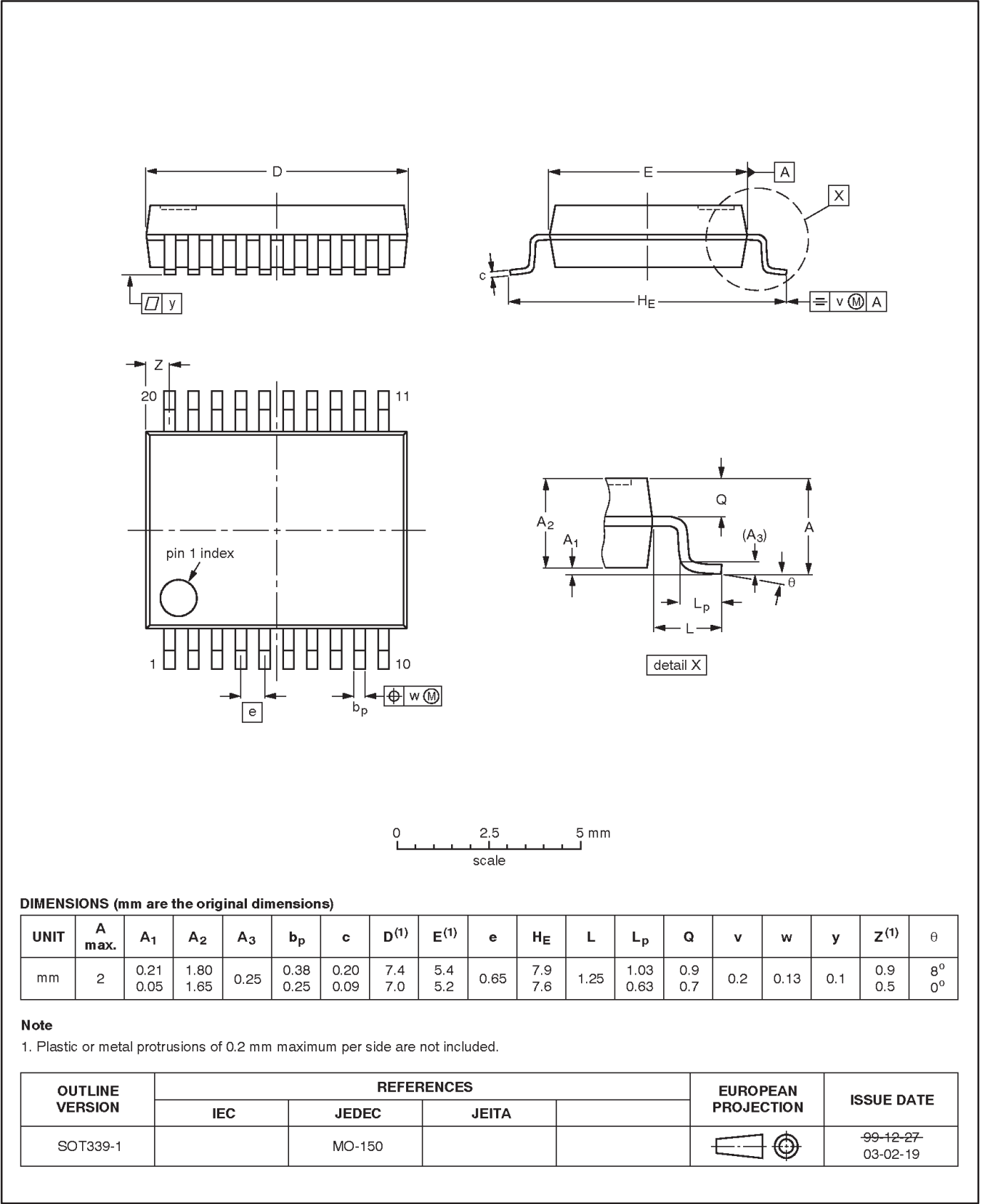


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SSOP20: plastic shrink small outline package; 20 leads; body width 5.3 mm

SOT339-1

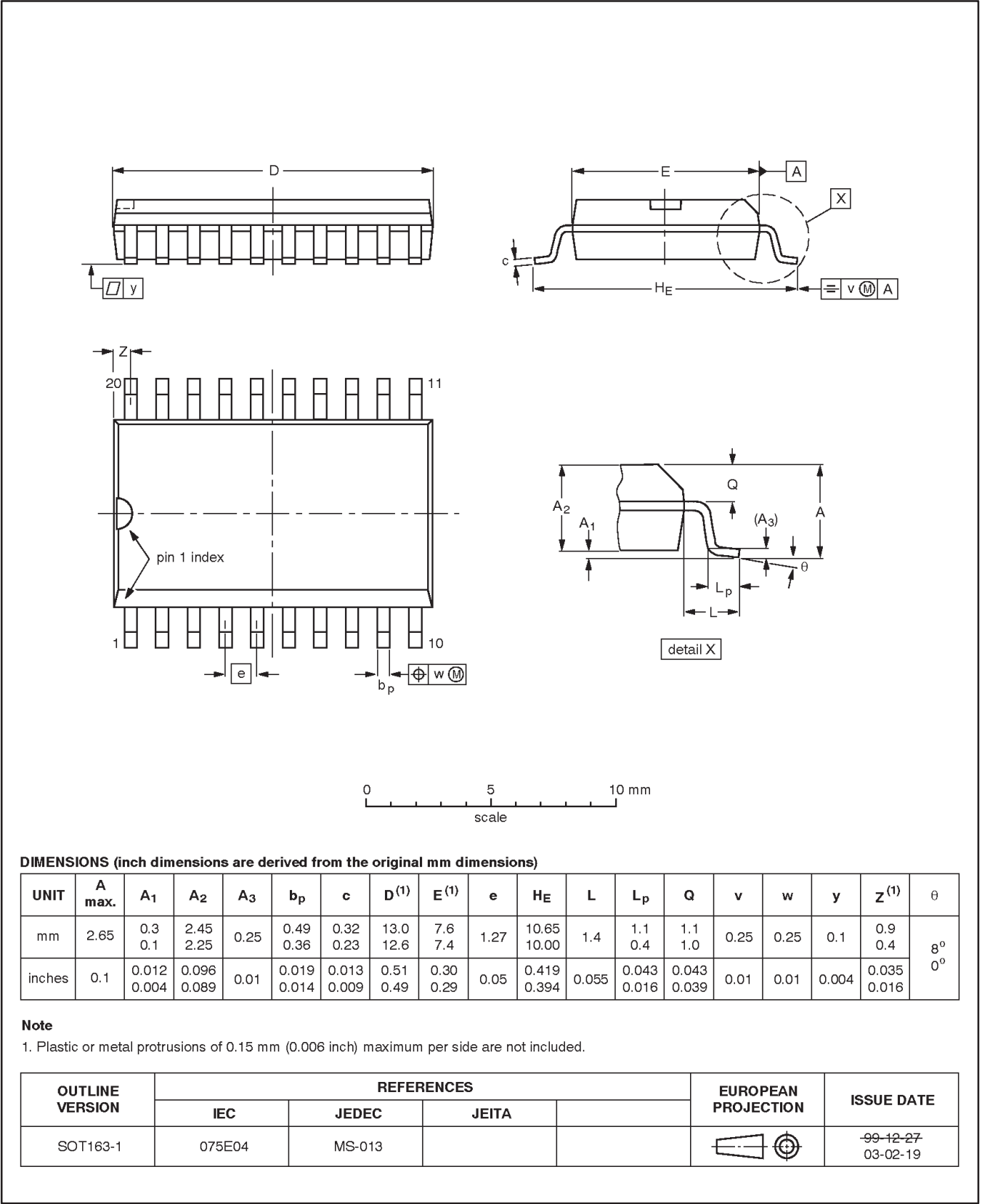


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SO20: plastic small outline package; 20 leads; body width 7.5 mm

SOT163-1



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REVISION HISTORY

Rev	Date	Description
_1	20040526	Product data sheet (9397 750 13281)

Data sheet status

Level	Data sheet status ^[1]	Product status ^{[2] [3]}	Definitions
I	Objective data	Development	This data sheet contains data from the objective specification for product development. Philips Semiconductors reserves the right to change the specification in any manner without notice.
II	Preliminary data	Qualification	This data sheet contains data from the preliminary specification. Supplementary data will be published at a later date. Philips Semiconductors reserves the right to change the specification without notice, in order to improve the design and supply the best possible product.
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[2] The product status of the device(s) described in this data sheet may have changed since this data sheet was published. The latest information is available on the Internet at URL <http://www.semiconductors.philips.com>.

[3] For data sheets describing multiple type numbers, the highest-level product status determines the data sheet status.

Definitions

Short-form specification — The data in a short-form specification is extracted from a full data sheet with the same type number and title. For detailed information see the relevant data sheet or data handbook.

Limiting values definition — Limiting values given are in accordance with the Absolute Maximum Rating System (IEC 60134). Stress above one or more of the limiting values may cause permanent damage to the device. These are stress ratings only and operation of the device at these or at any other conditions above those given in the Characteristics sections of the specification is not implied. Exposure to limiting values for extended periods may affect device reliability.

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