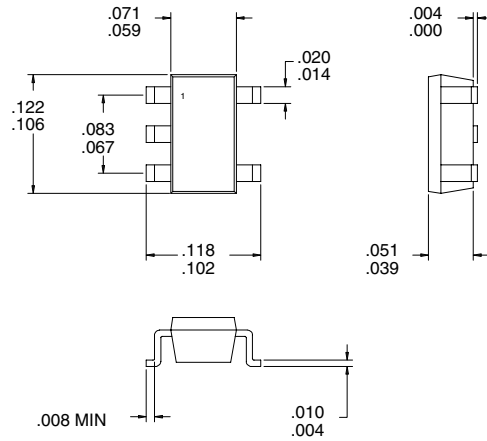


Typical Applications

- Broadband Gain Blocks
- Final PA for Low-Power Applications
- IF or RF Buffer Amplifiers
- Driver Stage for Power Amplifiers
- Oscillator Loop Amplifiers

Product Description

The RF2321 is a general purpose, low-cost silicon amplifier designed for operation from a 3V supply. The circuit configuration with resistive feedback allows for broadband cascadable amplification. Capacitive compensation extends the bandwidth of the amplifier and input stage design optimizes noise figure. The device is unconditionally stable and internally matched to 50Ω. The only external components required for specified performance are bypass and DC blocking capacitors (as shown in application schematic). The RF2321 is available in a very small industry-standard SOT-23 5-lead surface mount package, enabling compact designs which conserve board space.



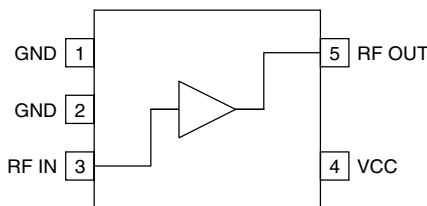
4
GENERAL PURPOSE AMPLIFIERS

Optimum Technology Matching® Applied

- | | | |
|--|-----------------------------------|--------------------------------------|
| <input checked="" type="checkbox"/> Si BJT | <input type="checkbox"/> GaAs HBT | <input type="checkbox"/> GaAs MESFET |
| <input type="checkbox"/> Si Bi-CMOS | <input type="checkbox"/> SiGe HBT | <input type="checkbox"/> Si CMOS |

Package Style: SOT-23-5

- Features**
- DC to >2000MHz Operation
 - 2.7V to 3.3V Single Supply
 - +3dBm Output IP3
 - 12dB Gain at 900MHz
 - 12dB Gain at 1900MHz
 - High Isolation (36dB at 900MHz)



Functional Block Diagram

Ordering Information

RF2321	3V General Purpose Amplifier
RF2321 PCBA	Fully Assembled Evaluation Board

RF Micro Devices, Inc.
 7625 Thorndike Road
 Greensboro, NC 27409, USA

Tel (336) 664 1233
 Fax (336) 664 0454
<http://www.rfmd.com>

Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage	4.0	V
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-40 to +150	°C

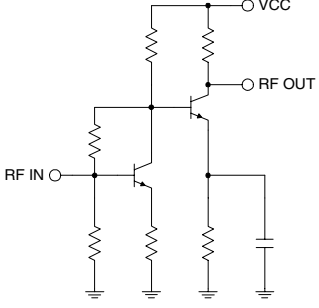


Caution! ESD sensitive device.

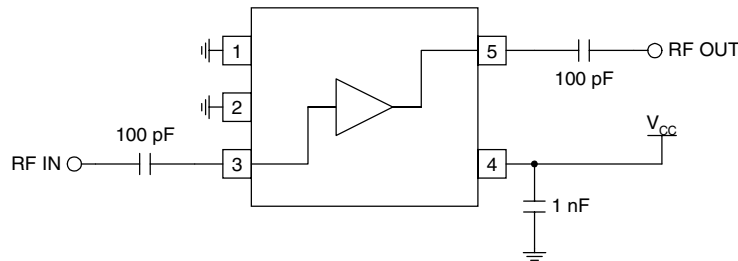
RF Micro Devices believes the furnished information is correct and accurate at the time of this printing. However, RF Micro Devices reserves the right to make changes to its products without notice. RF Micro Devices does not assume responsibility for the use of the described product(s).

4
GENERAL PURPOSE
AMPLIFIERS

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Overall					
Frequency Range		DC to >2000		MHz	T=27 °C, V _{CC} =3.0V
100MHz Performance					T=27 °C, V _{CC} =3.0V
Gain		12		dB	
Noise Figure		3.6		dB	
Output IP3		4		dBm	
Output P _{1dB}		-8		dBm	
Input Return Loss		15		dB	
Output Return Loss		11		dB	
Isolation		52		dB	
500MHz Performance					T=27 °C, V _{CC} =3.0V
Gain		12		dB	
Noise Figure		3.8		dB	
Output IP3		4		dBm	
Output P _{1dB}		-8		dBm	
Input Return Loss		15		dB	
Output Return Loss		11		dB	
Isolation		42		dB	
900MHz Performance					T=27 °C, V _{CC} =3.0V
Gain		12		dB	
Noise Figure		3.7		dB	
Output IP3		3		dBm	
Output P _{1dB}		-7		dBm	
Input Return Loss		13		dB	
Output Return Loss		9		dB	
Isolation		36		dB	
1000MHz Performance					T=27 °C, V _{CC} =3.0V
Gain		12		dB	
Noise Figure		3.7		dB	
Output IP3		3		dBm	
Output P _{1dB}		-8		dBm	
Input Return Loss		13		dB	
Output Return Loss		9		dB	
Isolation		35		dB	
2000MHz Performance					T=27 °C, V _{CC} =3.0V
Gain		12		dB	
Noise Figure		4.5		dB	
Output IP3		2		dBm	
Output P _{1dB}		-8		dBm	
Input Return Loss		8		dB	
Output Return Loss		9		dB	
Isolation		25		dB	
Power Supply					
Operating Voltage		3.0±10%		V	V _{CC} =3.0V
Operating Current		7.7		mA	

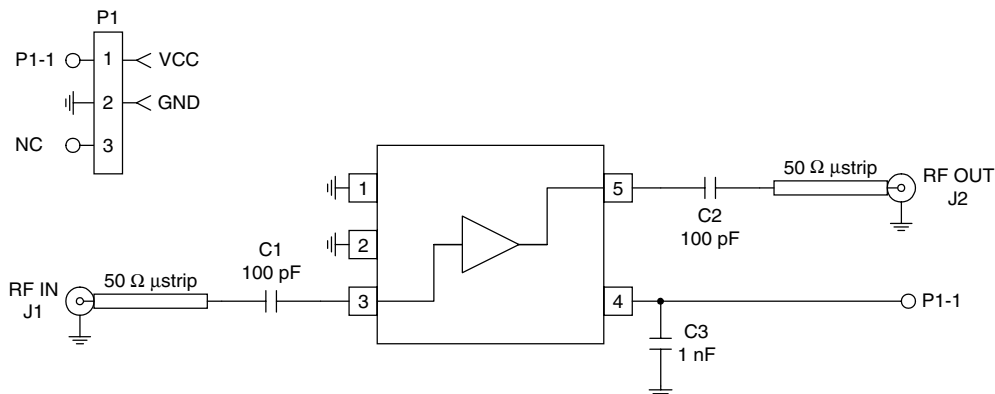
Pin	Function	Description	Interface Schematic
1	GND	Ground connection. Keep traces physically short and connect immediately to ground plane for best performance.	
2	GND	Same as pin 1.	
3	RF IN	RF input pin. This pin is not internally DC blocked and thus requires an external blocking capacitor suitable for the frequency of operation. The input impedance of this pin is internally matched to 50Ω using resistive feedback.	
4	VCC	Supply connection. This pin should be bypassed with a suitable capacitor(s).	
5	RF OUT	RF output and bias pin. The output impedance of this pin is internally matched to 50Ω using resistive feedback. Because DC biasing is present on this pin, a DC blocking capacitor should be used in most applications (see application schematic).	See pin 3 schematic.

Application Schematic

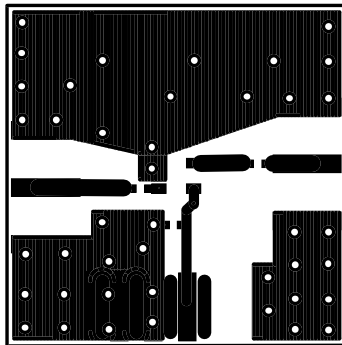
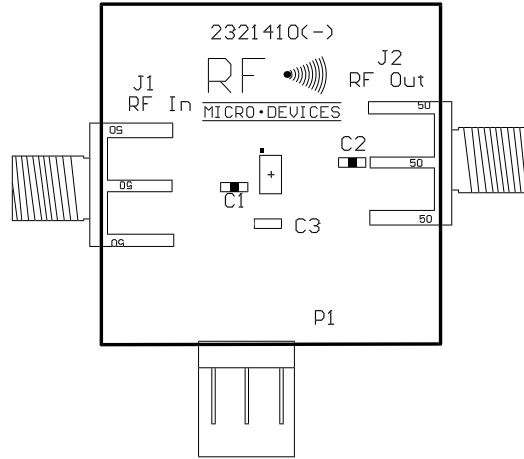


Evaluation Board Schematic

(Download [Bill of Materials](http://www.rfmd.com) from www.rfmd.com.)

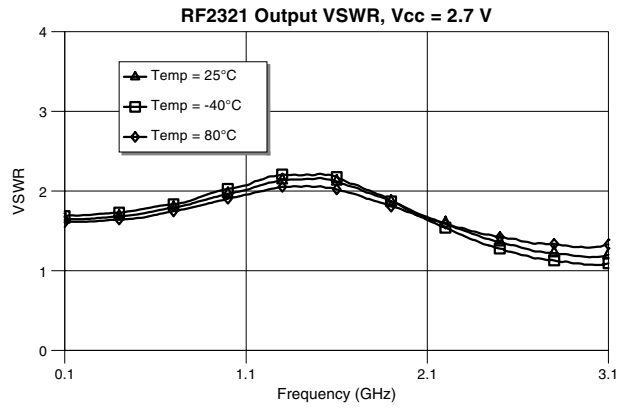
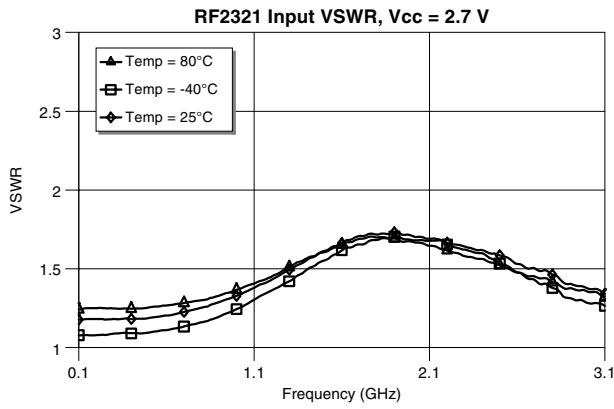
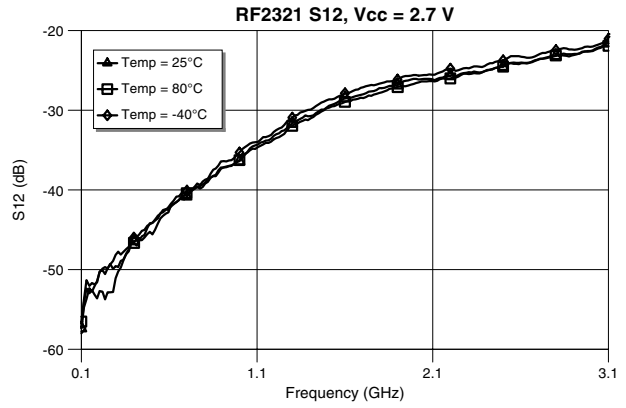
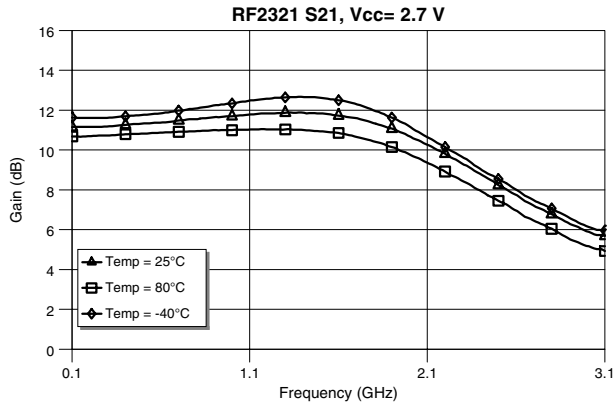


Evaluation Board Layout 1" x 1"

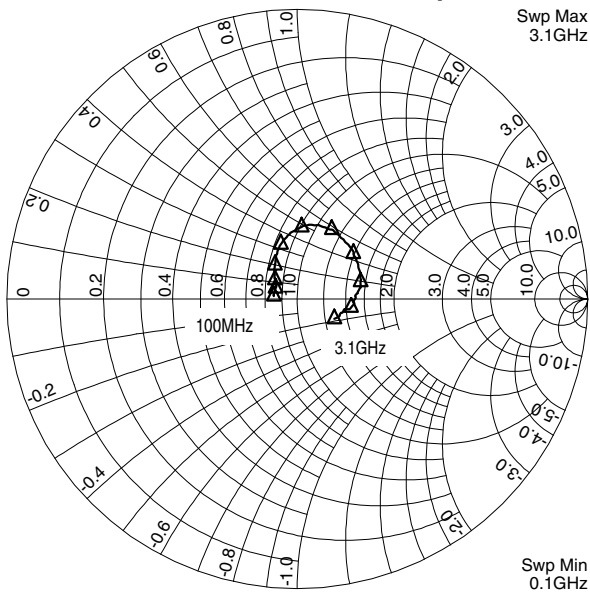


4

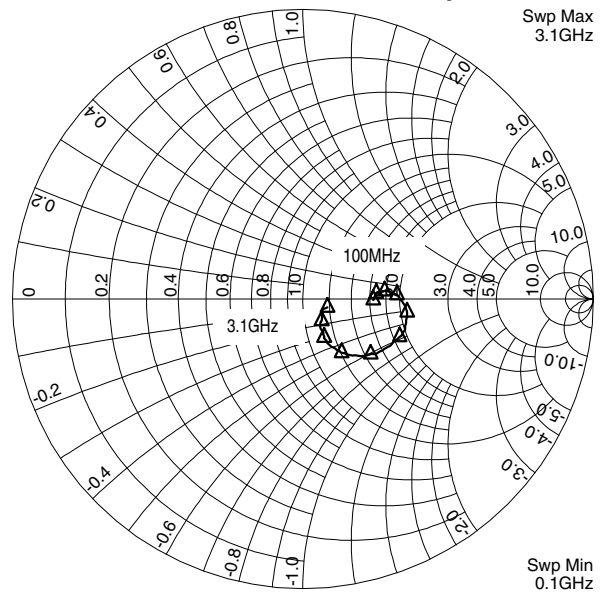
GENERAL PURPOSE
AMPLIFIERS

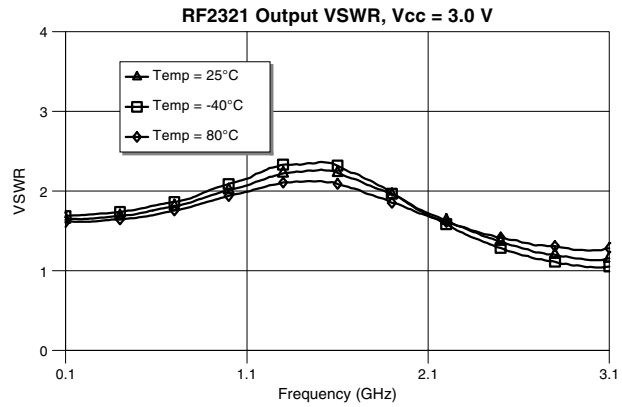
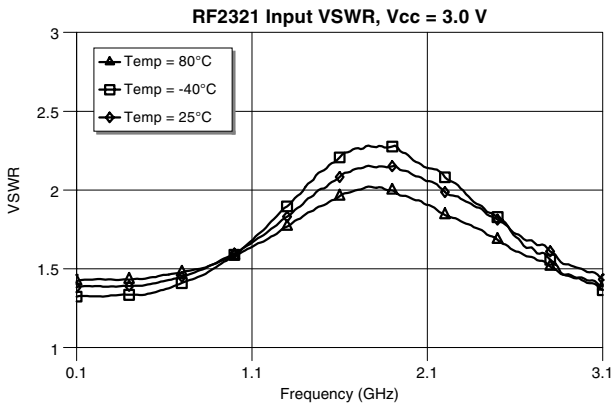
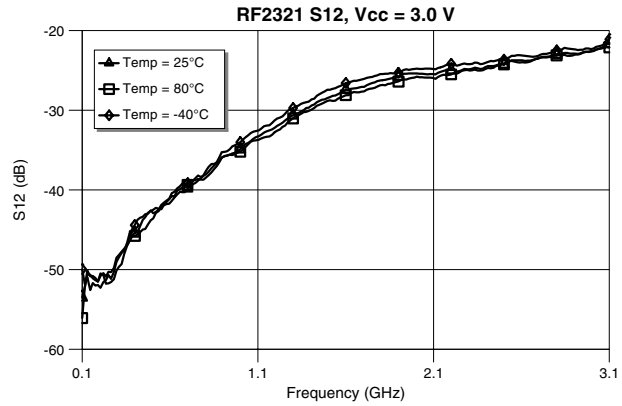
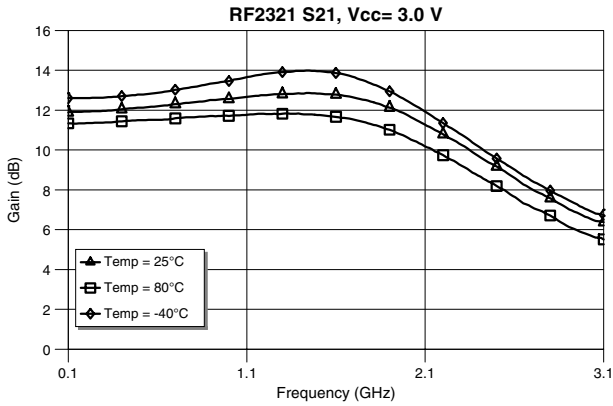


RF2321 S11, Vcc = 2.7 V, Temp = 25°C

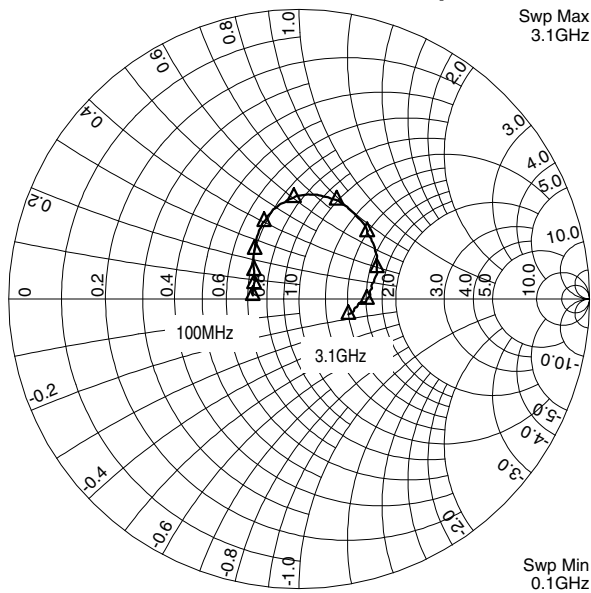


RF2321 S22, Vcc = 2.7 V, Temp= 25°C

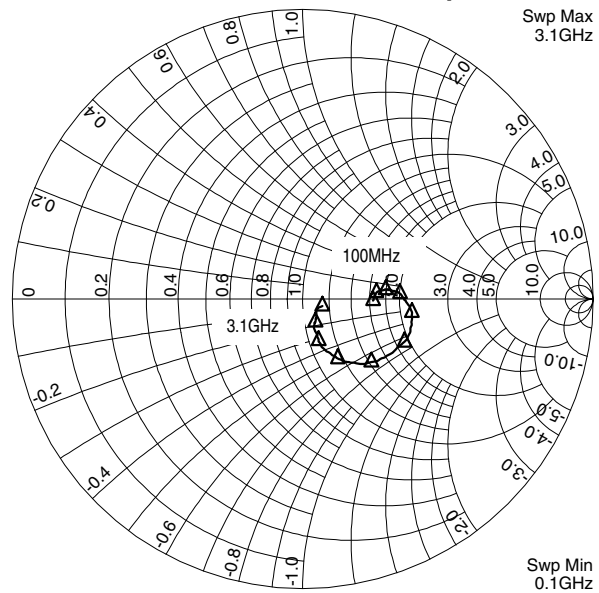


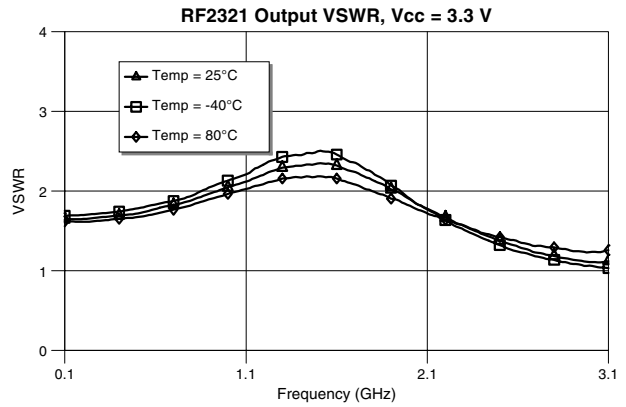
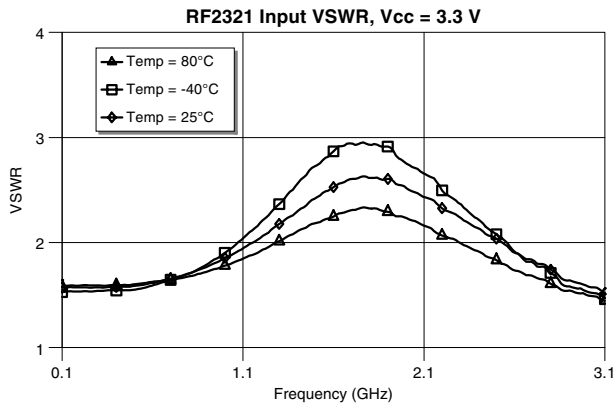
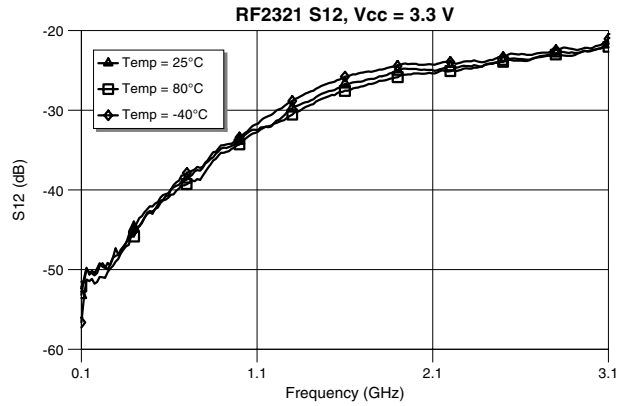
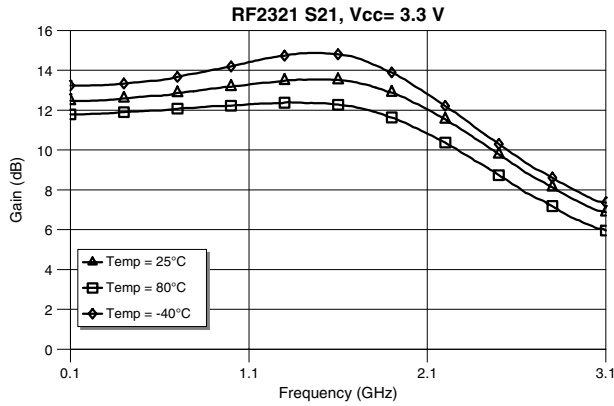


RF2321 S11, Vcc = 3.0 V, Temp = 25°C

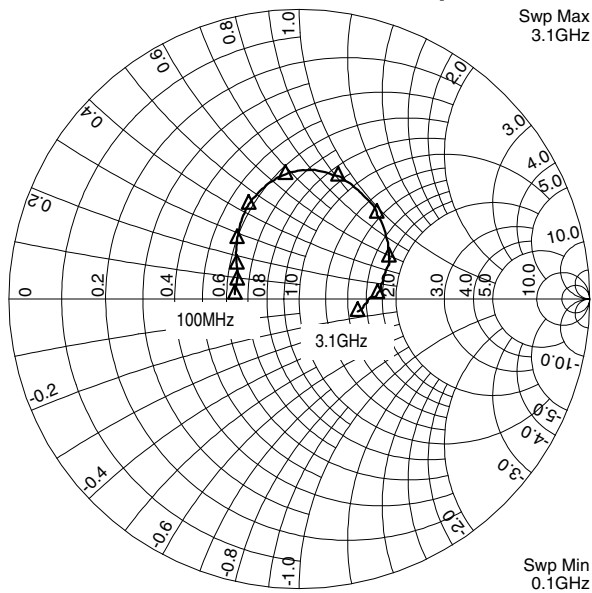


RF2321 S22, Vcc = 3.0 V, Temp = 25°C





RF2321 S11, Vcc = 3.3 V, Temp = 25°C



RF2321 S22, Vcc = 3.3 V, Temp= 25°C

