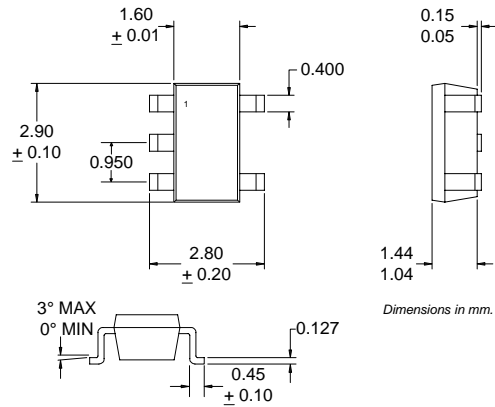


Typical Applications

- Broadband Gain Blocks
- Low Noise Amplifiers
- IF or RF Buffer Amplifiers
- Driver Stage for Power Amplifiers
- Oscillator Loop Amplifiers
- Receiver Front-Ends

Product Description

The RF2323 is a general purpose, low-cost silicon amplifier designed for operation from a 3V supply. The circuit configuration with resistive feedback allows for broadband cascadable amplification. Capacitive compensation extends the bandwidth of the amplifier and input stage design optimizes noise figure. The device is unconditionally stable and internally matched to 50Ω. The only external components required for specified performance are bypass and DC blocking capacitors (as shown in application schematic). The RF2323 is available in a very small industry-standard SOT-23 5-lead surface mount package, enabling compact designs which conserve board space.



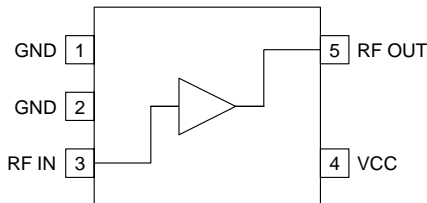
4
GENERAL PURPOSE AMPLIFIERS

Optimum Technology Matching® Applied

- | | | |
|--|-----------------------------------|--------------------------------------|
| <input checked="" type="checkbox"/> Si BJT | <input type="checkbox"/> GaAs HBT | <input type="checkbox"/> GaAs MESFET |
| <input type="checkbox"/> Si Bi-CMOS | <input type="checkbox"/> SiGe HBT | <input type="checkbox"/> Si CMOS |

Package Style: SOT 5-Lead

- Features**
- DC to >2000MHz Operation
 - 2.7V to 3.3V Single Supply
 - 2.3dB Noise Figure
 - 21dB Gain at 900MHz
 - 12dB Gain at 1900MHz
 - High Isolation (33dB at 900MHz)



Functional Block Diagram

Ordering Information

RF2323	3V General Purpose Amplifier
RF2323 PCBA	Fully Assembled Evaluation Board

RF Micro Devices, Inc. 7628 Thorndike Road Greensboro, NC 27409, USA	Tel (336) 664 1233 Fax (336) 664 0454 http://www.rfmd.com
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Absolute Maximum Ratings

Parameter	Rating	Unit
Supply Voltage	4.0	V
Operating Ambient Temperature	-40 to +85	°C
Storage Temperature	-40 to +150	°C



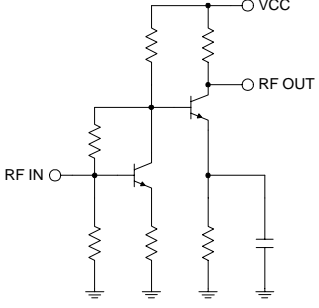
Caution! ESD sensitive device.

RF Micro Devices believes the furnished information is correct and accurate at the time of this printing. However, RF Micro Devices reserves the right to make changes to its products without notice. RF Micro Devices does not assume responsibility for the use of the described product(s).

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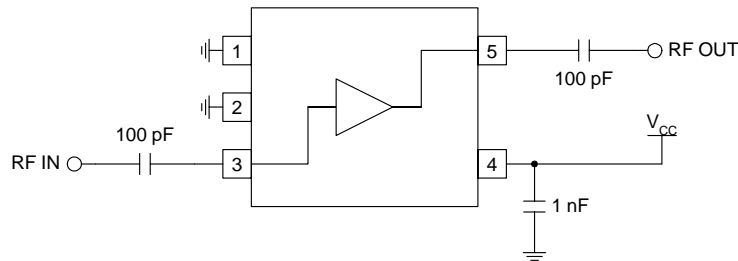
GENERAL PURPOSE
AMPLIFIERS

Parameter	Specification			Unit	Condition
	Min.	Typ.	Max.		
Overall Frequency Range		DC to >2000		MHz	T=27 °C, V _{CC} =3.0V
100MHz Performance Gain Noise Figure Output IP3 Output P _{1dB} Input Return Loss Output Return Loss Isolation		20 2.0 5 -6 19 12 50		dB dB dBm dBm dB dB dB	T=27 °C, V _{CC} =3.0V
500MHz Performance Gain Noise Figure Output IP3 Output P _{1dB} Input Return Loss Output Return Loss Isolation		19 2.3 4 -6 9 11 41		dB dB dBm dBm dB dB dB	T=27 °C, V _{CC} =3.0V
900MHz Performance Gain Noise Figure Output IP3 Output P _{1dB} Input Return Loss Output Return Loss Isolation	19.0	21 2.3 2 -7 6 8 33	21.5	dB dB dBm dBm dB dB dB	T=27 °C, V _{CC} =3.0V
1000MHz Performance Gain Noise Figure Output IP3 Output P _{1dB} Input Return Loss Output Return Loss Isolation		20 2.3 2 -7 6 8 32		dB dB dBm dBm dB dB dB	T=27 °C, V _{CC} =3.0V
2000MHz Performance Gain Noise Figure Output IP3 Output P _{1dB} Input Return Loss Output Return Loss Isolation		12 4.0 3 -7 14 20 27		dB dB dBm dBm dB dB dB	T=27 °C, V _{CC} =3.0V
Power Supply Operating Voltage Operating Current		3.0±10% 6.8		V mA	V _{CC} =3.0V

Pin	Function	Description	Interface Schematic
1	GND	Ground connection. Keep traces physically short and connect immediately to ground plane for best performance.	
2	GND	Same as pin 1.	
3	RF IN	RF input pin. This pin is not internally DC blocked and thus requires an external blocking capacitor suitable for the frequency of operation. The input impedance of this pin is internally matched to 50Ω using resistive feedback.	
4	VCC	Supply connection. This pin should be bypassed with a suitable capacitor(s).	
5	RF OUT	RF output and bias pin. The output impedance of this pin is internally matched to 50Ω using resistive feedback. Because DC biasing is present on this pin, a DC blocking capacitor should be used in most applications (see application schematic).	See pin 3 schematic.

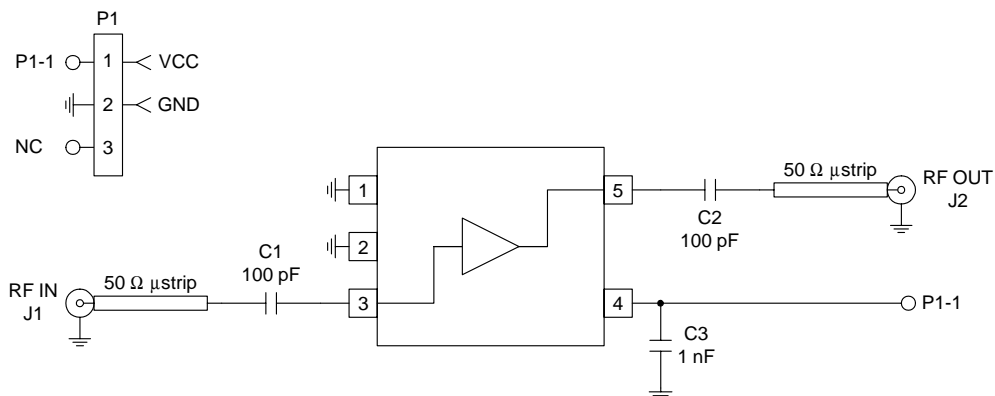
4
GENERAL PURPOSE
AMPLIFIERS

Application Schematic

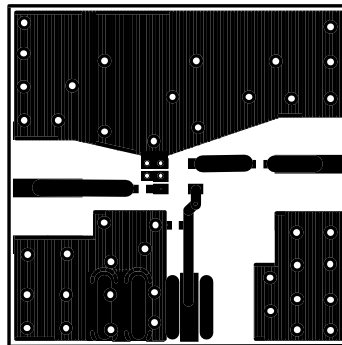
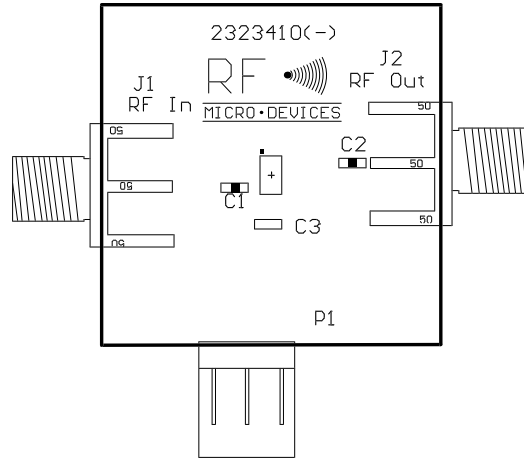


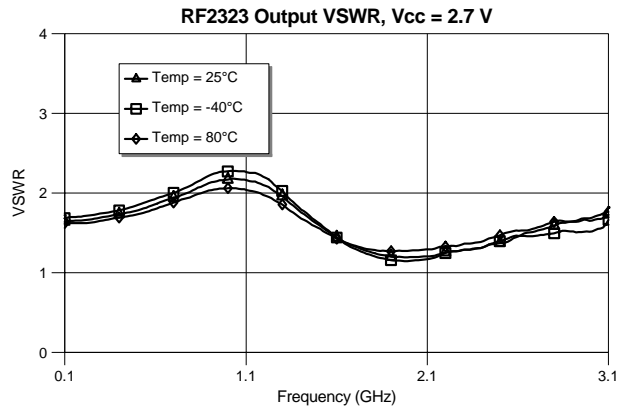
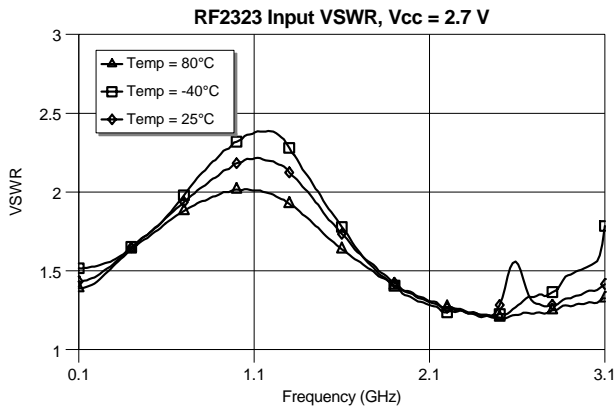
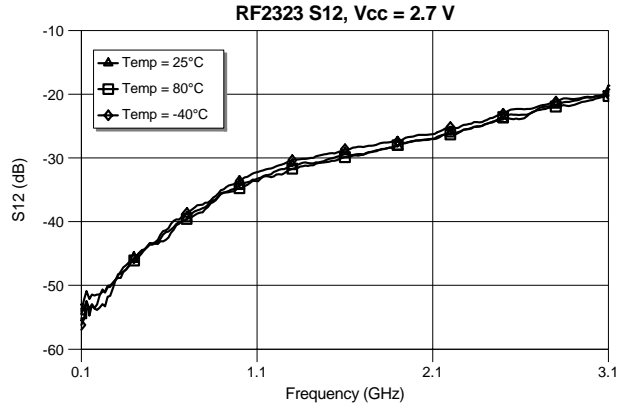
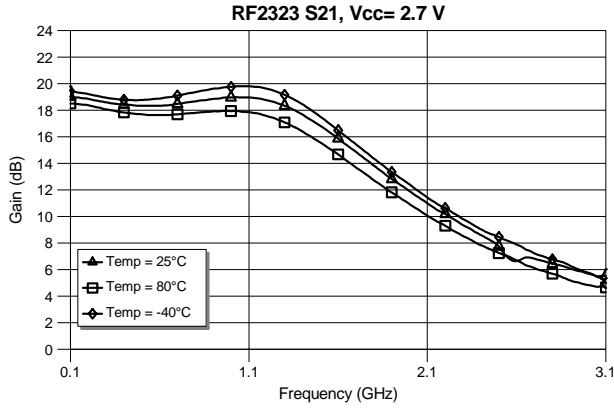
Evaluation Board Schematic

(Download [Bill of Materials](http://www.rfmd.com) from www.rfmd.com.)

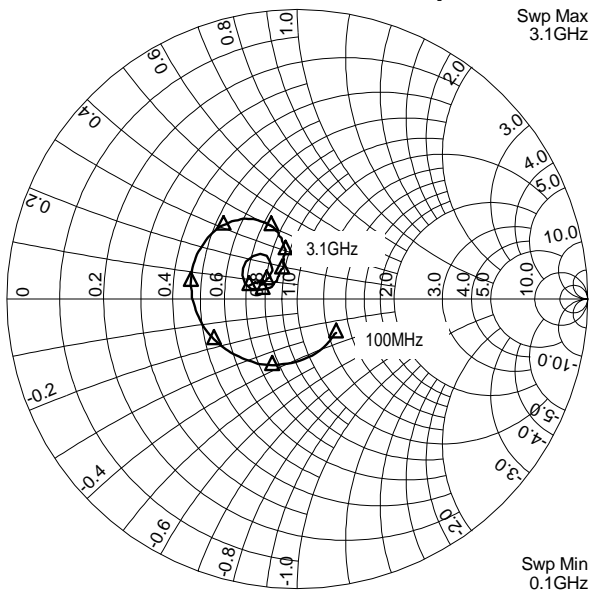


Evaluation Board Layout
1" x 1"

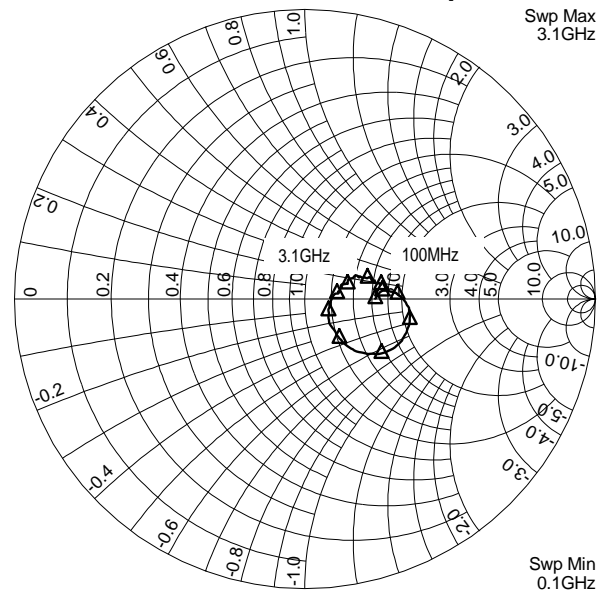


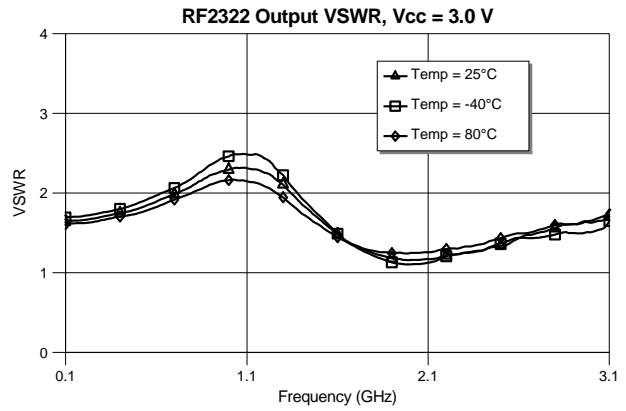
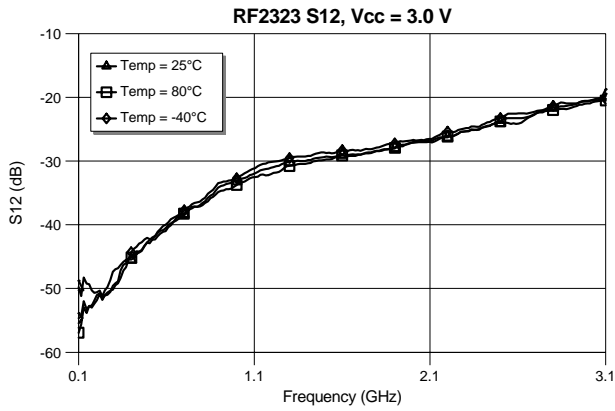
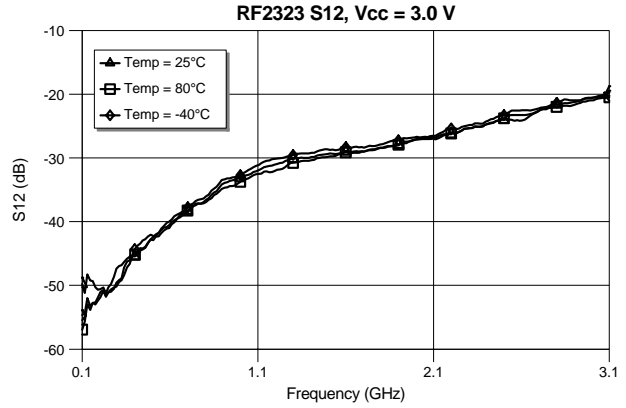
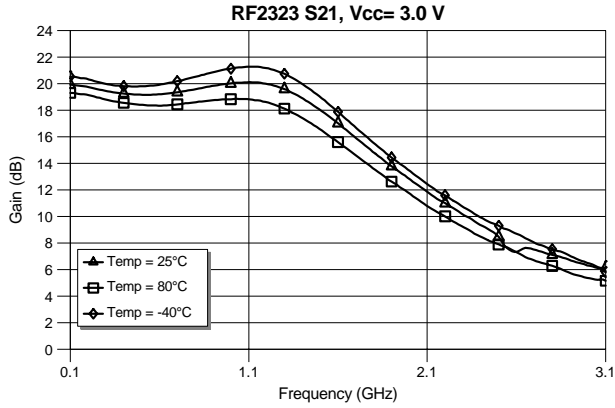


RF2323 S11, Vcc = 2.7 V, Temp = 25°C

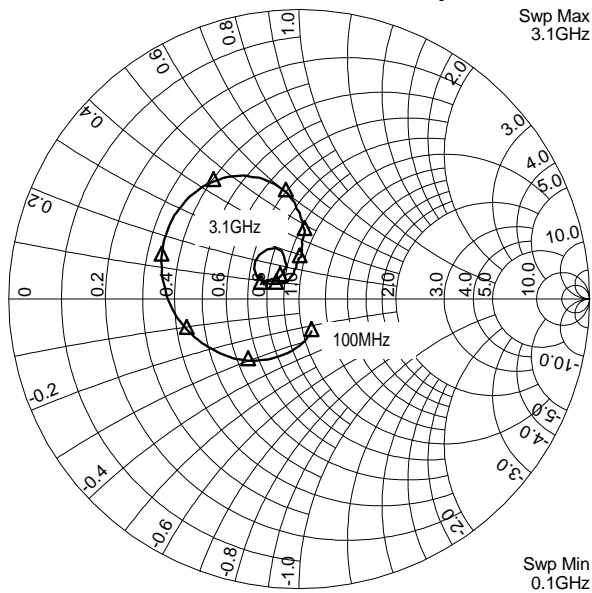


RF2323 S22, Vcc = 2.7 V, Temp= 25°C

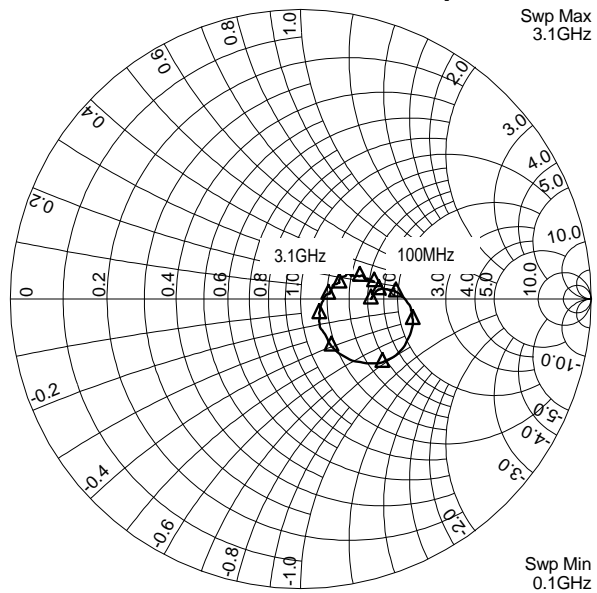


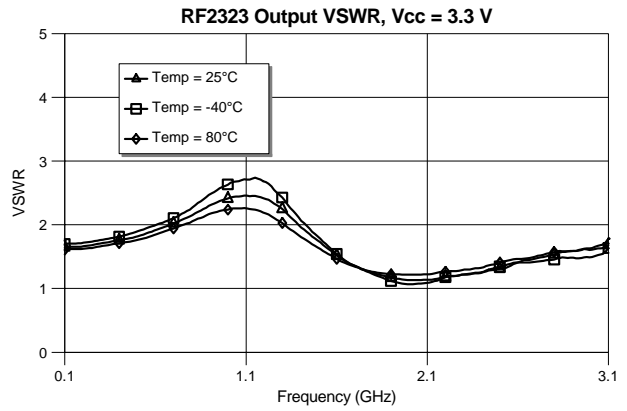
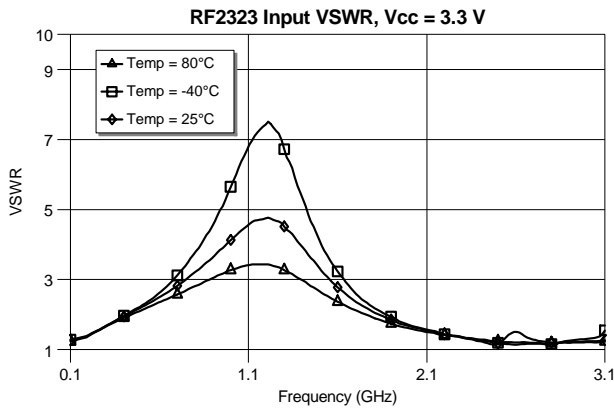
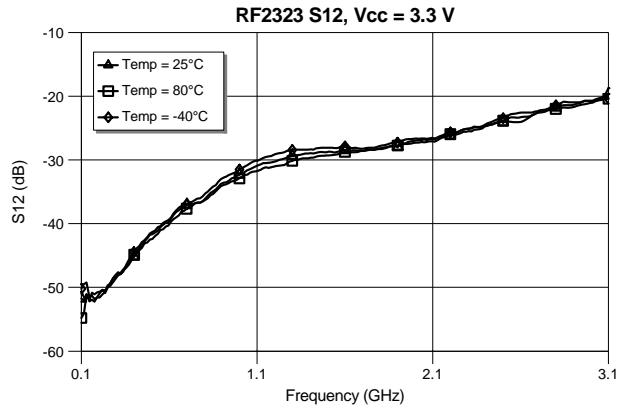
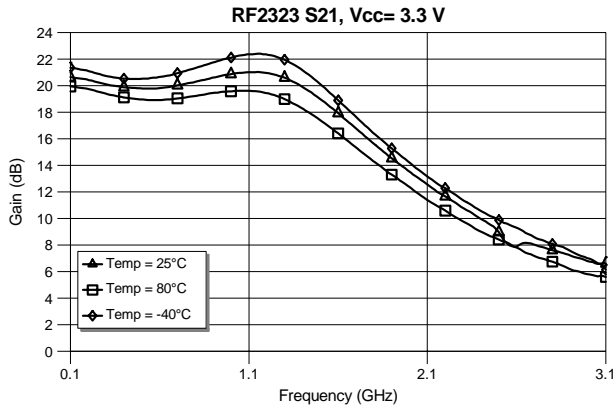


RF2323 S11, Vcc = 3.0 V, Temp = 25°C

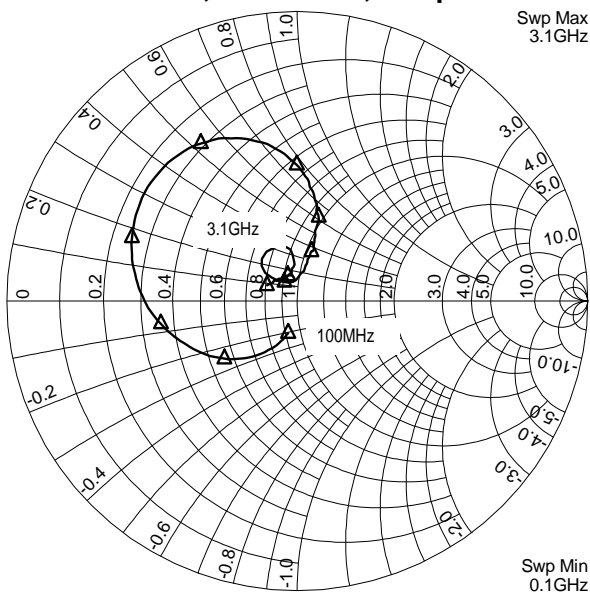


RF2323 S22, Vcc = 3.0 V, Temp = 25°C





RF2323 S11, Vcc = 3.3 V, Temp = 25°C



RF2323 S22, Vcc = 3.3 V, Temp= 25°C

