

1.2A Dual High-Speed MOSFET Drivers

Features

- Low Cost
- Latch-Up Protected: Will Withstand 500mA Reverse Output Current
- ESD Protected $\pm 2\text{kV}$
- High Peak Output Current: 1.2A
- Wide Operating Range
 - 4.5V to 16V
- High Capacitive Load Drive Capability: 1000pF in 38nsec
- Low Delay Time: 75nsec Max
- Logic Input Threshold Independent of Supply Voltage
- Output Voltage Swing to Within 25mV of Ground or V_{DD}
- Low Output Impedance: 8Ω

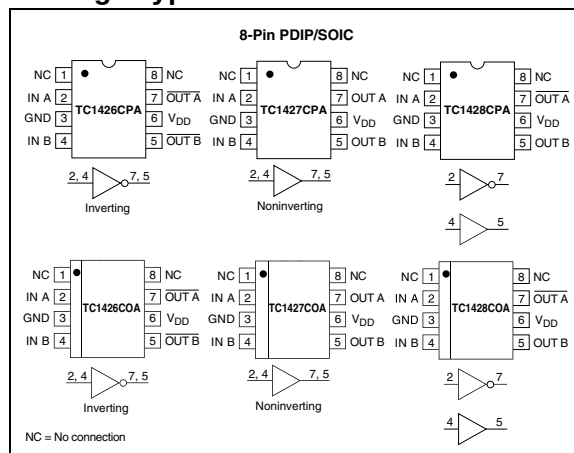
Applications

- Power MOSFET Drivers
- Switched Mode Power Supplies
- Pulse Transformer Drive
- Small Motor Controls
- Print Head Drive

Device Selection Table

Part Number	Package	Temp. Range
TC1426COA	8-Pin SOIC	0°C to +70°C
TC1426CPA	8-Pin PDIP	0°C to +70°C
TC1427COA	8-Pin SOIC	0°C to +70°C
TC1427CPA	8-Pin PDIP	0°C to +70°C
TC1428COA	8-Pin SOIC	0°C to +70°C
TC1428CPA	8-Pin PDIP	0°C to +70°C

Package Type



General Description

The TC1426/TC1427/TC1428 are a family of 1.2A dual high-speed drivers. CMOS fabrication is used for low power consumption and high efficiency.

These devices are fabricated using an epitaxial layer to effectively short out the intrinsic parasitic transistor responsible for CMOS latch-up. They incorporate a number of other design and process refinements to increase their long-term reliability.

The TC1426 is compatible with the bipolar DS0026, but only draws 1/5 of the quiescent current. The TC1426/TC1427/TC1428 are also compatible with the TC426/TC427/TC428, but with 1.2A peak output current rather than the 1.5A of the TC426/TC427/TC428 devices.

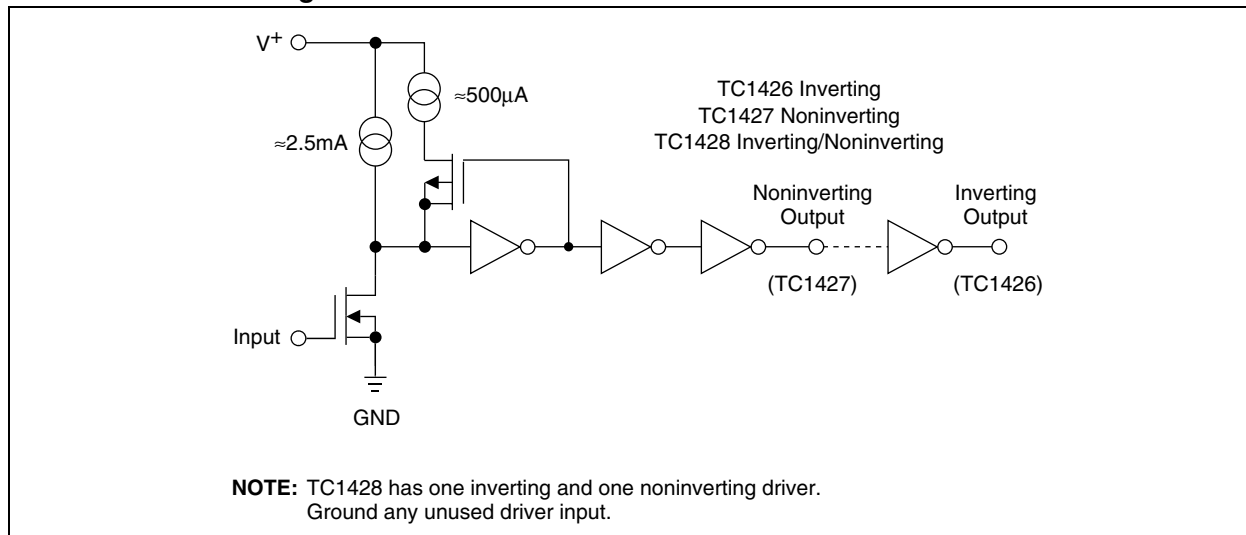
Other compatible drivers are the TC4426/TC4427/TC4428 and the TC4426A/TC4427A/TC4428A. The TC4426/TC4427/TC4428 have the added feature that the inputs can withstand negative voltage up to 5V with diode protection circuits. The TC4426A/TC4427A/TC4428A have matched input to output leading edge and falling edge delays, t_{D1} and t_{D2} , for processing short duration pulses in the 25 nanoseconds range. All of the above drivers are pin compatible.

The high-input impedance TC1426/TC1427/TC1428 drivers are CMOS/TTL input-compatible, do not require the speed-up needed by the bipolar devices, and can be directly driven by most PWM ICs.

This family of devices is available in inverting and non-inverting versions. Specifications have been optimized to achieve low-cost and high-performance devices, well-suited for the high-volume manufacturer.

TC1426/TC1427/TC1428

Functional Block Diagram



TC1426/TC1427/TC1428

1.0 ELECTRICAL CHARACTERISTICS

Absolute Maximum Ratings*

Supply Voltage.....	+18V
Input Voltage, Any Terminal	$V_{DD} + 0.3V$ to $GND - 0.3V$
Power Dissipation ($T_A \leq 70^\circ C$)	
PDIP	730mW
SOIC	470mW
Derating Factor	
PDIP	8mW/ $^\circ C$
SOIC	4mW/ $^\circ C$
Operating Temperature Range	
C Version	$0^\circ C$ to $+70^\circ C$
Storage Temperature Range	$-65^\circ C$ to $+150^\circ C$

*Stresses above those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only and functional operation of the device at these or any other conditions above those indicated in the operation sections of the specifications is not implied. Exposure to Absolute Maximum Rating conditions for extended periods may affect device reliability.

TC1426/TC1427/TC1428 ELECTRICAL SPECIFICATIONS

Electrical Characteristics: $T_A = +25^\circ C$, with $4.5V \leq V_{DD} \leq 16V$, unless otherwise noted.						
Symbol	Parameter	Min	Typ	Max	Units	Test Conditions
Input						
V_{IH}	Logic 1, High Input Voltage	3	—	—	V	
V_{IL}	Logic 0, Low Input Voltage	—	—	0.8	V	
I_{IN}	Input Current	-1	—	1	μA	$0V \leq V_{IN} \leq V_{DD}$
Output						
V_{OH}	High Output Voltage	$V_{DD} - 0.025$	—	—	V	Figure 3-1, Figure 3-2
V_{OL}	Low Output Voltage	—	—	0.025	V	Figure 3-1, Figure 3-2
R_O	Output Resistance	—	12 8	18 12	Ω	$I_{OUT} = 10mA$, $V_{DD} = 16V$
I_{PK}	Peak Output Current	—	1.2	—	A	
I_{REV}	Latch-Up Current Withstand Reverse Current	—	>500	—	mA	
Switching Time (Note 1)						
t_R	Rise Time	—	—	35	nsec	Figure 3-1, Figure 3-2
t_F	Fall Time	—	—	25	nsec	Figure 3-1, Figure 3-2
t_{D1}	Delay Time	—	—	75	nsec	Figure 3-1, Figure 3-2
t_{D2}	Delay Time	—	—	75	nsec	Figure 3-1, Figure 3-2
Power Supply						
I_S	Power Supply Current	— —	— —	9 0.5	mA	$V_{IN} = 3V$ (Both Inputs) $V_{IN} = 0V$ (Both Inputs)

Note 1: Switching times ensured by design.

TC1426/TC1427/TC1428

TC1426/TC1427/TC1428 ELECTRICAL SPECIFICATIONS (CONTINUED)

Electrical Characteristics: Over operating temperature range with $4.5V \leq V_{DD} \leq 16V$, unless otherwise noted.

Symbol	Parameter	Min	Typ	Max	Units	Test Conditions
Input						
V_{IH}	Logic 1, High Input Voltage	3	—	—	V	
V_{IL}	Logic 0, Low Input Voltage	—	—	0.8	V	
I_{IN}	Input Current	-10	—	10	μA	$0V \leq V_{IN} \leq V_{DD}$
Output						
V_{OH}	High Output Voltage	$V_{DD} - 0.025$	—	—	V	Figure 3-1, Figure 3-2
V_{OL}	Low Output Voltage	—	—	0.025	V	Figure 3-1, Figure 3-2
R_O	Output Resistance	—	15 10	23 18	Ω	$I_{OUT} = 10mA$, $V_{DD} = 16V$
I_{REV}	Latch-Up Current Withstand Reverse Current	—	>500	—	mA	
Switching Time (Note 1)						
t_R	Rise Time	—	—	60	nsec	Figure 3-1, Figure 3-2
t_F	Fall Time	—	—	40	nsec	Figure 3-1, Figure 3-2
t_{D1}	Delay Time	—	—	125	nsec	Figure 3-1, Figure 3-2
t_{D2}	Delay Time	—	—	125	nsec	Figure 3-1, Figure 3-2
Power Supply						
I_S	Power Supply Current	—	—	13 0.7	mA	$V_{IN} = 3V$ (Both Inputs) $V_{IN} = 0V$ (Both Inputs)

Note 1: Switching times ensured by design.

2.0 PIN DESCRIPTIONS

The descriptions of the pins are listed in Table 2-1.

TABLE 2-1: PIN FUNCTION TABLE

Pin No. (8-Pin PDIP, SOIC)	Symbol	Description
1	NC	No connection.
2	IN A	Control input A, TTL/CMOS compatible logic input.
3	GND	Ground.
4	IN B	Control input B, TTL/CMOS compatible logic input.
5	OUT B	Output B, CMOS totem-pole output.
6	V _{DD}	Supply input, 4.5V to 16V.
7	OUT A	Output A, CMOS totem-pole output.
8	NC	No connection.

TC1426/TC1427/TC1428

3.0 APPLICATIONS INFORMATION

3.1 SUPPLY BYPASSING

Large currents are required to charge and discharge capacitive loads quickly. For example, charging a 1000pF load to 16V in 25nsec requires a 0.8A current from the device's power supply.

To guarantee low supply impedance over a wide frequency range, a parallel capacitor combination is recommended for supply bypassing. Low-inductance ceramic MLC capacitors with short lead lengths (<0.5-in.) should be used. A 1.0μF film capacitor in parallel with one or two 0.1μF ceramic MLC capacitors normally provides adequate bypassing.

3.2 GROUNDING

The TC1426 and TC1428 contain inverting drivers. Individual ground returns for the input and output circuits or a ground plane should be used. This will reduce negative feedback that causes degradation in switching speed characteristics.

3.3 INPUT STAGE

The input voltage level changes the no-load or quiescent supply current. The N-channel MOSFET input stage transistor drives a 2.5mA current source load. With a logic "1" input, the maximum quiescent supply current is 9mA. Logic "0" input level signals reduce quiescent current to 500μA maximum. **Unused driver inputs must be connected to V_{DD} or GND.** Minimum power dissipation occurs for logic "0" inputs for the TC1426/TC1427/TC1428.

The drivers are designed with 100mV of hysteresis. This provides clean transitions and minimizes output stage current spiking when changing states. Input voltage thresholds are approximately 1.5V, making a logic "1" input any voltage greater than 1.5V up to V_{DD} . Input current is less than 1μA over this range.

The TC1426/TC1427/TC1428 may be directly driven by the TL494, SG1526/27, TC38C42, TC170 and similar switch-mode power supply integrated circuits.

FIGURE 3-1: INVERTING DRIVER SWITCHING TIME

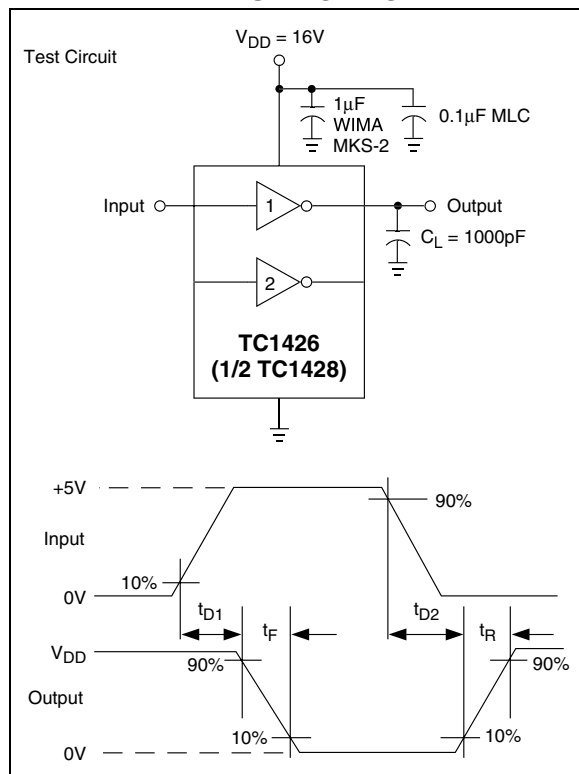
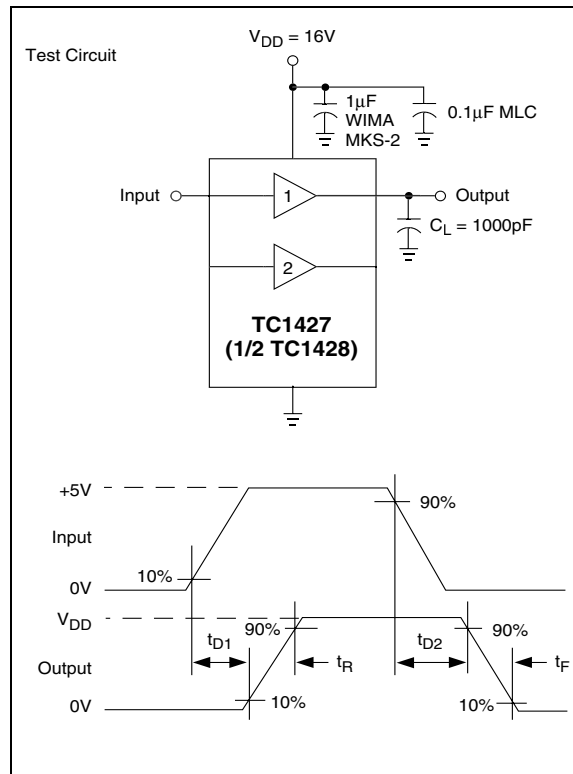
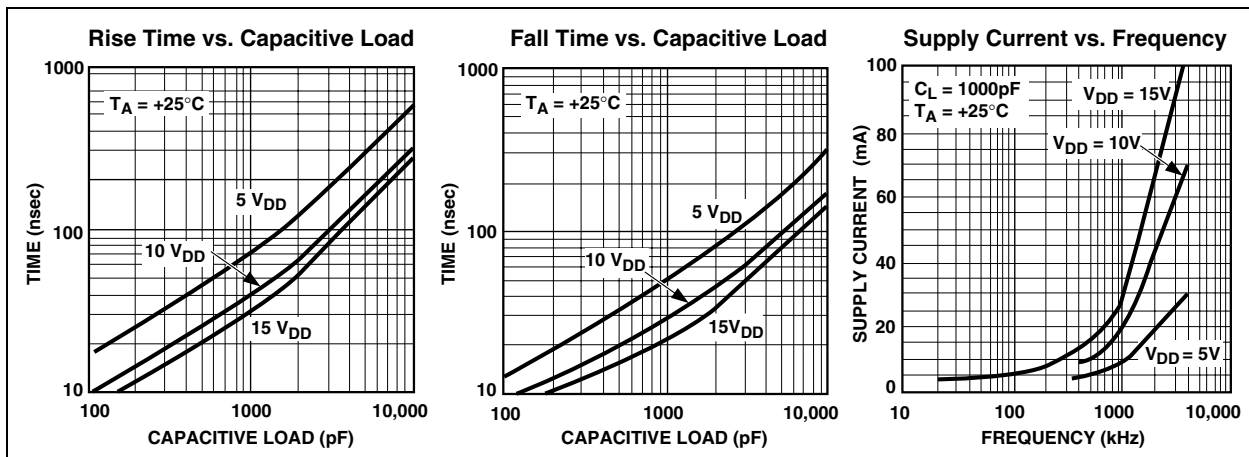
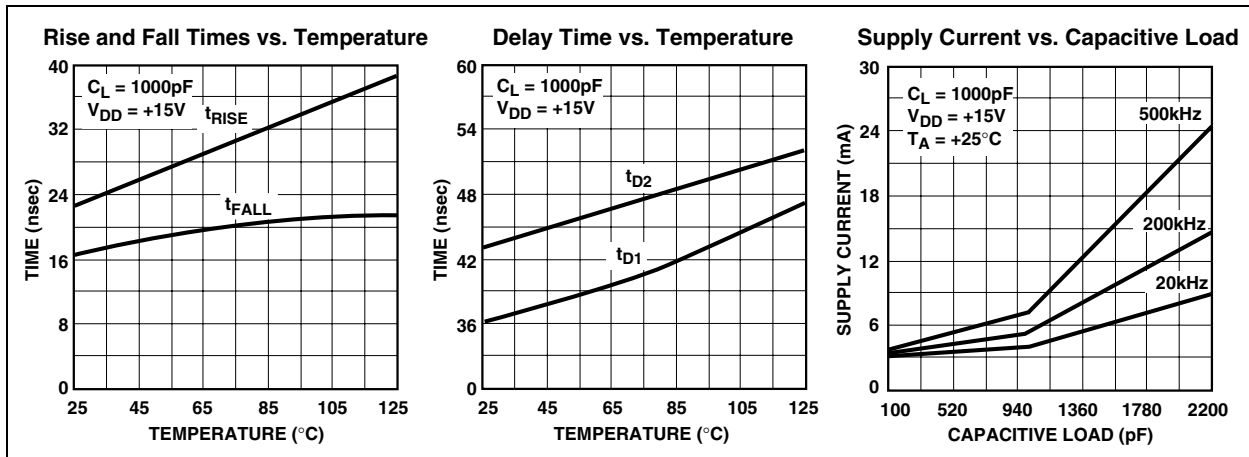
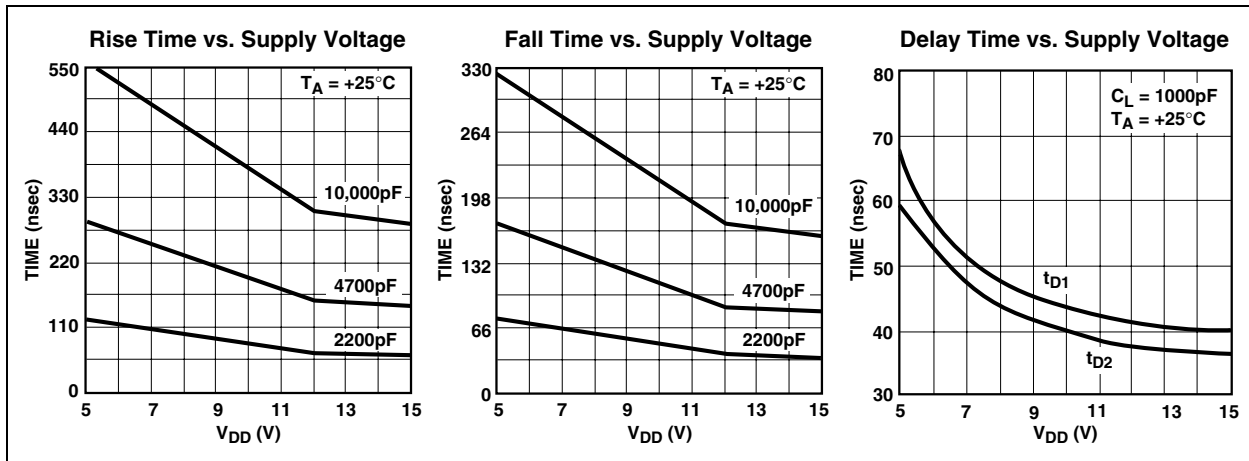


FIGURE 3-2: NONINVERTING DRIVER SWITCHING TIME



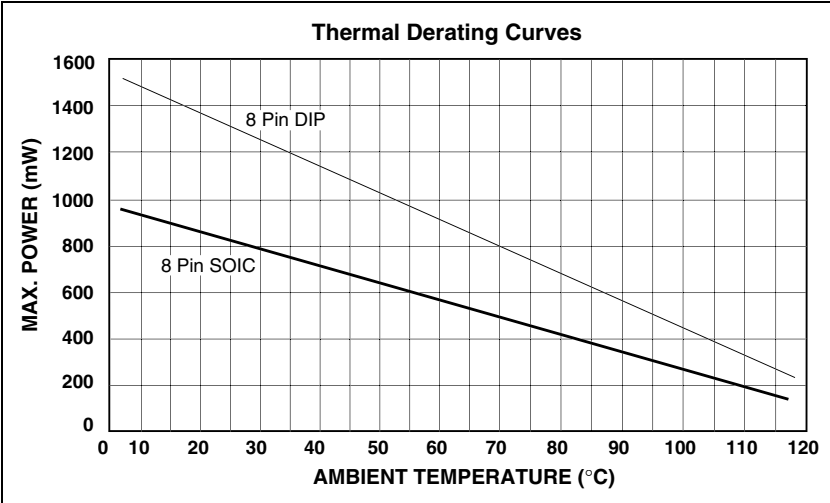
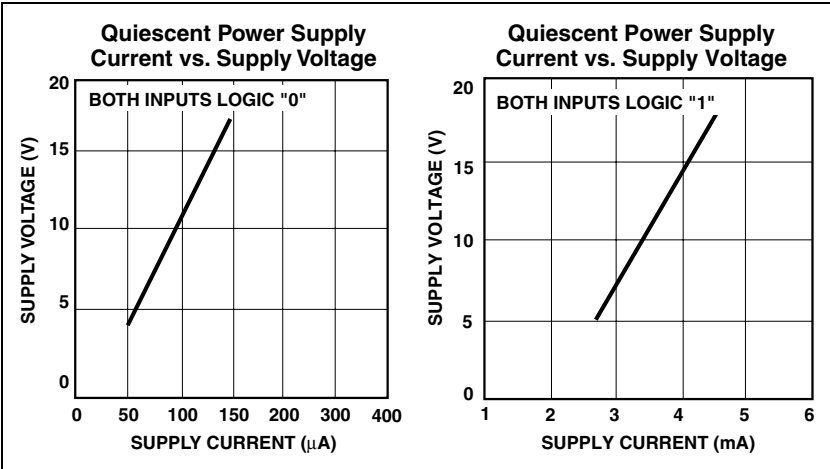
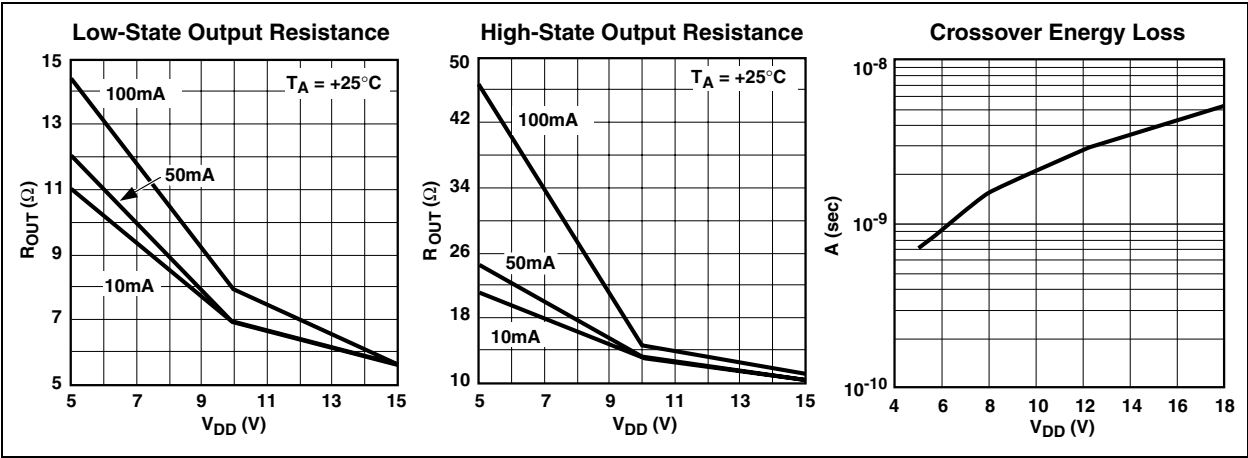
4.0 TYPICAL CHARACTERISTICS

Note: The graphs and tables provided following this note are a statistical summary based on a limited number of samples and are provided for informational purposes only. The performance characteristics listed herein are not tested or guaranteed. In some graphs or tables, the data presented may be outside the specified operating range (e.g., outside specified power supply range) and therefore outside the warranted range.



TC1426/TC1427/TC1428

TYPICAL CHARACTERISTICS (CONTINUED)

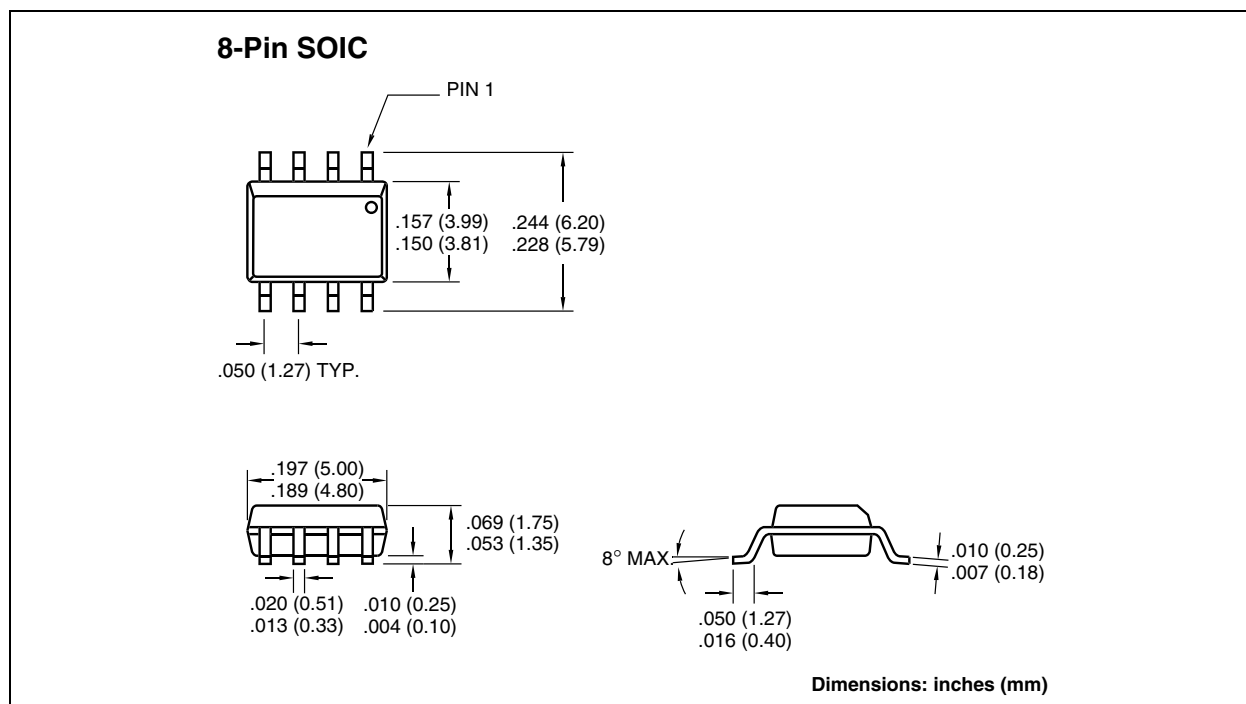
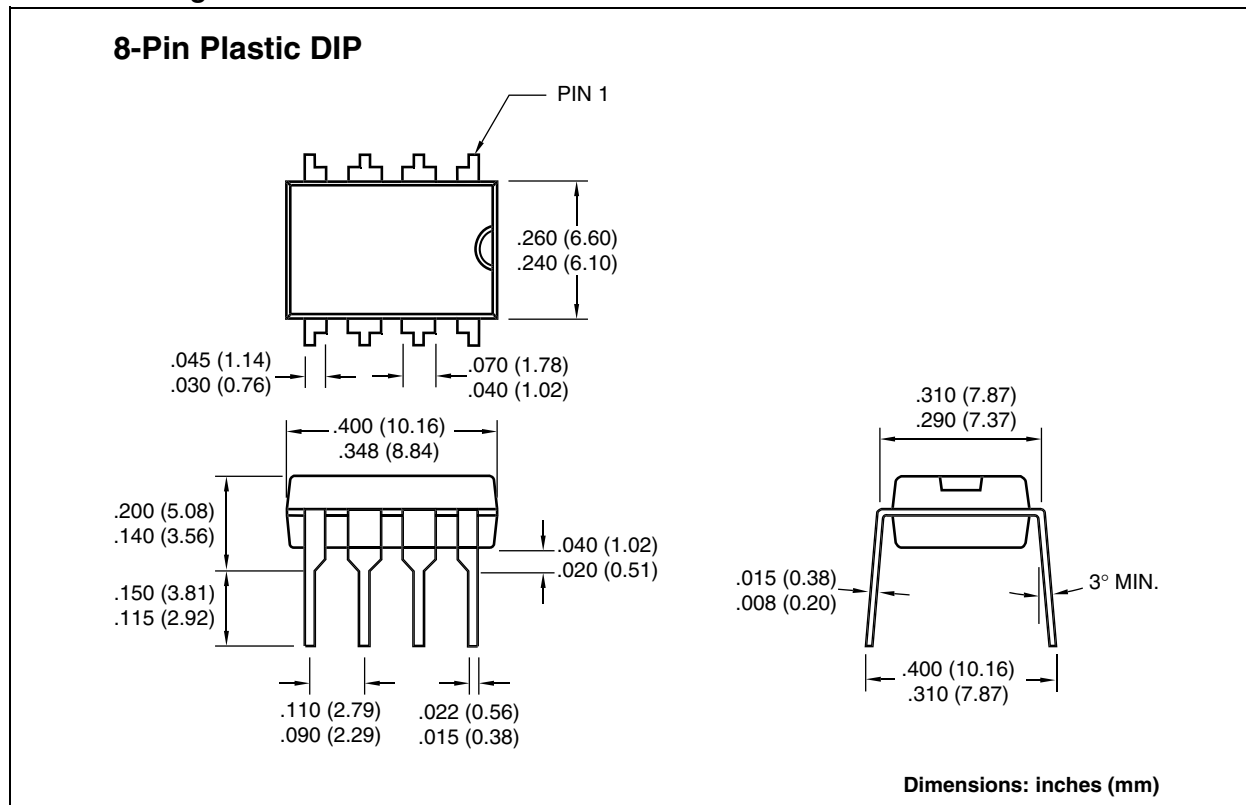


5.0 PACKAGING INFORMATION

5.1 Package Marking Information

Package marking data not available at this time.

5.2 Package Dimensions



TC1426/TC1427/TC1428

NOTES:

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Products supported by a preliminary Data Sheet may have an errata sheet describing minor operational differences and recommended workarounds. To determine if an errata sheet exists for a particular device, please contact one of the following:

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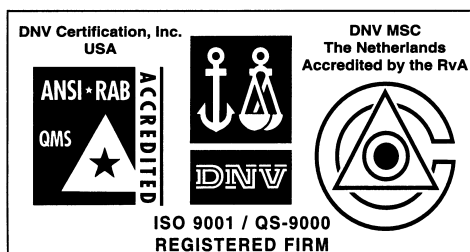
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New York

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Tel: 631-273-5305 Fax: 631-273-5335

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Microchip Technology Inc.
2107 North First Street, Suite 590
San Jose, CA 95131
Tel: 408-436-7950 Fax: 408-436-7955

Toronto

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Mississauga, Ontario L4V 1X5, Canada
Tel: 905-673-0699 Fax: 905-673-6509

ASIA/PACIFIC

Australia

Microchip Technology Australia Pty Ltd
Suite 22, 41 Rawson Street
Epping 2121, NSW
Australia
Tel: 61-2-9868-6733 Fax: 61-2-9868-6755

China - Beijing

Microchip Technology Consulting (Shanghai)
Co., Ltd., Beijing Liaison Office
Unit 915
Bei Hai Wan Tai Bldg.
No. 6 Chaoyangmen Beidajie
Beijing, 100027, No. China
Tel: 86-10-85282100 Fax: 86-10-85282104

China - Chengdu

Microchip Technology Consulting (Shanghai)
Co., Ltd., Chengdu Liaison Office
Rm. 2401, 24th Floor,
Ming Xing Financial Tower
No. 88 TIDU Street
Chengdu 610016, China
Tel: 86-28-6766200 Fax: 86-28-6766599

China - Fuzhou

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Co., Ltd., Fuzhou Liaison Office
Unit 28F, World Trade Plaza
No. 71 Wusi Road
Fuzhou 350001, China
Tel: 86-591-7503506 Fax: 86-591-7503521

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Co., Ltd.
Room 701, Bldg. B
Far East International Plaza
No. 317 Xian Xia Road
Shanghai, 200051
Tel: 86-21-6275-5700 Fax: 86-21-6275-5060

China - Shenzhen

Microchip Technology Consulting (Shanghai)
Co., Ltd., Shenzhen Liaison Office
Rm. 1315, 13/F, Shenzhen Kerry Centre,
Renminnan Lu
Shenzhen 518001, China
Tel: 86-755-2350361 Fax: 86-755-2366086

Hong Kong

Microchip Technology Hongkong Ltd.
Unit 901-6, Tower 2, Metroplaza
223 Hing Fong Road
Kwai Fong, N.T., Hong Kong
Tel: 852-2401-1200 Fax: 852-2401-3431

India

Microchip Technology Inc.
India Liaison Office
Divyasree Chambers
1 Floor, Wing A (A3/A4)
No. 11, O'Shaugnessey Road
Bangalore, 560 025, India
Tel: 91-80-2290061 Fax: 91-80-2290062

Japan

Microchip Technology Japan K.K.
Benex S-1 6F
3-18-20, Shinyokohama
Kohoku-Ku, Yokohama-shi
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Korea

Microchip Technology Korea
168-1, Youngbo Bldg. 3 Floor
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Microchip Technology Singapore Pte Ltd.
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Singapore, 188980
Tel: 65-6334-8870 Fax: 65-6334-8850

Taiwan

Microchip Technology Taiwan
11F-3, No. 207
Tung Hua North Road
Taipei, 105, Taiwan
Tel: 886-2-2717-7175 Fax: 886-2-2545-0139

EUROPE

Denmark

Microchip Technology Nordic ApS
Regus Business Centre
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Tel: 33-1-69-53-63-20 Fax: 33-1-69-30-90-79

Germany

Microchip Technology GmbH
Gustav-Heinemann Ring 125
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Tel: 49-89-627-144 0 Fax: 49-89-627-144-44

Italy

Microchip Technology SRL
Centro Direzionale Colleoni
Palazzo Taurus 1 V. Le Colleoni 1
20041 Agrate Brianza
Milan, Italy
Tel: 39-039-65791-1 Fax: 39-039-6899883

United Kingdom

Arizona Microchip Technology Ltd.
505 Eskdale Road
Winnersh Triangle
Wokingham
Berkshire, England RG41 5TU
Tel: 44 118 921 5869 Fax: 44-118 921-5820

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