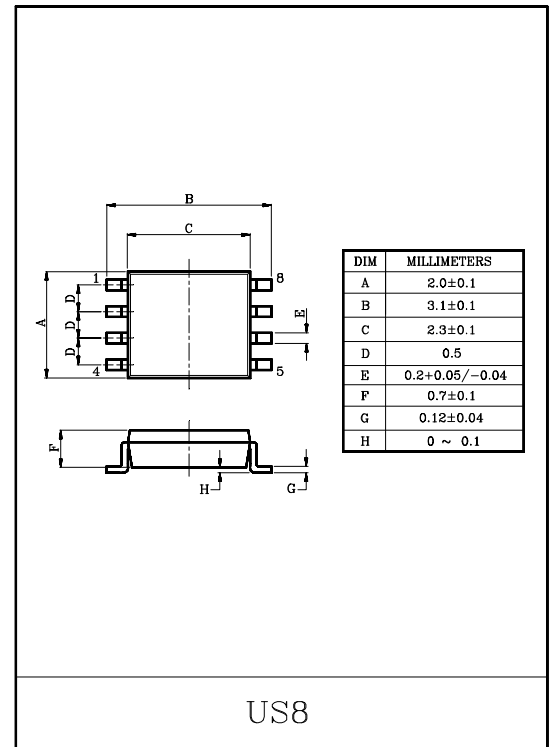


3 INVERTERS

The KIC7W04FK is a high speed C²MOS BUFFER fabricated with silicon gate C²MOS technology. The internal circuit is composed of 3 stages including buffer output, which enables high noise immunity and stable output. All inputs are equipped with protection circuits against static discharge or transient excess voltage.

FEATURES

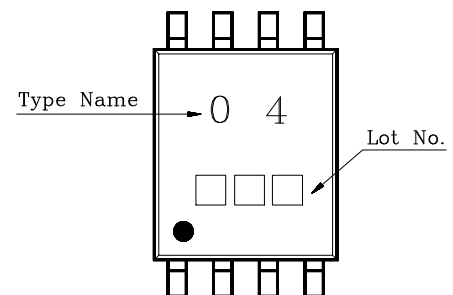
- High Speed : $t_{pd}=6ns(Typ.)$ at $V_{CC}=5V$.
- Low Power Dissipation : $I_{CC}=1\mu A(Max.)$ at $T_a=25^{\circ}C$.
- High Noise Immunity : $V_{NIH}=V_{NIL}=28\% V_{CC}(Min.)$.
- Output Drive Capability : 10 LSTTL Loads.
- Symmetrical Output Impedance : $|I_{OH}|=I_{OL}=4mA(Min.)$
- Balanced Propagation Delays : $t_{pLH}\approx t_{pHL}$
- Wide Operating Voltage Range : $V_{CC(opr)}=2\sim 6V$.



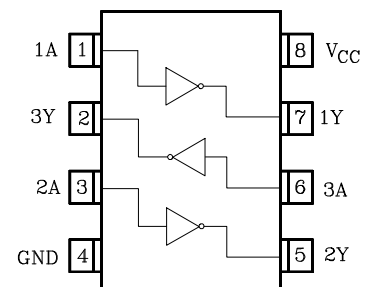
MAXIMUM RATINGS (Ta=25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage Range	V_{CC}	-0.5~7	V
DC Input Voltage	V_{IN}	-0.5~ $V_{CC}+0.5$	V
DC Output Voltage	V_{OUT}	-0.5~ $V_{CC}+0.5$	V
Input Diode Current	I_{IK}	±20	mA
Output Diode Current	I_{OK}	±20	mA
DC Output Current	I_{OUT}	±25	mA
DC V_{CC} /Ground Current	I_{CC}	±25	mA
Power Dissipation	P_D	200	mW
Storage Temperature	T_{stg}	-65~150	°C
Lead Temperature (10s)	T_L	260	°C

MARKING

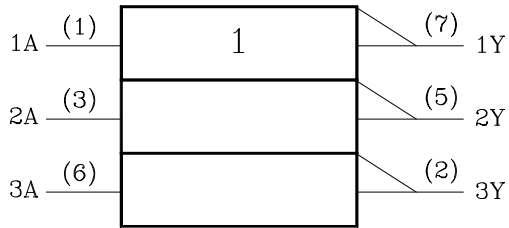


PIN CONNECTION(TOP VIEW)



KIC7W04FK

LOGIC DIAGRAM



TRUTH TABLE

A	Y
L	H
H	L

RECOMMENDED OPERATING CONDITIONS

CHARACTERISTIC	SYMBOL	RATING	UNIT
Supply Voltage	V_{CC}	2~6	V
Input Voltage	V_{IN}	0~ V_{CC}	V
Output Voltage	V_{OUT}	0~ V_{CC}	V
Operating Temperature	T_{opr}	-40~85	°C
Input Rise and Fall Time	t_r, t_f	0~1000 ($V_{CC}=2.0V$) 0~500 ($V_{CC}=4.5V$) 0~400 ($V_{CC}=6.0V$)	ns

DC ELECTRICAL CHARACTERISTICS

CHARACTERISTIC	SYMBOL	TEST CONDITION	V_{CC}	$T_a=25^{\circ}C$			$T_a=-40\sim 85^{\circ}C$		UNIT
				MIN.	TYP.	MAX.	MIN.	MAX.	
High-Level Input Voltage	V_{IH}	-	2.0 4.5 6.0	1.5 3.15 4.2	- - -	- - -	1.5 3.15 4.2	- - -	V
Low-Level Input Voltage	V_{IL}	-	2.0 4.5 6.0	- - -	- - -	0.5 1.35 1.8	- - -	0.5 1.35 1.8	V
High-Level Output Voltage	V_{OH}	$V_{IN}=V_{IL}$	$I_{OH}=-20\mu A$	2.0 4.5 6.0	1.9 4.4 5.9	2.0 4.5 6.0	- - -	1.9 4.4 5.9	V
			$I_{OH}=-4mA$	4.5	4.18	4.31	-	4.13	
			$I_{OH}=-5.2mA$	6.0	5.68	5.80	-	5.63	
Low-Level Output Voltage	V_{OL}	$V_{IN}=V_{IH}$	$I_{OL}=20\mu A$	2.0 4.5 6.0	- - -	0.0 0.0 0.0	0.1 0.1 0.1	- - 0.1	V
			$I_{OL}=4mA$	4.5	-	0.17	0.26	-	
			$I_{OL}=5.2mA$	6.0	-	0.18	0.26	-	
Input Leakage Current	I_{IN}	$V_{IN}=V_{CC}$ or GND	6.0	-	-	± 0.1	-	± 1.0	μA
Quiescent Supply Current	I_{CC}	$V_{IN}=V_{CC}$ or GND	6.0	-	-	1.0	-	10.0	

KIC7W04FK

AC ELECTRICAL CHARACTERISTICS ($C_L=15\text{pF}$, $V_{CC}=5\text{V}$, $T_a=25^\circ\text{C}$)

CHARACTERISTIC	SYMBOL	TEST CONDITION	$T_a=25^\circ\text{C}$			UNIT
			MIN.	TYP.	MAX.	
Output Transition Time	t_{TLH} t_{THL}	-	-	4	8	ns
Propagation Delay Time	t_{pLH} t_{pHL}	-	-	6	12	ns

AC ELECTRICAL CHARACTERISTICS ($C_L=50\text{pF}$, Input $t_r=t_f=6\text{ns}$)

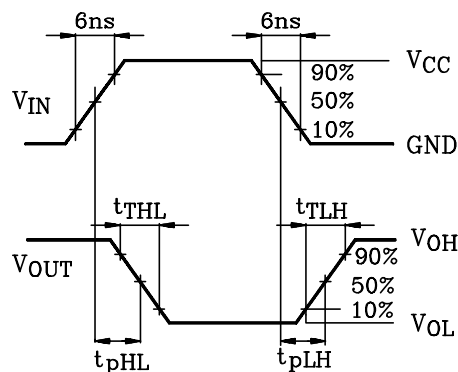
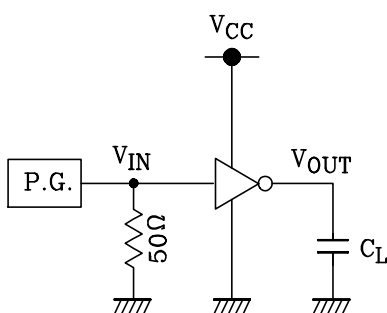
CHARACTERISTIC	SYMBOL	TEST CONDITION	V_{CC}	$T_a=25^\circ\text{C}$			$T_a=-40\sim 85^\circ\text{C}$		UNIT
				MIN.	TYP.	MAX.	MIN.	MAX.	
Output Transition Time	t_{TLH} t_{THL}	-	2.0 4.5 6.0	- - -	30 8 7	75 15 13	- - -	95 19 16	ns
Propagation Delay Time	t_{pLH} t_{pHL}	-	2.0 4.5 6.0	- - -	27 9 8	75 15 13	- - -	95 19 16	ns
Input Capacitance	C_{IN}	-	-	-	5	10	-	10	pF
Power Dissipation Capacitance	C_{PD}	(Note 1)	-	-	20	-	-	-	

Note 1 : C_{PD} is defined as the value of internal equivalent capacitance of IC which is calculated from the operating current consumption without load (refer to Test Circuit.)

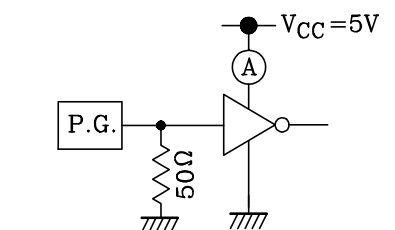
Average operating current can be obtained by the equation hereunder.

$$I_{CC(opr)} = C_{PD} \cdot V_{CC} \cdot f_{IN} + I_{CC}/3 \text{ (per gate)}$$

SWITCHING CHARACTERISTICS TEST CIRCUIT



OPERATING CURRENT CONSUMPTION TEST CIRCUIT



This input waveform is equal to SWITCHING CHARACTERISTICS TEST CIRCUIT input waveform.