TOSHIBA Biplar Linear Integrated Circuit Silicon Monolithic

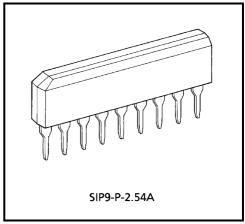
TA7343AP

FM PLL MPX

The TA7343AP is PLL FM stereo multiplex IC. It is suitable for automotive applications and portable radio applications because of space merit by the package and wide supply voltage range.

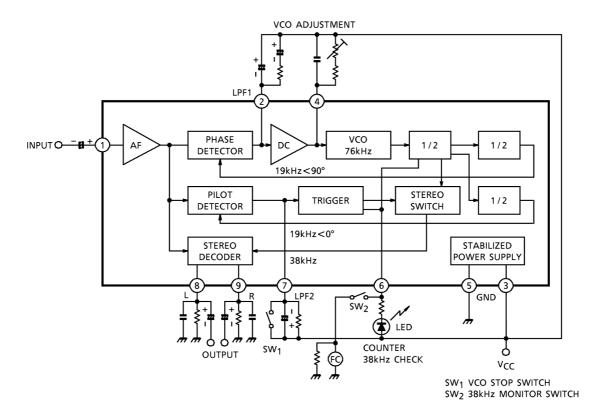
Features

- Excellent stereo LED sensitivity
 - $: V_{L}(ON) = 9mV_{rms}(typ.)$
- Suitable for LED driving: ILED = 20mA (max.)
- Recommendable input voltage range
 - $: V_{in} = 200 \sim 700 \text{mV}_{rms}$
- Operating supply voltage range: $V_{CC} = 3.5 \sim 12V$
- Excellent channel separation through entire audio frequency range: Sep = 45dB (typ.)
- Low distortion: THD = 0.08% (typ.) at $V_{in} = 200 \text{mV}_{rms}$ (stereo)
- Built-in compulsive monaural function. (The VCO is stopped when the pin(7) is connected with the power supply line, and then the stereo indicator is turn off.)
- Easy adjustment (the monitored free running frequency of VCO is 38kHz at pin(6).)



Weight: 0.92g (typ.)

Block Diagram



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Maximum Ratings (Ta = 25°C)

Characteristic	Symbol	Rating	Unit	
Supply voltage	V _{CC}	12	V	
LED voltage	V_{LED}	16	V	
LED current	I _{LED}	20	mA	
Power dissipation	P _D (Note)	500	mW	
Operating temperature	T _{opr}	-30~75	°C	
Storage temperature	T _{stg}	-55~155	°C	

(Note) Derated above Ta = 25° C in the proportion of 4mW / $^{\circ}$ C

Elecrical Characteristics

1. DC Characteristics (Ta = 25°C, V_{CC} = 8V, terminal voltage at no signal)

Pin No.	Characteristic	Symbol	Тур.	Unit
1	INPUT	V1	3.5	V
2	LPF 1	V2	6.6	V
3	V _{CC}	V3	8.0	V
4	VCO	V4	7.1	V
5	GND	V5	0	V
6	ST LED	V6	_	V
7	LPF 2	V7	7.4	V
8	L-ch OUTPUT	V8	4.0	V
9	R-ch OUTPUT	V9	4.0	V

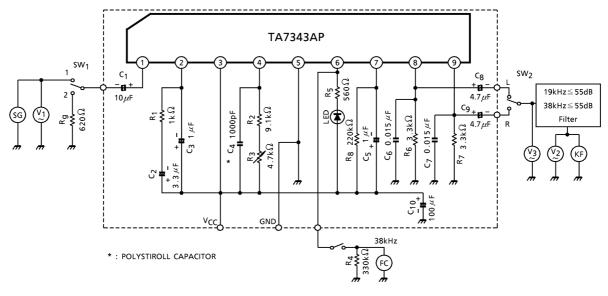
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2. AC Characteristics (unless otherwise specified, Ta = 25° C, V_{CC} = 8V, f = 1kHz)

Charact	eristic	Symbol	Test Condition Cuit Cuit Condition Cuit Condition Cuit Condition Cuit Condition Cuit Condition Cuit Condition Cuit Cu		Min.	Тур.	Max.	Unit		
Supply current		Icc	_	at LED off		_	11	18	mA	
Input resistance		R _{IN}	_			_	33	_	kΩ	
Max. Composite signification input voltage	nal	V _{in} max (stereo)	_	L + R = 90%, P = 10% THD = 1%		_	900	_	mV _{rms}	
Separation		Sep	-	L + R = 180mV _{rms} P = 20mV _{rms}		36	45	_	dB	
Total harmonic	Monaural	THD (monau- ral)	_	V _{in} = 200mV _{rm}	ns	_	0.08	0.3	%	
distortion	Stereo	THD (stereo)	ı	L + R = 180mV _{rms} P = 20mV _{rms}		_	0.08	_		
Voltage gain	Voltage gain G_V — $V_{in} = 200 \text{mV}_{rms}$		าร	-2.0	0	2.0	dB			
Channel balance		СВ	_	V _{in} = 200mV _{rms}		_	0	1.5	dB	
Stereo LED	On	V _{L (ON)}	ı	Pilot input		_	9	15	mV _{rms}	
sensitivity	Off	V _{L (OFF)}	_			2	6	_		
Stereo LED hysteresis		V _H	_	To turn off from LED turn on		_	3	_	mV _{rms}	
Capture range		CR	_	P = 20mV _{rms}		_	±3	_	%	
Carrier leak	19kHz	CL		P = 20mV _{rms} L + R = 180mV _{rms}		_	34	_	- dB	
Carrier leak	38kHz	OL .	_			_	42	_		
SCA rejection ratio		SCA rej	_	$P = 20mV_{rms}$ $L + R = 160mV_{rms}$ $SCA = 20mV_{rms}$ $f_{SCA} = 67kHz$		_	70	_	dB	
Signal to noise ratio		S/N	$- V_{in} = 200 \text{mV}_{rms}$ f = 1kHz, R _g = 620 Ω		_	74	_	dB		
				V _{CC} = 3.5V	_	0.3	0.6			
Output current (Pin(8), Pin(9))		Гоит	_	$R_L = 3.3k\Omega$	V _{CC} = 8.0V	_	1.2	1.8	mA	
					V _{CC} = 12V		1.4	2.1		

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Test Circuit

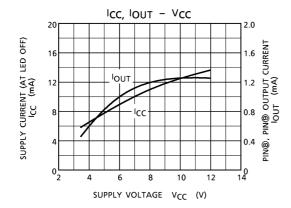


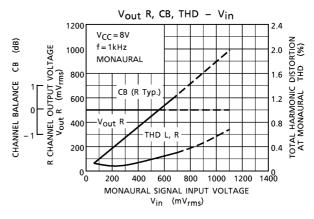
SG : STEREO SIGNAL GENERATOR
FC : FREQUENCY COUNTER
V1, V2, V3 : AC VOLTMETER
KF : DISTORTION METER

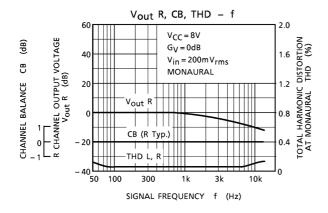
External Parts Table

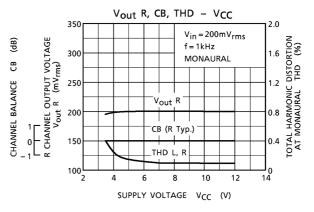
Parts No. Typical		Purpose	Infl	Influence	
		Fulpose	Smaller than typ.	Smaller than typ. Greater than typ.	
C ₁	10μF	Coupling	Separation is bad at 50~300Hz	"POP" noise is high	Input
C ₂	3.3µF				
C ₃	1µF	LPF at PLL	THD is bad at 5~10kHz (stereo)	Narrow capture range	_
R ₁	1kΩ		,		
C ₄	1000pF	VCO free running	C ₄ : Small → wide captu	C ₄ : Small → wide capture range and large	
R ₂	9.1kΩ	frequency	glitter		_
R ₃	4.7kΩVR	adjustment	C ₄ : Large → narrow cap	C ₄ : Large → narrow capture range	
R ₄	330kΩ	Monitor load		_	
R ₅	560Ω	Rush current limiter	IC is damaged by the rush current	LED is dark	I _{LED} ≤ 20mA
LED	_	Stereo indicator	Usable for LED		
C ₆	0.015µF	Load and	Diemphasis (50µs)		C ₆ = 0.022µF
R ₆	3.3kΩ	diemphasis	Output voltage is small	THD is bad for low V _{CC}	for 75µs
C ₇	0.015µF	Load and	Diemphasis (50µs)		C ₇ = 0.022µF
R ₇	3.3kΩ	diemphasis	Output voltage is small	THD is bad for low V _{CC}	for 75μs
C ₈	4.7µF	Output coupling	Frequency response	"DOD" poigo io lorgo	L-ch
C ₉	4.7µF	Output coupling	is bad	"POP" noise is large	R-ch
R ₈	220kΩ	LED sensitivity adjustment	V _{L (ON)} is large	V _{L (ON)} is small	_
C ₅	1µF	LPF at LED	THD is bad at 50~300Hz	Slow LED response	_

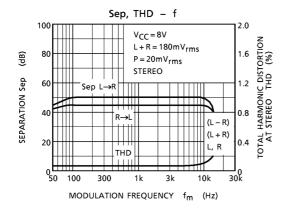
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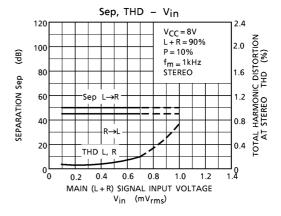




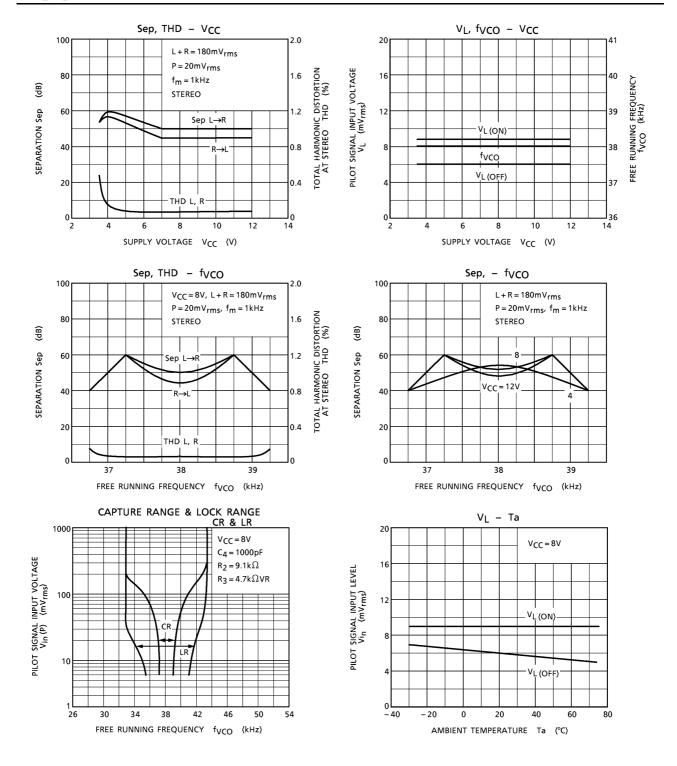


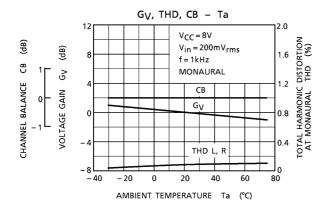


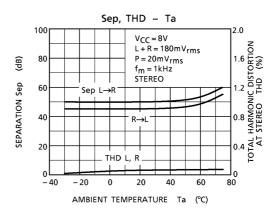


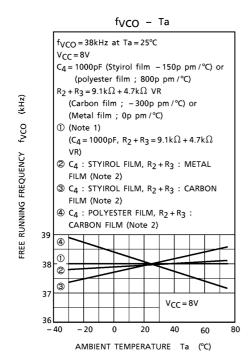


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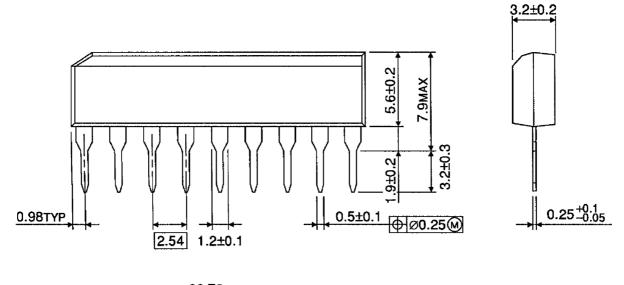


(Note 1) 1: With IC only put into a temperature test chamber

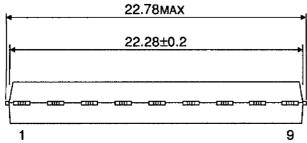
(Note 2) ②③④: With IC, resistors and capacitors put into a temperature test chamber

Package Dimensions

SIP9-P-2.54A Unit: mm



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Weight: 0.92g (typ.)

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