TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA7555P,TA7555F

TIMER APPLICATIONS

The TA7555P monolithic circuit is a highly stable device as producing accurate time delay or timing pulse.

Additional terminals are provided for triggering or reseting, if desired.

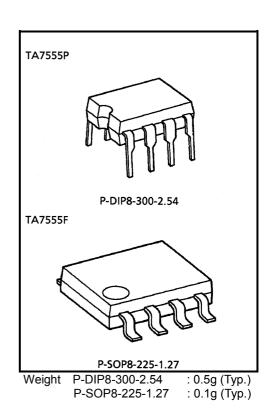
In the time delay or monostable mode of operation, the time is precisely controlled by one external resistor and capacitor. In the astable mode of operation, the frequency and duty cycle are accurately and independently controlled with two external resistors and one capacitor.

The circuit of the TA7555P may be triggered and reset on falling waveforms, and the output structure can source and sink up to 200mA or drive TTL circuit.

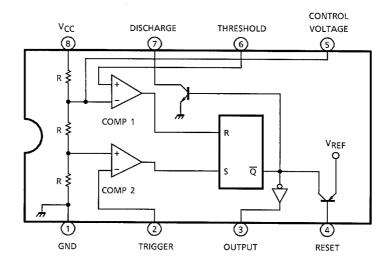
Operation is specified for supplies of 5 to $15\mathrm{V}$

FEATURES

- Timing from microseconds through hours
- Operates in both astable and monostable modes
- Adjustable duty cycle
- Output can source or sink 200mA
- Output TTL compatible
- Temperature stability of 0.005% / °C (Typ.)
- Normally ON or normally OFF output
- Direct replacement for SE555 / NE555

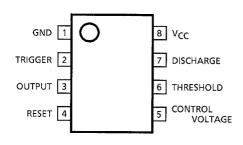


BLOCK DIAGRAM

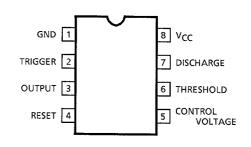


PIN CONNECTION (TOP VIEW)

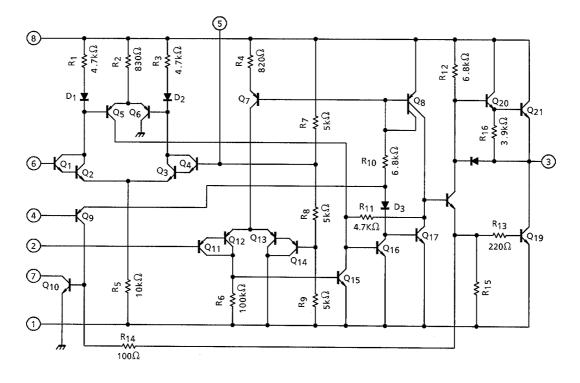
TA7555F







EQUIVALENT CIRCUIT



APPLICATIONS

- DC-DC CONVERTER
- LINEAR RAMP GENERATOR
- PULSE GENERATOR
- PRECISION TIMING
- SEQUENTIAL TIMING
- TIMING DELAY GENERATION
- PULSE WIDTH MODULATION
- PULSE

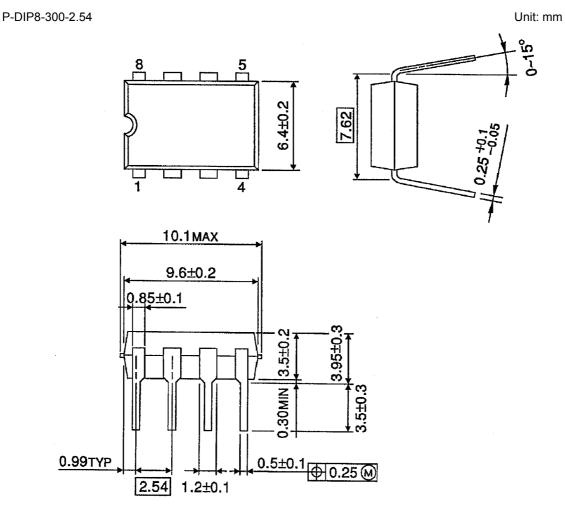
MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC		SYMBOL	RATING	UNIT	
Supply Voltage	TA7555P	V _{CC}	18	V	
	TA7555F	VCC	15		
Power Dissipation	TA7555P	PD	600	mW	
	TA7555F	۲D	240		
Operating Temperature		T _{opr}	-30~75	°C	
Storage Temperature		T _{stg}	-55~125	°C	

ELECTRICAL CHARACTERISTICS (Ta = 25° C, V_{CC} = $5 \sim 15$ V)

CHARACTERISTIC	SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN	TYP.	MAX	UNIT
Supply Voltage	V _{CC}	_	_		4.5	_	16	V
Supply Current	Icc	_	V_{CC} = 5V, R_L = ∞ , Low state		—	3	6	mA
	100		V _{CC} = 15V, R _L = ∞, Low state			10	15	
Control Voltage	V _{CT}	_	V _{CC} = 5V		2.6	3.33	4	v
			V _{CC} = 15V		9	10	11	
Threshold Voltage	V _{TH}	_	_		_	(2 / 3) V _{CC}		V
Threshold Current	ITH	_	V _{CC} = 5V, 15V		_	0.1	0.25	μA
Trigger Voltage	V _{TG}	_	V _{CC} = 5V		_	1.67	١	V
	۷IG		V _{CC} = 15V		_	5	١	
Trigger Current	I _{TG}	_	_		_	0.5		μA
Reset Voltage	V _{RT}	—	_		0.4	0.7	1.0	V
Reset Current	I _{RT}	—	-			0.1		mA
Initial Accuracy			Monostable mode R_A , $R_B = 1k\Omega \sim 100k\Omega$ $C = 0.1\mu F$, $V_{CC} = 15V$			1	_	%
Drift with Temperature	—	—				50		ppm / °C
Drift with Supply Voltage					_	0.1		% / V
Output Voltage (" L " Level)	V _{OL}	_	V _{CC} = 15V	I _{sink} = 10mA		0.1	0.25	V
				I _{sink} = 50mA		0.4	0.75	
				I _{sink} = 100mA	—	2	2.5	
				I _{sink} = 200mA	—	2.5	_	
			V _{CC} = 5V	I _{sink} = 5mA	—	0.25	0.35	
				I _{sink} = 8mA	—	—	_	
Output Voltage (" H " Level)	V _{OH}	_	V _{CC} = 15V	I _{source} = 100mA	12.75	13.3	_	V
				I _{source} = 200mA	—	12.5	—	
			V _{CC} = 5V	I _{source} = 100mA	2.75	3.3	_	
Rise Time	t _r	_			_	100	_	ns
Fall Time	t _f	-			—	100	—	ns

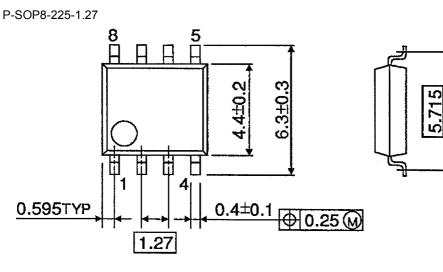
PACKAGE DIMENSIONS

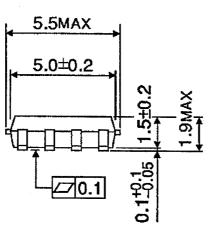


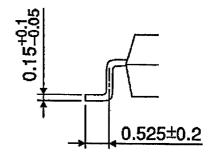
Weight: 0.5g (Typ.)

Unit: mm

PACKAGE DIMENSIONS







Weight: 0.1g (Typ.)

(225mil)

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