

TOSHIBA BIPOLAR DIGITAL INTEGRATED CIRCUIT SILICON MONOLITHIC

TD62083AFN,TD62084AFN

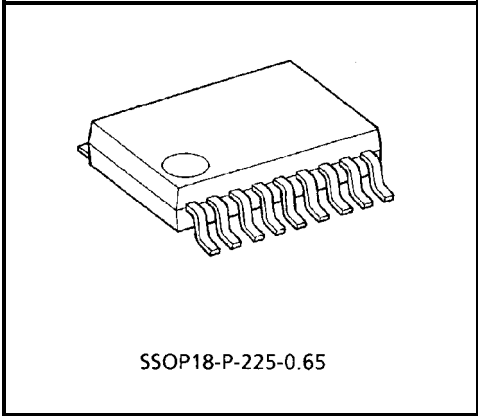
8CH DARLINGTON SINK DRIVER

The TD62083AFN and TD62084AFN are high-voltage, high-current darlington drivers comprised of eight NPN darlington pairs. All units feature integral clamp diodes for switching inductive loads. Applications include relay, hammer, lamp and display (LED) drivers. Please observe the thermal condition for using.

FEATURES

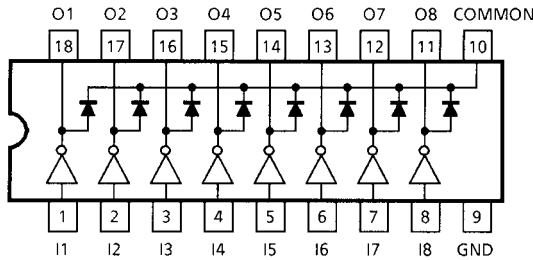
- Package Type : SSOP18 pin
- High Sustaining Voltage Output : 50 V (Min)
- Output Current (Single Output) : 500 mA / ch (Max)
- Output Clamp Diodes
- Inputs compatible with Various Types of Logic.

TYPE	INPUT BASE RESISTOR	DESIGNATION
TD62083AFN	2.7 kΩ	TTL, 5-V CMOS
TD62084AFN	10.5 kΩ	6~15-V P-MOS, CMOS

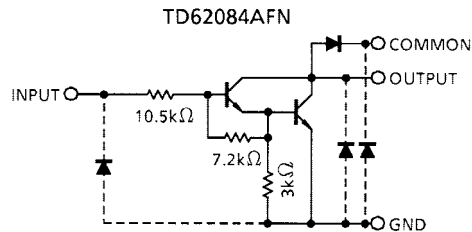
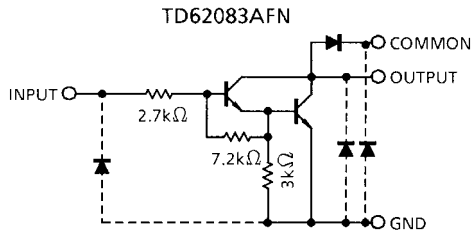


Weight: 0.09 g (Typ.)

PIN CONNECTION (TOP VIEW)



SCHEMATICS (EACH DRIVER)



Note: The input and output parasitic diodes cannot be used as clamp diodes.

MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL	RATING	UNIT
Output Sustaining Voltage	V _{CE (SUS)}	-0.5~50	V
Output Current	I _{OUT}	500	mA / ch
Input Voltage	V _{IN}	-0.5~30	V
Clamp Diode Reverse Voltage	V _R	50	V
Clamp Diode Forward Current	I _F	500	mA
Power Dissipation	P _D (Note)	0.96	W
Operating Temperature	T _{opr}	-40~85	°C
Storage Temperature	T _{stg}	-55~150	°C

Note: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 40%)

RECOMMENDED OPERATING CONDITIONS (Ta = -40~85°C)

CHARACTERISTIC		SYMBOL	CONDITION	MIN	TYP.	MAX	UNIT
Output Sustaining Voltage		V _{CE (SUS)}		0	—	50	V
Output Current		I _{OUT} (Note)	DC 1 Circuit	—	—	350	mA / ch
			T _{pw} = 25 ms, 8 Circuits Ta = 85°C, T _j = 120°C				
			Duty = 10%	0	—	260	
			Duty = 50%	0	—	90	
Input Voltage		V _{IN}		0	—	30	V
Input Voltage (Output ON)	TD62083	V _{IN(ON)}		2.5	—	30	V
	TD62084			8	—	30	
Clamp Diode Reverse Voltage		V _R		—	—	50	V
Clamp Diode Forward Current		I _F		—	—	400	mA
Power Dissipation		P _D	Ta = 85°C (Note)	—	—	0.4	W

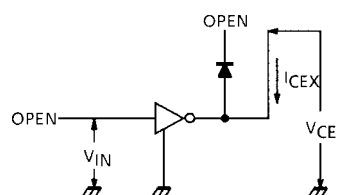
Note: On Glass Epoxy PCB (50 × 50 × 1.6 mm Cu 40%)

ELECTRICAL CHARACTERISTICS (Ta = 25°C)

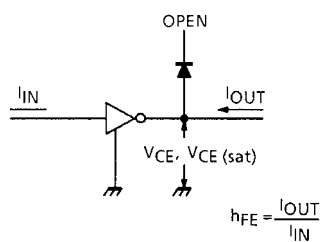
CHARACTERISTIC		SYMBOL	TEST CIR- CUIT	TEST CONDITION		MIN	TYP.	MAX	UNIT
Output Leakage Current	TD62083	I _{CEX}	1	V _{CE} = 50 V	T _a = 25°C	—	—	50	μA
				V _{CE} = 50 V	T _a = 85°C	—	—	100	
	TD62084				V _{CE} = 50 V	V _{IN} = 1 V	—	—	
Output Saturation Voltage		V _{CE (sat)}	2	I _{OUT} = 350 mA, I _{IN} = 500 μA		—	1.3	1.6	V
				I _{OUT} = 200 mA, I _{IN} = 350 μA		—	1.1	1.3	
				I _{OUT} = 100 mA, I _{IN} = 250 μA		—	0.9	1.1	
Input Current	TD62083	I _{IN (ON)}	3	V _{IN} = 3.85 V		—	0.93	1.35	mA
	TD62084			V _{IN} = 5 V		—	0.35	0.5	
				V _{IN} = 12 V		—	1.0	1.45	
		I _{IN (OFF)}	4	I _{OUT} = 500 μA, T _a = 85°C		50	65	—	μA
Input Voltage	TD62083	V _{IN (ON)}	5	V _{CE} = 2 V, I _{OUT} = 200 mA		—	—	2.4	V
				V _{CE} = 2 V, I _{OUT} = 250 mA		—	—	2.7	
				V _{CE} = 2 V, I _{OUT} = 300 mA		—	—	3.0	
	TD62084			V _{CE} = 2 V, I _{OUT} = 125 mA		—	—	5.0	
				V _{CE} = 2 V, I _{OUT} = 200 mA		—	—	6.0	
				V _{CE} = 2 V, I _{OUT} = 275 mA		—	—	7.0	
				V _{CE} = 2 V, I _{OUT} = 350 mA		—	—	8.0	
DC Current Transfer Ratio		h _{FE}	2	V _{CE} = 2 V, I _{OUT} = 350 mA		1000	—	—	
Clamp Diode Reverse Current		I _R	6	T _a = 25°C V _R = 50 V		—	—	50	μA
				T _a = 85°C V _R = 50 V		—	—	100	
Clamp Diode Forward Voltage		V _F	7	I _F = 350 mA		—	—	2.0	V
Input Capacitance		C _{IN}	—			—	15	—	pF
Turn-On Delay		t _{ON}	8	R _L = 125 Ω, V _{OUT} = 50 V		—	0.1	—	μs
Turn-Off Delay		t _{OFF}		R _L = 125 Ω, V _{OUT} = 50 V		—	0.2	—	

TEST CIRCUIT

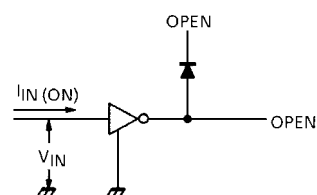
1. I_{CEX}



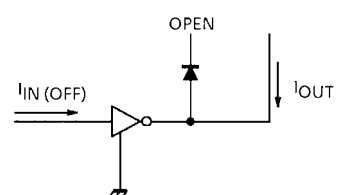
2. $V_{CE} (sat)$, h_{FE}



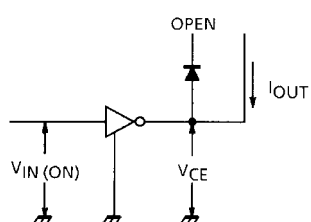
3. $I_{IN} (ON)$



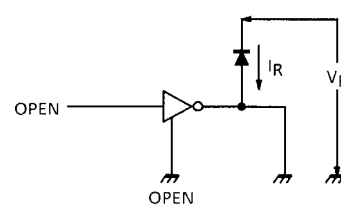
4. $I_{IN} (OFF)$



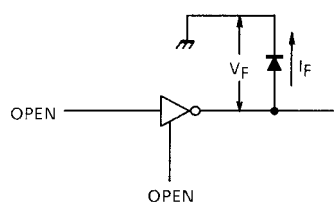
5. $V_{IN} (ON)$



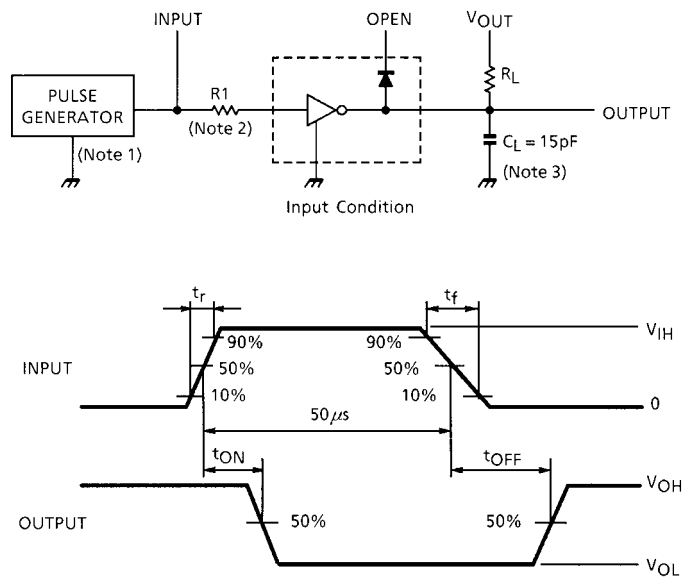
6. I_R



7. V_F



8. t_{ON} , t_{OFF}



Note 1: Pulse Width 50 μ s, Duty Cycle 10%
Output Impedance 50 Ω , $t_r \leq 5$ ns, $t_f \leq 10$ ns

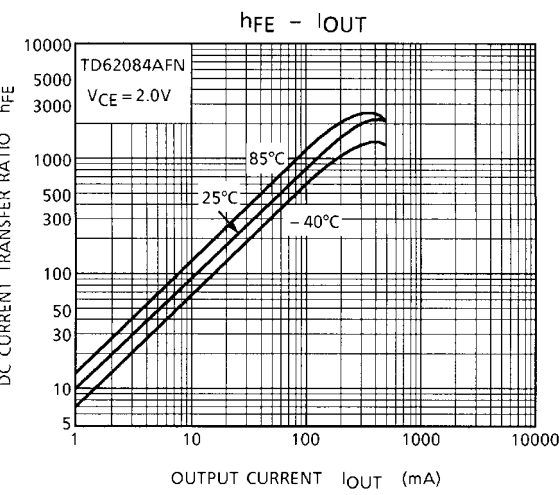
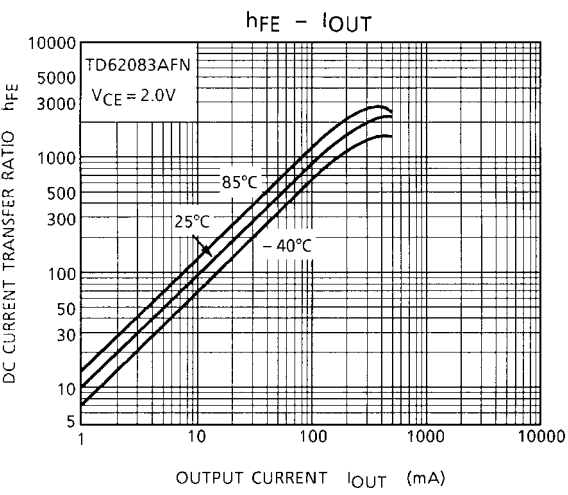
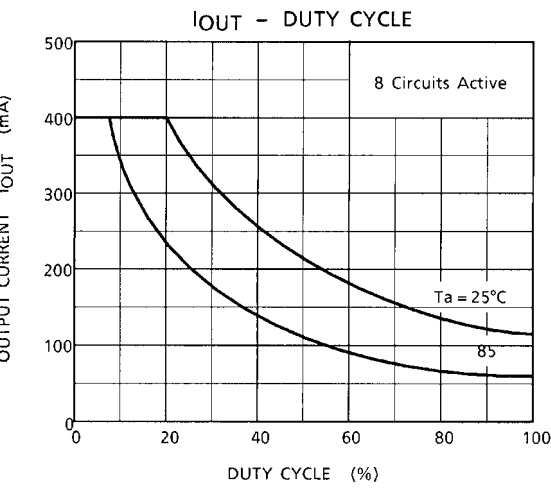
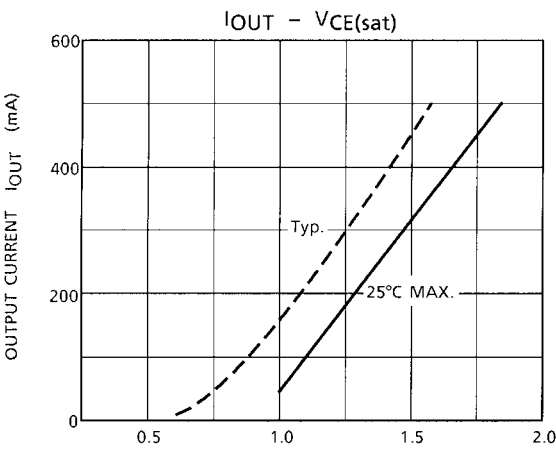
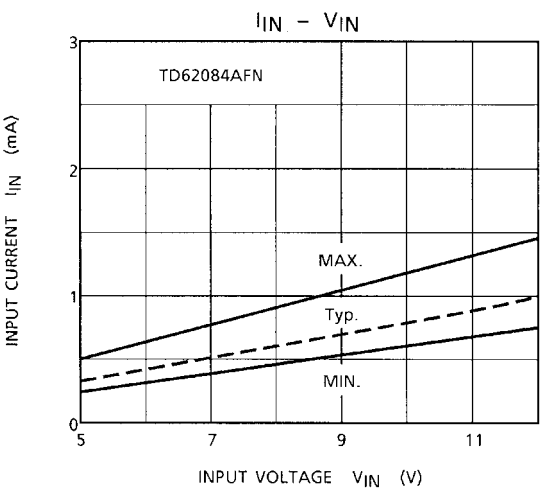
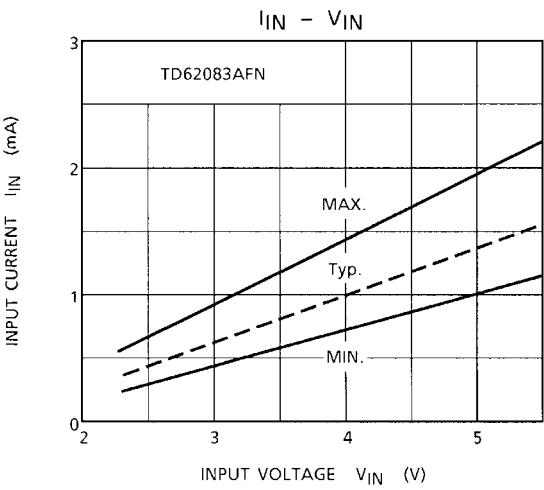
Note 2: See below
Input Condition

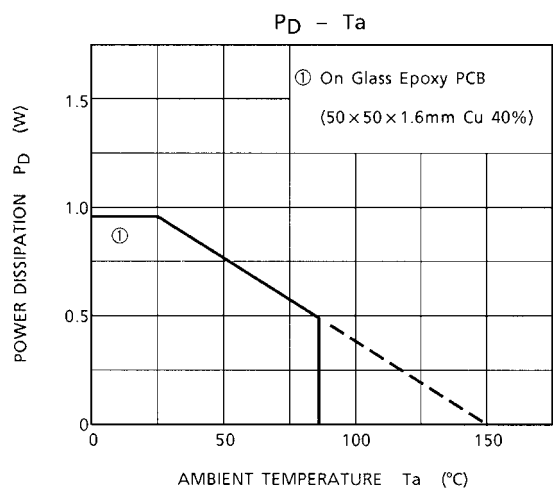
TYPE NUMBER	R1	V _{IH}
TD62083AFN	0	3 V
TD62084AFN	0	8 V

Note 3: CL includes probe and jig capacitance

PRECAUTIONS for USING

This IC does not include built-in protection circuits for excess current or overvoltage.
If this IC is subjected to excess current or overvoltage, it may be destroyed.
Hence, the utmost care must be taken when systems which incorporate this IC are designed.
Utmost care is necessary in the design of the output line, COMMON and GND line since IC may be destroyed due to short-circuit between outputs, air contamination fault, or fault by improper grounding.

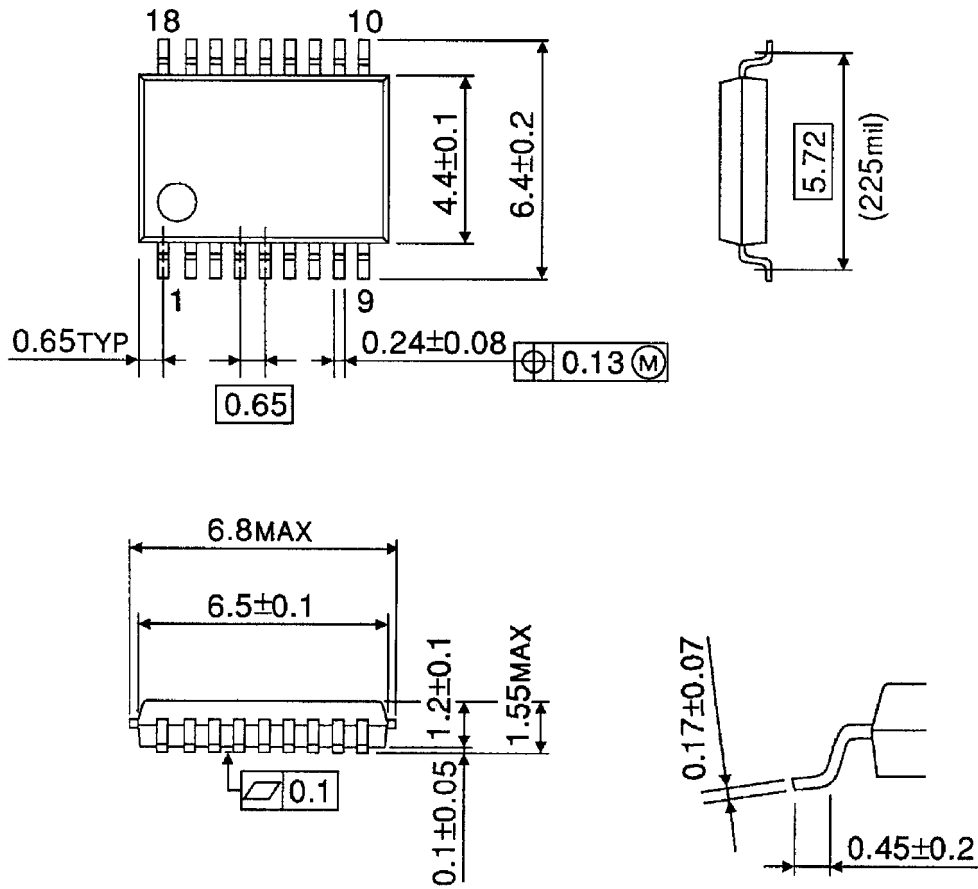




PACKAGE DIMENSIONS

SSOP18-P-225-0.65

Unit : mm



Weight: 0.09 g (Typ.)

RESTRICTIONS ON PRODUCT USE

000707EBA

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