TOSHIBA Field Effect Transistor Silicon N, P Channel MOS Type (U-MOSII)

TPC8401

Lithium Ion Secondary Battery Applications
Portable Equipment Applications
Notebook PCs

• Low drain-source ON resistance

: P Channel RDS (ON) = 27 m Ω (typ.) N Channel RDS (ON) = 14 m Ω (typ.)

• High forward transfer admittance

: P Channel $|Y_{fs}| = 7 S \text{ (typ.)}$

N Channel $|Y_{fs}| = 8 S \text{ (typ.)}$

• Low leakage current

: P Channel IDSS = $-10 \mu A (VDS = -30 V)$

N Channel IDSS = $10 \mu A \text{ (VDS} = 30 \text{ V)}$

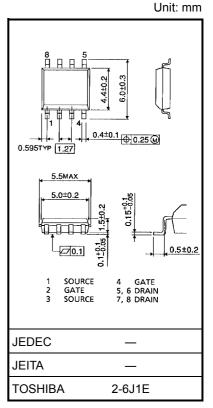
• Enhancement-mode

: P Channel $V_{th} = -0.8 \sim -2.0 \text{ V (V}_{DS} = -10 \text{ V, I}_{D} = -1 \text{mA})$

N Channel $V_{th} = 0.8 \sim 2.5 \text{ V (V}_{DS} = 10 \text{ V, I}_{D} = 1 \text{mA})$

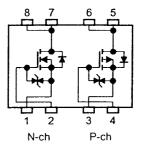
Maximum Ratings (Ta = 25°C)

Characteristics		0	Rat	1.114			
C	Symbol	P Channel	N Channel	Unit			
Drain-source v	V_{DSS}	-30	30	٧			
Drain-gate vol	V_{DGR}	-30	30	٧			
Gate-source v	V _{GSS}	±20	±20	V			
Drain current	DC (Note 1)	I _D	-4.5	6	Α		
Diam current	Pulse (Note 1)	I _{DP}	-18	24	A		
Drain power dissipation	Single-device operation (Note 3a)	P _{D (1)}	1.5	1.5			
(t = 10s) (Note 2a)	Single-device value at dual operation (Note 3b)	P _{D (2)}	1.0	1.0	W		
Drain power dissipation	Single-device operation (Note 3a)	P _{D (1)}	0.75	0.75	VV		
(t = 10s) (Note 2b)	Single-device value at dual operation (Note 3b)	P _{D (2)}	0.45	0.45			
Single pulse a	Single pulse avalanche energy		26.3 (Note 4a)	46.8 (Note 4b)	mJ		
Avalanche cur	I _{AR}	-4.5	6	Α			
Repetitive avalanche energy Single-device value at operation (Note 2a, 3b, 5)		E _{AR}	0.10		mJ		
Channel temperature		T _{ch}	150		°C		
Storage temper	T _{stg}	-55~150		°C			



Weight: 0.080 g (typ.)

Circuit Configuration



Note: For (Note 1), (Note 2a), (Note 2b), (Note 3a), (Note 3b), (Note 4a), (Note 4b) and (Note 5), please refer to the next page.

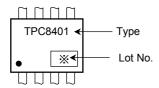
1

This transistor is an electrostatic sensitive device. Please handle with caution.

Thermal Characteristics

Characteristics	Symbol	Max	Unit		
Thermal resistance, channel to ambient (t = 10s) (Note 2a)	Single-device operation (Note 3a)	R _{th (ch-a) (1)}	83.3		
	Single-device value at dual operation (Note 3b)	R _{th (ch-a) (2)}	125		
Thormal recistance, channel to ambient	Single-device operation (Note 3a)	R _{th (ch-a) (1)}	167	°C/W	
Thermal resistance, channel to ambient (t = 10s) (Note 2b)	Single-device value at dual operation (Note 3b)	R _{th (ch-a) (2)}	278		

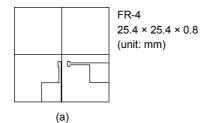
Marking

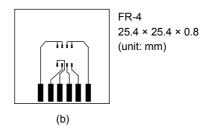


Note 1: Please use devices on condition that the channel temperature is below 150°C.

Note 2:

- a) Device mounted on a glass-epoxy board (a)
- b) Device mounted on a glass-epoxy board (b)





Note 3:

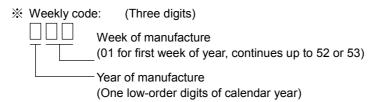
- a) The power dissipation and thermal resistance values are shown for a single device (During single-device operation, power is only applied to one device.)
- b) The power dissipation and thermal resistance values are shown for a single device (During dual operation, power is evenly applied to both devices.)

Note 4:

- a) $V_{DD} = -24 \text{ V}$, $T_{ch} = 25 ^{\circ}\text{C}$ (Initial), L = 1.0 mH, $R_G = 25 \Omega$, $I_{AR} = -4.5 \text{ A}$
- b) $V_{DD} = 24 \text{ V}$, $T_{ch} = 25^{\circ}\text{C}$ (Initial), L = 1.0 mH, $R_{G} = 25 \Omega$, $I_{AR} = 6.0 \text{ A}$

Note 5: Repetitive rating: pulse width limited by maximum channel temperature

Note 6: • on lower left of the marking indicates Pin 1.



2

2003-03-11

P-ch

Electrical Characteristics (Ta = 25°C)

Charac	cteristics	Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-OFF	current	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$	_	_	-10	μΑ
Drain-source breakdown		V _{(BR)DSS}	$I_D = -10 \text{ mA}, V_{GS} = 0 \text{ V}$	-30	_	_	V
voltage		V (BR) DSX	$I_D = -10 \text{ mA}, V_{GS} = 20 \text{ V}$	-15	_	_	V
Gate threshold v	voltage	V_{th}	$V_{DS} = -10 \text{ V}, I_D = -1 \text{ mA}$	-0.8	_	-2.0	V
Drain-source O	N resistance	R _{DS (ON)}	$V_{GS} = -4 \text{ V}, I_D = -2.2 \text{ A}$	_	55	65	mΩ
Dialii-source O	iv resistance	R _{DS (ON)}	$V_{GS} = -10 \text{ V}, I_D = -2.2 \text{ A}$	_	27	35	11122
Forward transfer	r admittance	Y _{fs}	V _{DS} = -10 V, I _D = -2.2 A	3.5	7	_	S
Input capacitance		C _{iss}		_	970	_	
Reverse transfer capacitance		C _{rss}	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	_	180	_	pF
Output capacitance		C _{oss}		_	370	_	
	Rise time	t _r	V_{GS} $1_{D} = -2.2 \text{ A}$ V_{OUT} $R_{L} = 6.8 \Omega$	_	17	_	
Switching time	Turn-ON time	t _{on}		_	20	_	no
Switching time	Fall time	t _f		_	75	_	ns
	Turn-OFF time	t _{off}	$V_{\mathrm{DD}} = -15 \mathrm{V}$ Duty $\leq 1\%$, $t_{\mathrm{w}} = 10 \mu \mathrm{s}$	_	160	_	
Total gate charge (Gate-source plus gate-drain)		Qg		_	28	_	
Gate-source charge 1		Q _{gs1}	$V_{DD} \approx -24 \text{ V}, V_{GS} = -10 \text{ V}, I_D = -4.5 \text{ A}$	_	6	_	nC
Gate-drain ("miller") charge		Q _{gd}		_	12	_	

Source-Drain Ratings and Characteristics (Ta = 25°C)

Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	-	_	1	-18	Α
Forward voltage (diode)		V_{DSF}	$I_{DR} = -4.5 \text{ A}, V_{GS} = 0 \text{ V}$	_		1.2	V

3 2003-03-11

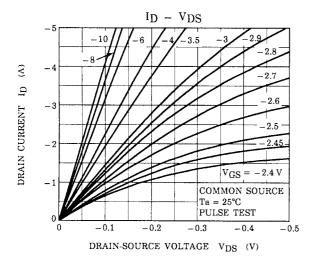
Electrical Characteristics (Ta = 25°C)

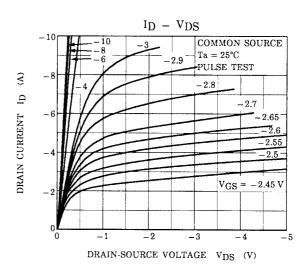
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Gate leakage current		I _{GSS}	V _{GS} = ±16 V, V _{DS} = 0 V	_	_	±10	μΑ
Drain cut-OFF current		I _{DSS}	V _{DS} = 30 V, V _{GS} = 0 V	_	_	10	μΑ
Drain-source breakdown voltage		V _{(BR)DSS}	$I_D = 10 \text{ mA}, V_{GS} = 0 \text{ V}$	30	_	1	V
		V _{(BR)DSX}	$I_D = 10 \text{ mA}, V_{GS} = -20 \text{ V}$	15	_	1	V
Gate threshold v	oltage	V_{th}	V _{DS} = 10 V, I _D = 1 mA	0.8	_	2.5	V
Drain-source O	N resistance	R _{DS (ON)}	V _{GS} = 4 V, I _D = 3 A	_	21	32	mΩ
Dialii-source Of	N IESISIAIICE	R _{DS (ON)}	V _{GS} = 10 V, I _D = 3 A	_	14	21	11122
Forward transfer	admittance	Y _{fs}	V _{DS} = 10 V, I _D = 3 A	4	8	_	S
Input capacitance		C _{iss}		_	1700	_	
Reverse transfer capacitance		C _{rss}	V _{DS} = 10 V, V _{GS} = 0 V, f = 1 MHz	_	260	_	pF
Output capacitance		Coss		_	380	1	
	Rise time	t _r	$V_{GS} = 3.0 \text{ A}$ $V_{GS} = 0 \text{ V}$ V_{OUT} $R_{L} = 5.0 \Omega$	_	10	-	
Switching time	Turn-ON time	t _{on}		_	20	ı	ne
Switching time	Fall time	t _f		_	35	_	ns
	Turn-OFF time	t _{off}	$V_{DD} = 15 \text{ V}$ Duty $\leq 1\%$, $t_{W} = 10 \mu\text{s}$	_	120	_	
Total gate charge (Gate-source plus gate-drain)		Qg			40		
Gate-source charge 1		Q _{gs1}	$V_{DD} \approx 24 \text{ V, } V_{GS} = 10 \text{ V, } I_D = 6 \text{ A}$	_	28	_	nC
Gate-drain ("miller") charge		Q_{gd}		_	12	_	

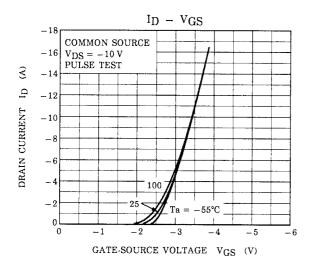
Source-Drain Ratings and Characteristics (Ta = 25°C)

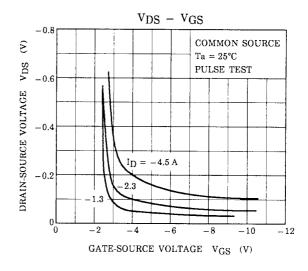
Characteristics		Symbol	Test Condition	Min	Тур.	Max	Unit
Drain reverse current	Pulse (Note 1)	I _{DRP}	_	_		24	Α
Forward voltage ((diode)	V_{DSF}	I _{DR} = 6 A, V _{GS} = 0 V	_	_	-1.2	V

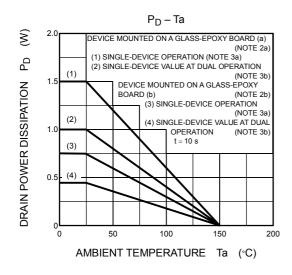
P-ch

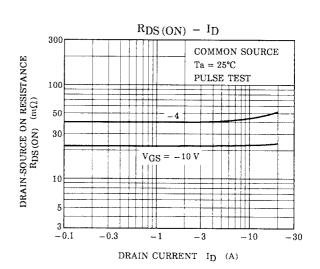




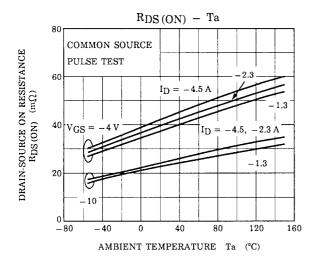


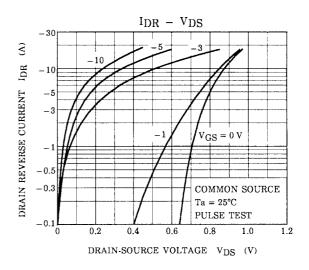


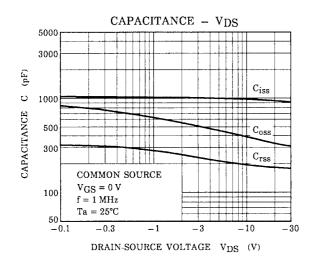


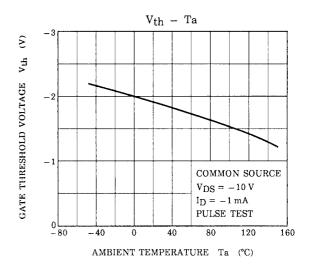


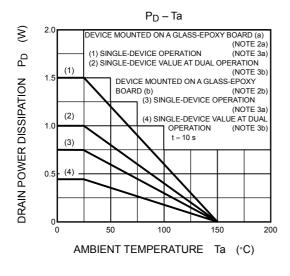
P-ch

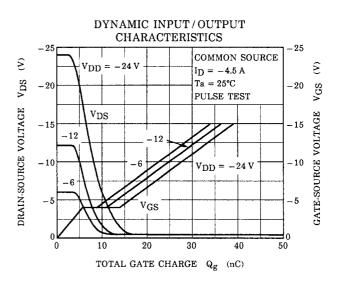




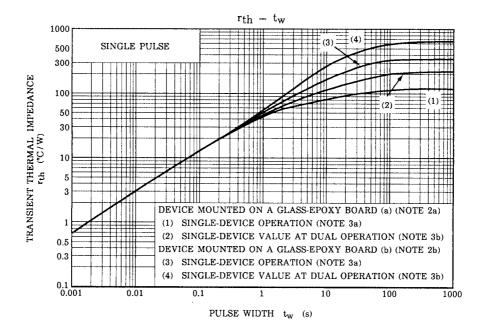




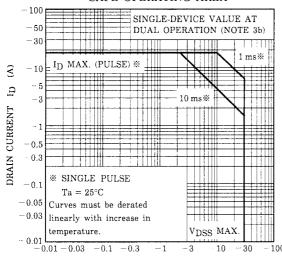




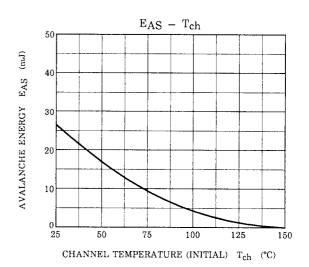
P-ch

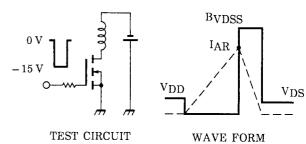




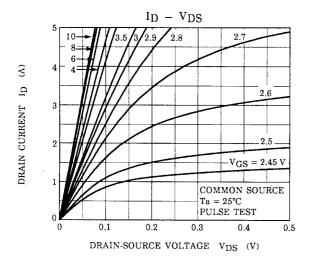


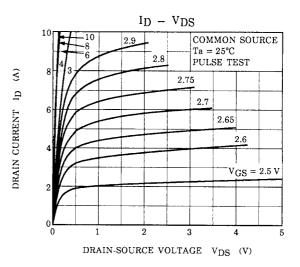
DRAIN-SOURCE VOLTAGE V_{DS} (V)

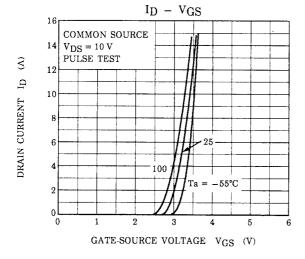


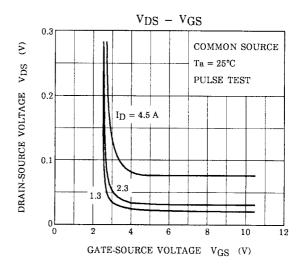


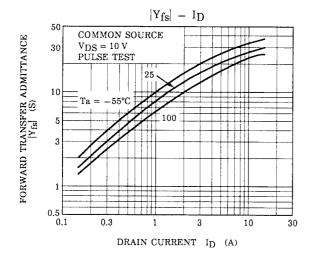
$$\begin{array}{l} T_{ch}=25^{\circ}C~(Initial)\\ Peak~I_{AR}=-4.5~A,~R_{G}=25~\Omega \quad E_{AS}=\frac{1}{2}\cdot L\cdot I^{2}\cdot (\frac{B_{VDSS}}{B_{VDSS}-V_{DD}})\\ V_{DD}=-24~V,~L=1.0~mH \end{array}$$

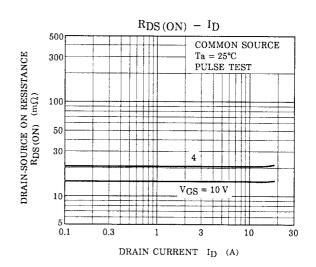


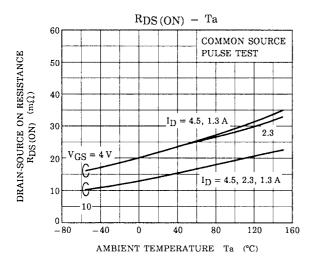


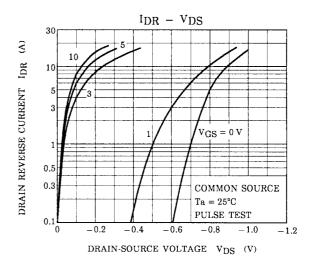


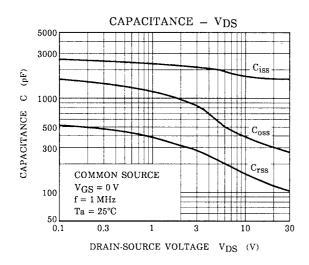


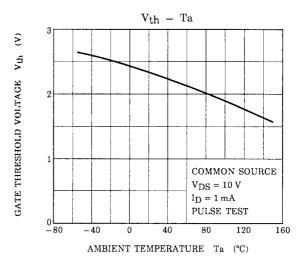


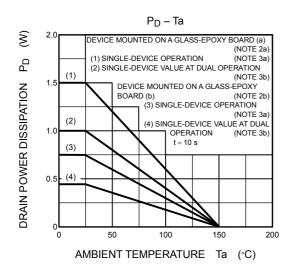


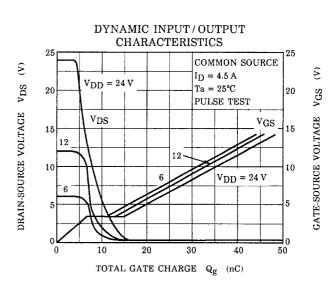




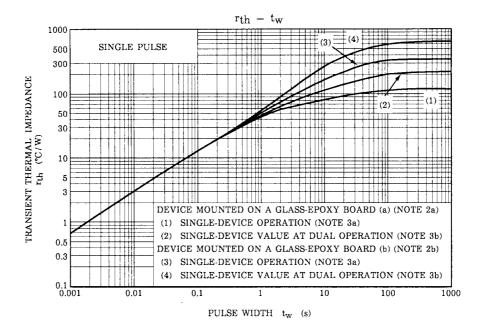


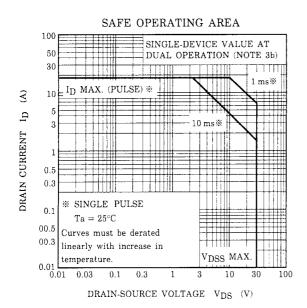


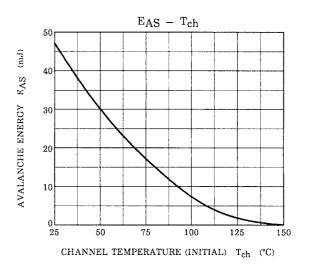


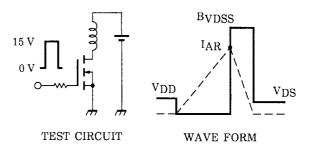


9









$$\begin{array}{l} T_{ch} = 25^{\circ}C \ (Initial) \\ Peak \ I_{AR} = 6 \ A, \ R_G = 25 \ \Omega \\ V_{DD} = 24 \ V, \ L = 1.0 \ mH \end{array} \\ \begin{array}{l} E_{AS} = \frac{1}{2} \cdot L \cdot I^2 \cdot (\ \frac{BVDSS}{BVDSS} - V_{DD}) \end{array}$$

RESTRICTIONS ON PRODUCT USE

000707EAA

- TOSHIBA is continually working to improve the quality and reliability of its products. Nevertheless, semiconductor devices in general can malfunction or fail due to their inherent electrical sensitivity and vulnerability to physical stress. It is the responsibility of the buyer, when utilizing TOSHIBA products, to comply with the standards of safety in making a safe design for the entire system, and to avoid situations in which a malfunction or failure of such TOSHIBA products could cause loss of human life, bodily injury or damage to property. In developing your designs, please ensure that TOSHIBA products are used within specified operating ranges as set forth in the most recent TOSHIBA products specifications. Also, please keep in mind the precautions and conditions set forth in the "Handling Guide for Semiconductor Devices," or "TOSHIBA Semiconductor Reliability Handbook" etc..
- The TOSHIBA products listed in this document are intended for usage in general electronics applications (computer, personal equipment, office equipment, measuring equipment, industrial robotics, domestic appliances, etc.). These TOSHIBA products are neither intended nor warranted for usage in equipment that requires extraordinarily high quality and/or reliability or a malfunction or failure of which may cause loss of human life or bodily injury ("Unintended Usage"). Unintended Usage include atomic energy control instruments, airplane or spaceship instruments, transportation instruments, traffic signal instruments, combustion control instruments, medical instruments, all types of safety devices, etc.. Unintended Usage of TOSHIBA products listed in this document shall be made at the customer's own risk.
- The information contained herein is presented only as a guide for the applications of our products. No
 responsibility is assumed by TOSHIBA CORPORATION for any infringements of intellectual property or other
 rights of the third parties which may result from its use. No license is granted by implication or otherwise under
 any intellectual property or other rights of TOSHIBA CORPORATION or others.
- The information contained herein is subject to change without notice.