

## 150KHz, 2A PWM Buck DC/DC Converter

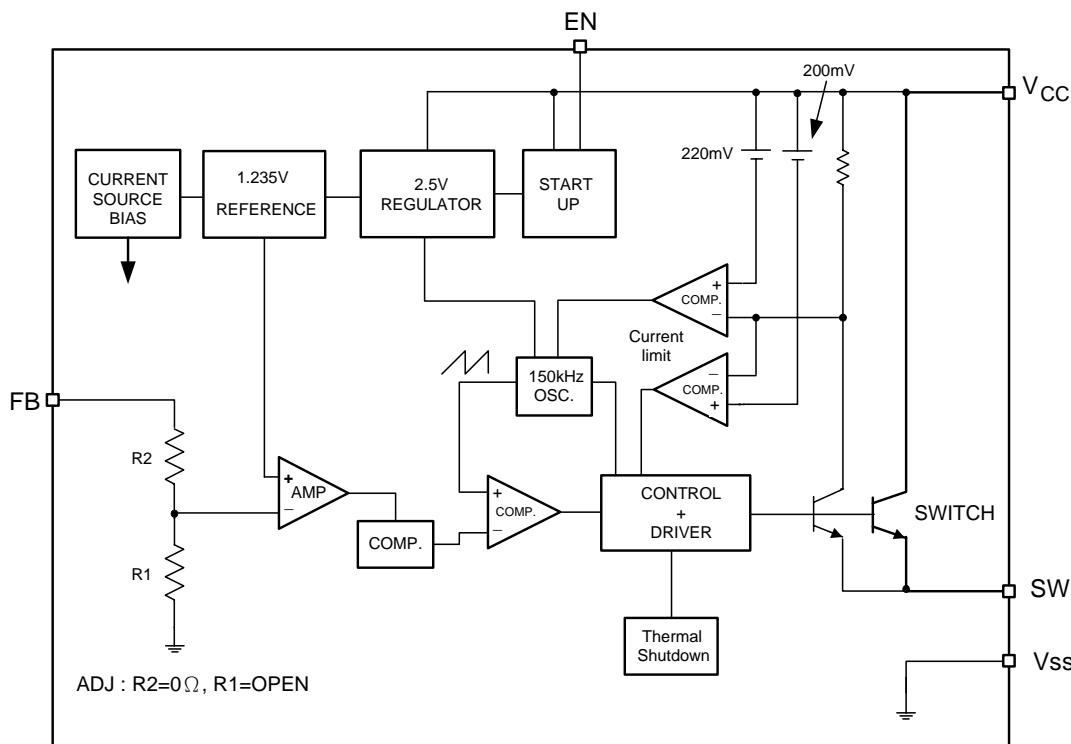
### ❖ GENERAL DESCRIPTION

The AX3001 series are monolithic IC designed for a step-down DC/DC converter, and own the ability of driving a 2A load without additional transistor. It saves board space. The external shutdown function can be controlled by logic level and then come into standby mode. The internal compensation makes feedback control having good line and load regulation without external design. Regarding protected function, thermal shutdown is to prevent over temperature operating from damage, and current limit is against over current operating of the output switch. If current limit function occurs and  $V_{FB}$  is down below 0.5V, the switching frequency will be reduced. The AX3001 series operates at a switching frequency of 150KHz thus allow smaller sized filter components than what would be needed with lower frequency switching regulators. The output version included fixed 3.3V, 5V, 12V, and an adjustable type. The chips are available in a standard 8-lead SOP package.

### ❖ FEATURES

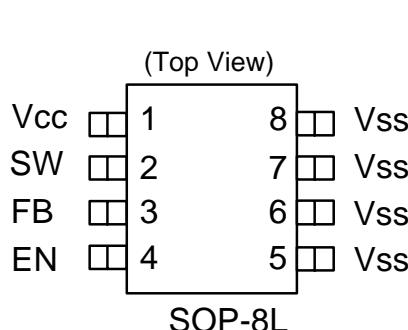
- Output voltage: 3.3V, 5V, 12V and adjustable output version.
- Adjustable version output voltage range, 1.23V to 19.5V.
- 150KHz fixed switching frequency.
- Voltage mode non-synchronous PWM control.
- Thermal-shutdown and current-limit protection.
- ON/OFF shutdown control input.
- Short Circuit Protect (SCP).
- Operating voltage can be up to 22V.
- Output load current: 2A.
- SOP-8L Pb-Free packages.
- Low power standby mode.
- Built-in switching transistor on chip.

## ❖ BLOCK DIAGRAM



## ❖ PIN ASSIGNMENT

The package of AX3001 is SOP-8L; the pin assignment is given by:

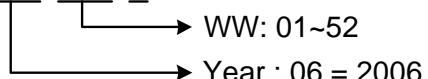
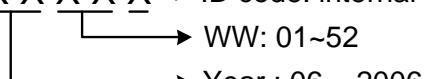


Name	Description
V <sub>CC</sub>	Operating voltage input
SW	Switching output
FB	Output voltage feedback control
EN	ON/OFF Shutdown
V <sub>SS</sub>	GND pin

## ❖ ORDER INFORMATION

Order Information		
AX3001- <u>XX</u> <u>XX</u> <u>X</u>		
Output Voltage	Package Type	Packing
33:3.3V	S: SOP-8L	Blank : Tube
50:5.0V		A : Taping
12: 12V		
Blank:ADJ		

## ❖ MARKING INFORMATION

Top Marking
<b>ADJ</b>
Logo  3001 → Part number X X X X X → ID code: internal  WW: 01~52 Year : 06 = 2006
FIX
Logo  3001 → Part number -50 → Output Voltage X X X X X → ID code: internal  WW: 01~52 Year : 06 = 2006

### ❖ Absolute Maximum Ratings

Characteristics	Symbol	Rating	Unit
Maximum Supply Voltage	V <sub>CC</sub>	+24	V
ON/OFF Pin Input Voltage	V <sub>EN</sub>	-0.3 to V <sub>CC</sub>	V
Feedback Pin Voltage	V <sub>FB</sub>	-0.3 to 16	V
Output Voltage to Ground	V <sub>OUT</sub>	-0.8	V
Power Dissipation Internally limited	PD	( T <sub>J</sub> -T <sub>A</sub> ) / θ <sub>JA</sub>	W
Storage Temperature Range	T <sub>ST</sub>	-65 to +150	°C
Operating Temperature Range	T <sub>OP</sub>	-20 to +125	°C
Operating Supply Voltage	V <sub>OP</sub>	+4.5 to +22	V
Thermal Resistance from Junction to case	θ <sub>JC</sub>	20	°C/W
Thermal Resistance from Junction to ambient	θ <sub>JA</sub>	60	°C/W

Note :  $\theta_{JA}$  is measured with the PCB copper area(need connect to V<sub>SS</sub> pins) of approximately 1.5 in<sup>2</sup> (Multi-layer).

### ❖ Electrical Characteristics (Unless otherwise specified, Ta=25°C, V<sub>CC</sub>=12V for 3.3V, 5V, adjustable version and V<sub>CC</sub>=18V for the 12V version. ILOAD = 0.2A)

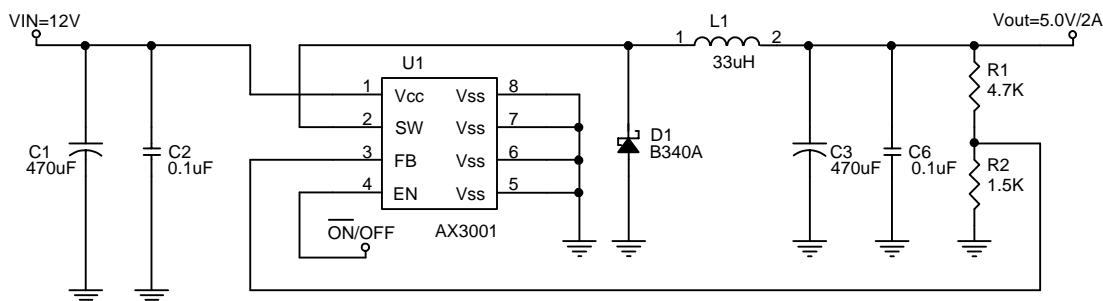
Characteristics	Symbol	Conditions	Min	Typ	Max	Units
Quiescent Current	I <sub>Q</sub>	V <sub>FB</sub> =12V force driver off		4	8	mA
Feedback bias current	I <sub>FB</sub>	V <sub>FB</sub> =1.3V (Adjustable version only)		-10	-50	nA
Shutdown supply Current	I <sub>SD</sub>	EN pin=5V V <sub>CC</sub> =22V		35	100	uA
Oscillator frequency	F <sub>OSC</sub>		127	150	173	KHz
Oscillator frequency of short circuit protect	F <sub>SCP</sub>	(Adjustable) When V <sub>FB</sub> <0.5V (Fixed)When < V <sub>OUT</sub> *40%		50		KHz
Max. Duty Cycle (ON)	DC	V <sub>FB</sub> =0V force driver on		100		%
Min. Duty Cycle (OFF)		V <sub>FB</sub> =12V force driver off		0		
Current limit	I <sub>CL</sub>	Pear current, No outside circuit V <sub>FB</sub> =0V force driver on	2.4			A
Saturation voltage	V <sub>SAT</sub>	I <sub>OUT</sub> =2A, No outside circuit V <sub>FB</sub> =0V force driver on		1.2	1.4	V
SW pin=0V	I <sub>SWL</sub>	No outside circuit V <sub>FB</sub> =12V force driver off			-200	uA
SW pin=-0.8V		V <sub>CC</sub> =22V force driver off		-5		mA
EN pin logic input threshold voltage	V <sub>IL</sub>	Low (regulator ON)	-	1.3	0.6	V
	V <sub>IH</sub>	High (regulator OFF)	2.0		-	
EN pin logic input current	I <sub>H</sub>	V <sub>EN</sub> =2.5V (OFF)		-0.1	-0.5	uA
EN pin input current	I <sub>L</sub>	V <sub>EN</sub> =0.5V (ON)			-0.01	
Thermal shutdown Temp	TSD			135		°C

### ❖ Electrical Characteristics (Continued)

version	Characteristics	Symbol	Conditions	Min	Typ	Max	Units
AX3001-ADJ	Output Feedback voltage	$V_{FB}$	$I_{LOAD} = 0.2A$ $V_{OUT}$ programmed for 3.3V	1.193	1.23	1.267	V
	Efficiency	$\eta$	$V_{CC} = 12V, I_{LOAD}=2A$		79		%
AX3001-3.3V	Output voltage	$V_{OUT}$	$I_{LOAD} = 0.2A$	3.20	3.30	3.40	V
	Efficiency	$\eta$	$V_{CC} = 12V, I_{LOAD}=2A$		80		%
AX3001-5.0V	Output voltage	$V_{OUT}$	$I_{LOAD} = 0.2A$	4.85	5.00	5.15	V
	Efficiency	$\eta$	$V_{CC} = 12V, I_{LOAD}=2A$		84		%
AX3001-12V	Output voltage	$V_{OUT}$	$I_{LOAD} = 0.2A$	11.64	12.0	12.36	V
	Efficiency	$\eta$	$V_{CC} = 15V, I_{LOAD} = 2A$		90		%

## ❖ Application Circuit

### (1) Adjustable Output Voltage Version



$$V_{out} = V_{FB} \times \left(1 + \frac{R_1}{R_2}\right)$$

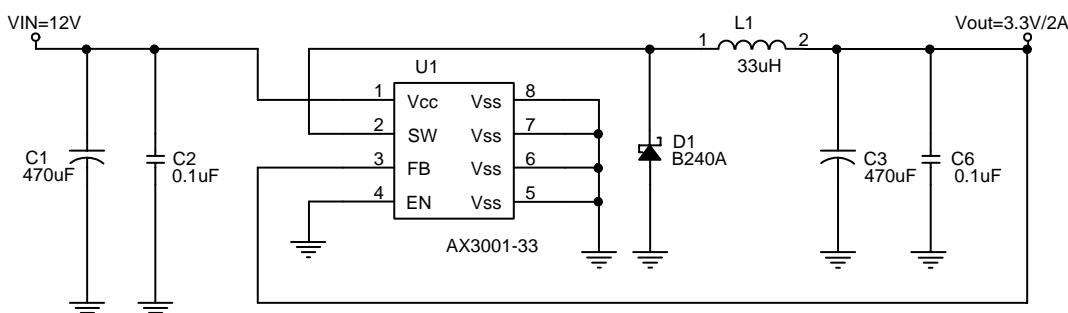
$$V_{FB} = 1.23V$$

$$R_2 = 0.47K \sim 2.6K$$

Table 1 Resistor select for output voltage setting

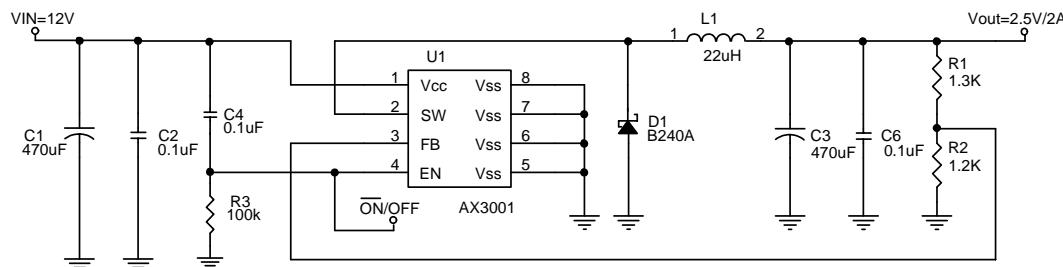
$V_{OUT}$	$R_2$	$R_1$
5V	1.5K	4.7K
	1.8K	5.6K
3.3V	1.5K	2.5K
	1.8K	3.0K
2.5V	1.8K	1.8K
1.8V	1.8K	0.82K

### (2) Fixed Output Voltage Version



### ❖ Application Circuit (Continued)

#### (3) Adjustable Output Voltage Version With Delayed Startup

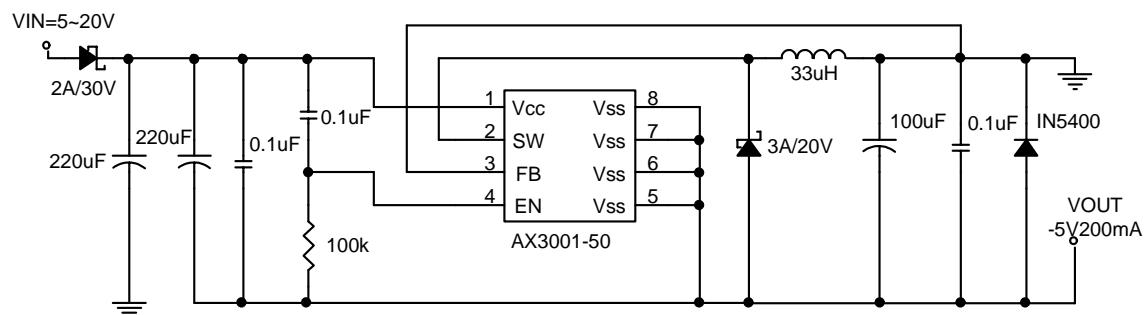


$$V_{out} = V_{FB} \times \left(1 + \frac{R_1}{R_2}\right)$$

$$V_{FB} = 1.23V$$

$$R_2 = 0.47K \sim 2.6K$$

#### (4) Inverting -5V Regulator with Delayed Startup



## ❖ Function Descriptions

### Pin Functions

#### Vcc

This is the positive input supply for the IC switching regulator. A suitable input bypass capacitor must be presented at this pin to minimize voltage transients and to supply the switching currents needed by the regulator.

#### Vss

Circuit ground.

#### SW

Internal switch. The voltage at this pin switches between ( $+V_{CC} - V_{SAT}$ ) and approximately – 0.5V, with a duty cycle of approximately  $V_{OUT} / V_{CC}$ . To minimize coupling to sensitive circuitry, the PC board copper area connected to this pin should be minimized.

#### Feedback

Senses the regulated output voltage to complete the feedback loop.

#### EN

Allows the switching regulator circuit to be shutdown using logic level signals thus dropping the total input supply current to approximately 100uA. Pulling this pin below a threshold voltage of approximately 1.3V turns the regulator on, and pulling this pin above 1.3V (up to a maximum of Vcc) shuts the regulator down. If this shutdown feature is not needed, the EN pin can be wired to the ground pin.

### Thermal Considerations

The SOP-8 package needs a heat sink under most conditions. The size of the heat sink depends on the input voltage, the output voltage, the load current and the ambient temperature. The AX3001 junction temperature rises above ambient temperature for a 2A load and different input and output voltages.

The data for these curves was taken with the AX3001 (SOP-8 package) operating as a buck-switching regulator in an ambient temperature of 25°C (still air). These temperature increments are all approximate and are affected by many factors. Higher ambient temperatures requires more heat sinker.

## ❖ Function Descriptions (Continued)

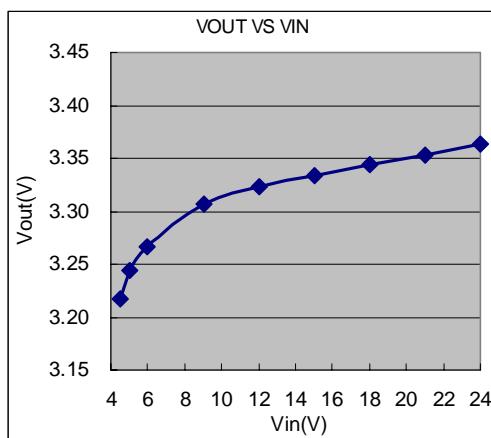
For the best thermal performance, wide copper traces and generous amounts of printed circuit board copper (need connect to the V<sub>ss</sub> pins) should be used in the board layout, (One exception is the SW(switch) pin, which should not have large areas of copper.) Large areas of copper provide the best transfer of heat (lower thermal resistance) to the surrounding air, and moving air lowers the thermal resistance even further.

Package thermal resistance and junction temperature increments are all approximate. The increments are affected by a lot of factors. Some of these factors include board size, shape, thickness, position, location, and even board temperature. Other factors are, trace width, total printed circuit copper area, copper thickness, single or double-sided, multi-layer board and the amount of solder on the board.

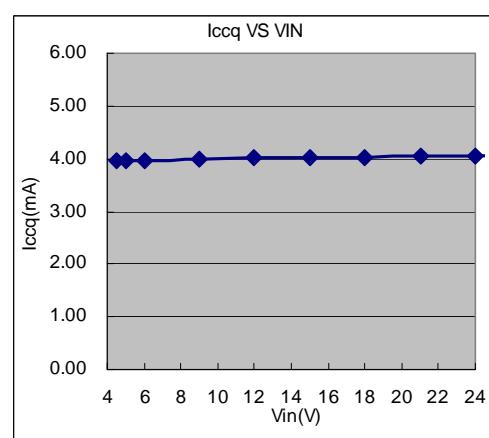
The effectiveness of the PC board to dissipate heat also depends on the size, quantity and spacing of other components on the board, as well as whether the surrounding air is still or moving. Furthermore, some of these components such as the catch diode will add heat to the PC board and the heat can vary as the input voltage changes. For the inductor, depending on the physical size, type of core material and the DC resistance, it could either act as a heat sink taking heat away from the board, or it could add heat to the board.

## ❖ Typical Characteristics

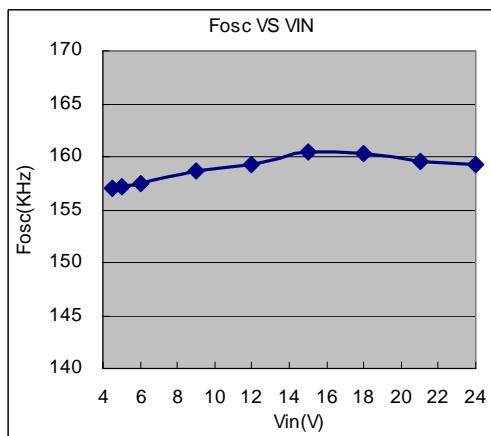
VOUT VS VIN



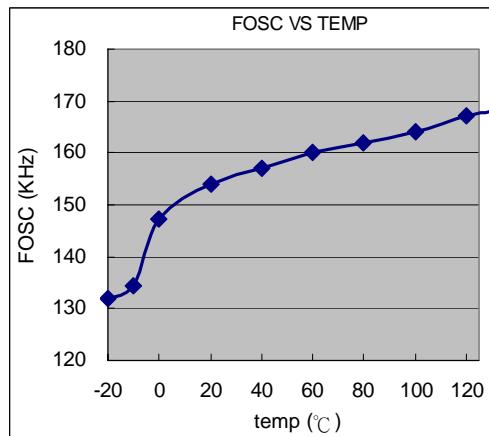
ICCQ VS VIN



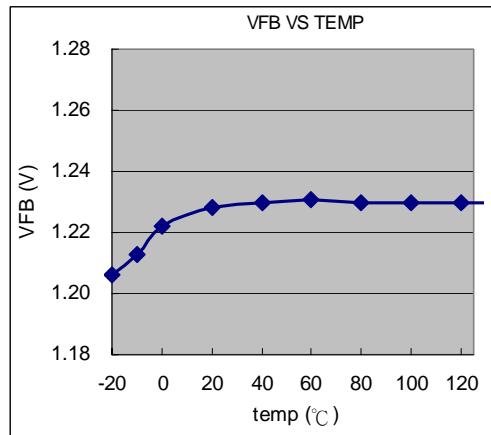
FOSC VS VIN



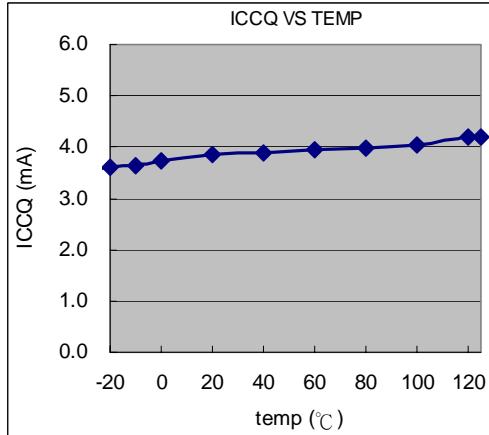
FOSC VS TEMPERATURE



VFB VS TEMPERATURE

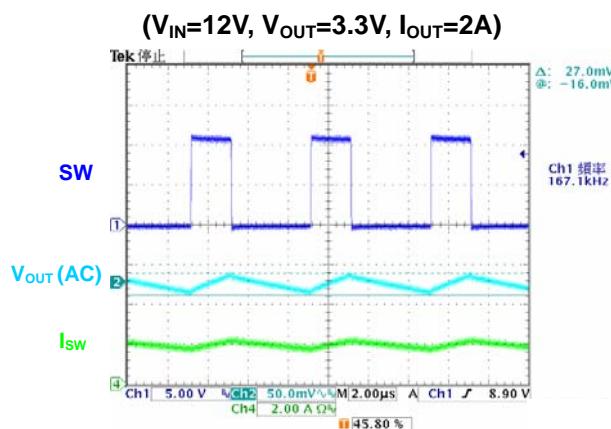


ICCQ VS TEMPERATURE

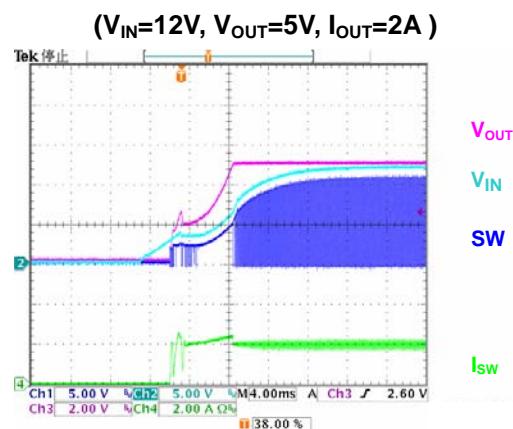


## ❖ Typical Characteristics

### Output Ripple

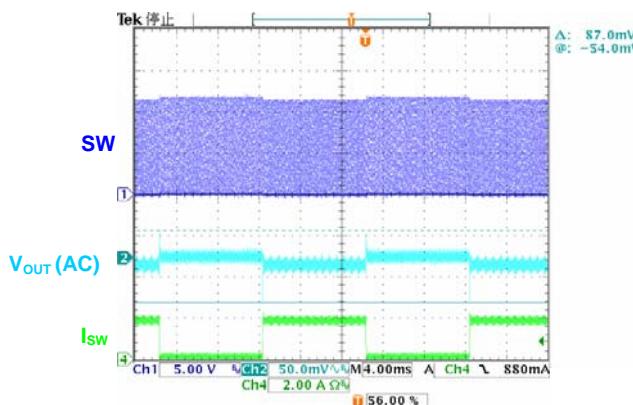


### Power on test wave



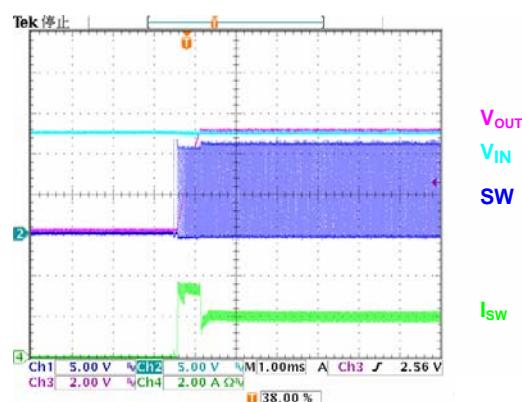
### Load Transient Response

( $V_{IN}=12V$ ,  $V_{OUT}=5V$ ,  $I_{OUT}=0.1\sim2A$ )



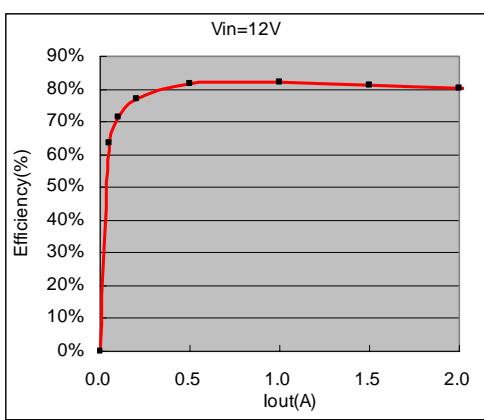
### EN on test wave

( $V_{IN}=12V$ ,  $V_{OUT}=5V$ ,  $I_{OUT}=2A$ )



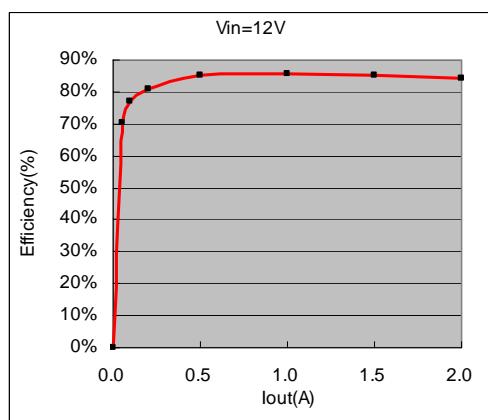
### Efficiency

( $V_{IN}=12V$ ,  $V_{OUT}=3.3V$ )

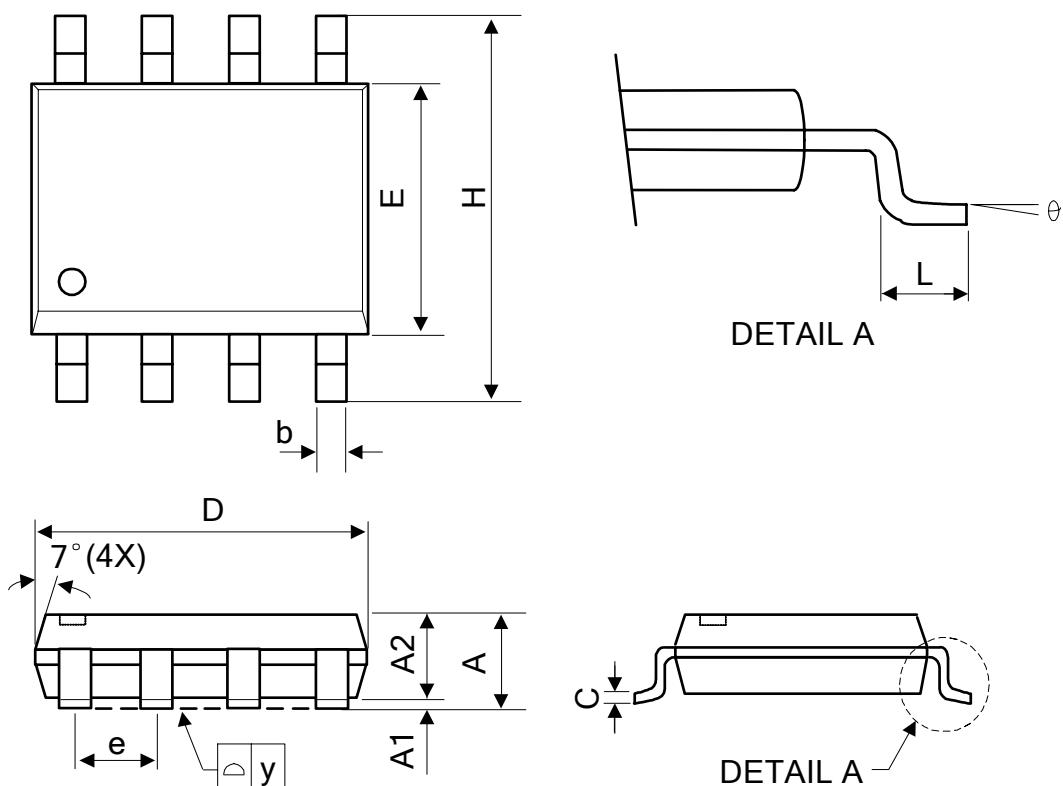


### Efficiency

( $V_{IN}=12V$ ,  $V_{OUT}=5.0V$ )



### ❖ Package Outlines



Symbol	Dimensions In Millimeters			Dimensions In Inches		
	Min.	Nom.	Max.	Min.	Nom.	Max.
A	1.40	1.60	1.75	0.055	0.063	0.069
A1	0.10	-	0.25	0.040	-	0.100
A2	1.30	1.45	1.50	0.051	0.057	0.059
C	0.19	0.20	0.25	0.0075	0.008	0.010
D	4.80	4.90	5.00	0.189	0.193	0.197
E	3.80	3.90	4.00	0.150	0.154	0.157
H	5.79	5.99	6.20	0.228	0.236	0.244
L	0.38	0.71	1.27	0.015	0.028	0.050
b	0.33	0.41	0.51	0.013	0.016	0.020
e	1.27 TYP			0.050 TYP		
y	-	-	0.10	-	-	0.004
θ	0°	-	80	0°	-	80