A4742



45 W Four-Channel (Bridge Circuit) Power Amplifier

Overview

The LA4742 is a 45 W 4-channel power amplifier IC for car stereo systems. It features a built-in bridge circuit and the ability to radically reduce the number of external components required.

Features

- Maximum output power: 45 W × 4 channels ($V_{CC} = 14.4 \text{ V}, 4 \Omega \text{ load}, 1 \text{ kHz}$)
- 40 W × 4 channels (V_{CC} = 13.7 V, 4 Ω load, 1 kHz)
- Requires only seven external components and does not require an oscillation prevention RC circuit, a noise filter, or a BS capacitor.

Functions

- Output offset detection function (DDL)
- Warning tone (beep) generation function
- Muting function
- Built-in standby switch
- Full complement of built-in protection circuits, including protection from shorting to V_{CC}, shorting to ground, load shorting, overvoltages, and overheating.
- Maximum supply voltage before damage in the open ground state: 16 V

Specifications Maximum Ratings at Ta = 25°C

Parameter Ratings Unit Symbol Conditions V_{CC} max1 Signal present V 18 Maximum supply voltage No signal (for 1 minute) V V_{CC} max2 26 Maximum output current I_Opeak 4.5/ch A Allowable power dissipation Pd max 50 w With an arbitrarily large heat sink Operating temperature Topr -40 to +85 °C Storage temperature Tstg -40 to +150 °C °C/W Package thermal resistance θјс 1

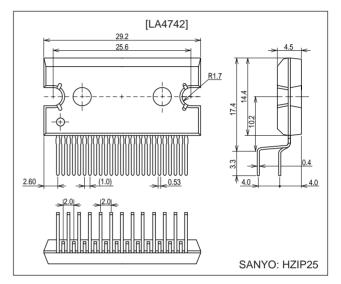
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Package Dimensions

unit: mm

3236-HZIP25

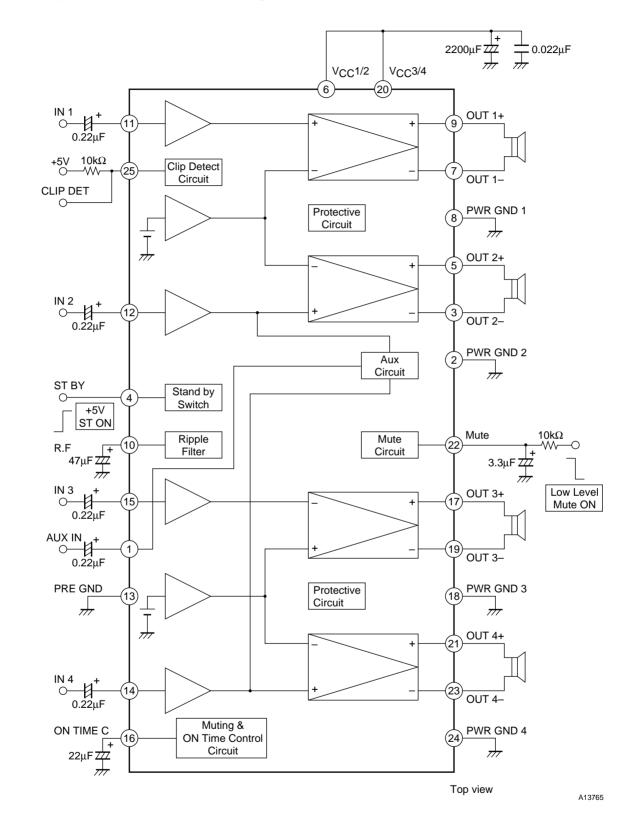


Operating Conditions at $Ta = 25^{\circ}C$

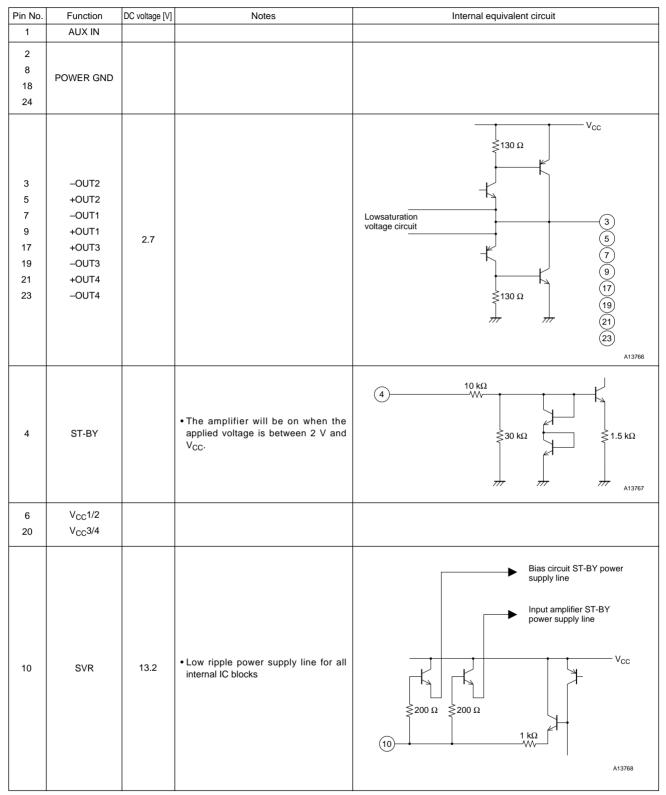
Parameter	Symbol	Conditions	Ratings	Unit
Recommended supply voltage	V _{CC}		14.4	V
Recommended load resistance	RL		4	Ω
Operating supply voltage range	V _{CC} op		9 to 18	V

Operating Characteristics at Ta = 25°C, V_{CC} = 14.4 V, f = 1 kHz, R_L = 4 $\Omega,$ Rg = 600 Ω

Parameter	Sumbol	Conditions	Ratings			Unit
	Symbol		min	typ	max	
Quiescent current	Icco	$R_{L} = \infty, Rg = 0$	100	200	350	mA
Standby current	Ist	Vst = 0 V			10	μA
Output offset voltage	V _N offset	Rg = 0	-100		+100	mV
Voltage gain	VG	$V_0 = 0 \text{ dBm}$	25	26	27	dB
Voltage gain difference	ΔVG		-1		+1	dB
Output power	P ₀ 1	THD = 10 %	23	28		W
	P _O max1	V _{CC} = 13.7 V, V _{IN} = 5 Vrms		40		W
	P _O max2	V _{IN} = 5 Vrms		45		W
Total harmonic distortion THD P		$P_{O} = 4 W$		0.05	0.4	%
Channel separation	CHsep	$V_0 = 0 \text{ dBm}, \text{Rg} = 10 \text{ k}\Omega$	55	65		dB
Ripple rejection ratio	SVRR	$f_r = 100 \text{ Hz}, \text{ V}_R = 0 \text{ dBm}, \text{ Rg} = 0$	50	60		dB
Output noise voltage	V _{NO}	Rg = 0, B.P.F. = 20 Hz to 20 kHz		100	200	μVrms
Muting attenuation	Mute(att)	V _O = 20 dBm	70	80		dB



Sample Application Circuit and Block Diagram



Pin Functions and Equivalent Circuits at V_{CC} = 14.4 V, ST-BY = 5 V

Continued on next page.

Continued from preceding page.

Pin No. Function DC voltage [V] Notes Internal equivalent circuit Image: Input amplifier Image:	
$ \begin{array}{ c c c c c c } 11 & IN1 \\ 12 & IN2 \\ 14 & IN4 \\ 15 & IN3 \\ \end{array} $	Inverter amplifier A13769
13 PRE GND	
16 ON TIME MUTE 2.6 • Amplifier turn-on time control circuit • Impulse noise prevention circuit • With a 22 μ F capacitor, the turn-on time will be 0.6 s.	 20 kΩ 10 kΩ 2 kΩ 13 kΩ
22 MUTE 4.1 The making labeled is under 1 V. the applied voltage is under 1 V. V_{REF}	0 Ω W-22

Notes on Usage and Handling

Oscillator stabilization

In some cases, details of the printed circuit board layout may lead to induced parasitic oscillation. This oscillation can be prevented by adding any one of the following components. Verify the optimal values for these capacitors by testing in actual end products.

Technique 1 ... Connect Mylar capacitors (0.1 µF) between the BTL amplifier outputs.

Technique 2 ... Connect an RC circuit (2.2 Ω and 0.1 μ F in series) between each output and ground.

• Audio quality (low band)

The low-band frequency characteristics can be improved by adjusting the values of the input capacitors. The recommended value is $2.2 \,\mu\text{F}$.

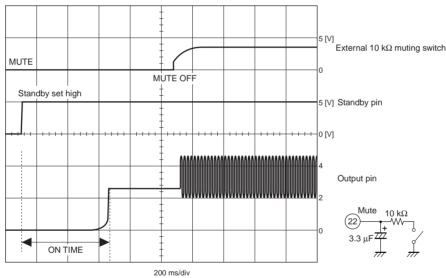
• Impulse noise

The LA4742 includes a built-in impulse noise suppression circuit. However, further improvement can be achieved by using the muting circuit. When first applying power, activate the muting function at the same time as applying power. Then, after the output DC potential has stabilized, turn off the muting function. When turning off the power, first activate the muting function and then turn off the power. Sample transient responses are attached (see the timing charts).

Transient Responses at Power On

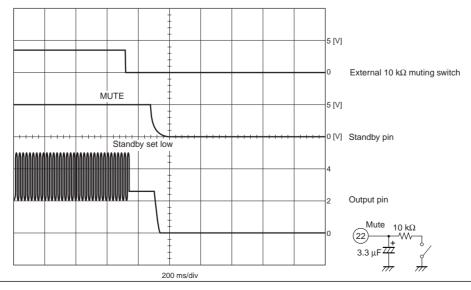
Power on: Standby and muting activated at the same time.

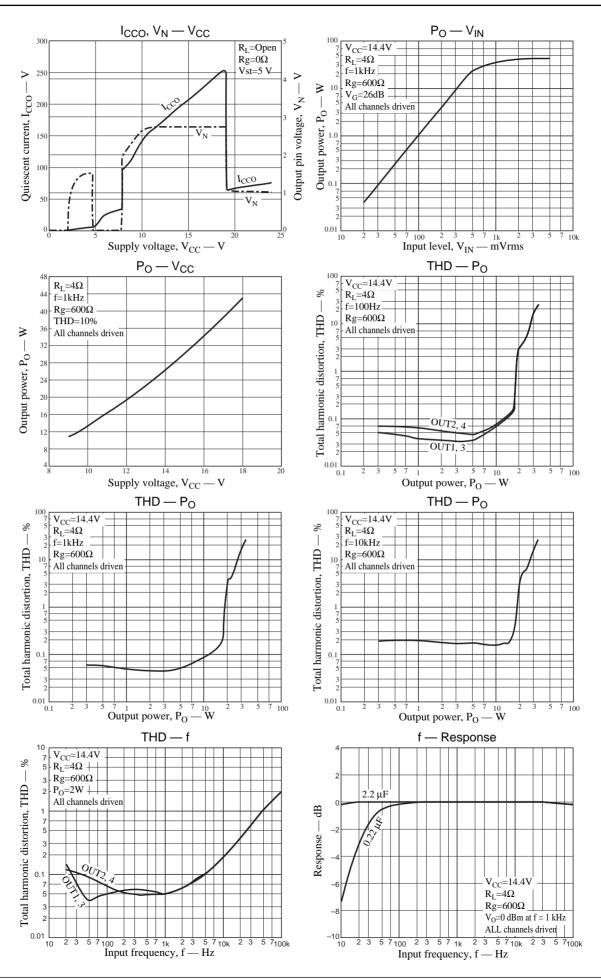
Muting is turned off after the output has stabilized.

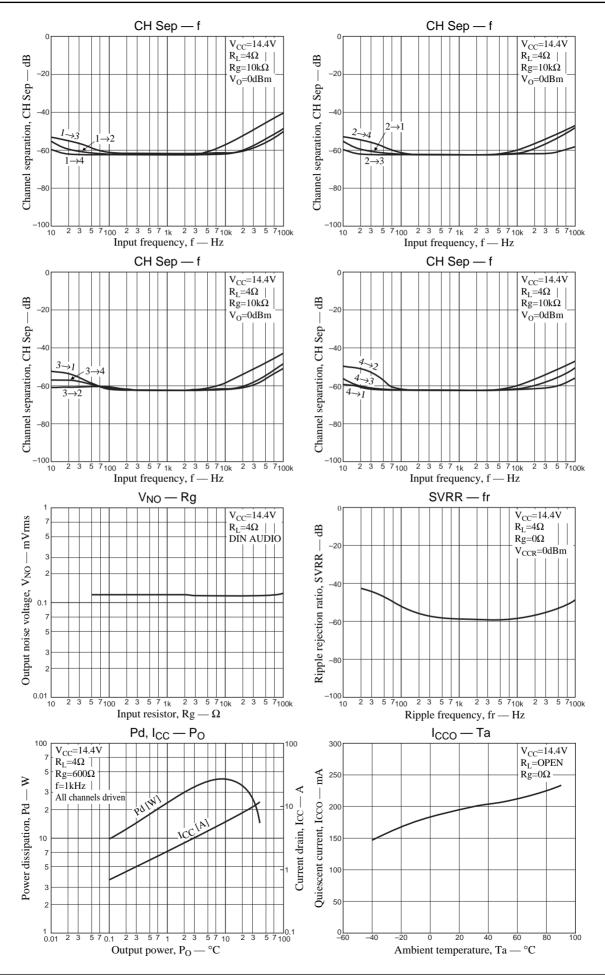


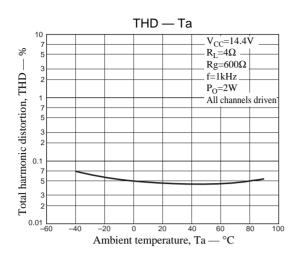
Transient Responses at Power Off

Power off: After activating the muting circuit, turn the power off.









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