# **Freescale Semiconductor**

MPX10 Rev 14, 10/2008

# 10 kPa Uncompensated Silicon Pressure Sensors

The MPX10 series silicon piezoresistive pressure sensors provide a very accurate and linear voltage output, directly proportional to the applied pressure. These standard, low cost, uncompensated sensors permit manufacturers to design and add their own external temperature compensation and signal conditioning networks. Compensation techniques are simplified because of the predictability of Freescale's single element strain gauge design.

#### **Features**

- Low Cost
- Patented Silicon Shear Stress Strain Gauge Design
- Ratiometric to Supply Voltage
- · Differential and Gauge Options
- Durable Epoxy Unibody Element or Thermoplastic (PPS) Surface Mount Package

# MPX10 Series

0 to 10 kPa (0 to 1.45 psi) 35 mV Full Scale Span (Typical)

#### **Application Examples**

- Air Movement Control
- Environmental Control Systems
- · Level Indicators
- Leak Detection
- Medical Instrumentation
- Industrial Controls
- · Pneumatic Control Systems
- Robotics

				ORDERI	NG INFORM	MATION			
Device Name	Package Case		# of Ports		Pressure Type			Device	
	Options	No.	None	Single	Dual	Gauge	Differential	Absolute	Marking
Unibody Packa	ge (MPX10	Series)							
MPX10D	Tray	344	•				•		MPX10D
MPX10DP	Tray	344C			•		•		MPX10DP
MPX10GP	Tray	344B		•		•			MPX10GP
Small Outline F	ackage (MF	XV10G S	eries)	•	•				*
MPXV10GC6U	Rail	482A		•		•			MPXV10G
MPXV10GC7U	Rail	482C		•		•			MPXV10G

### **SMALL OUTLINE PACKAGE**



MPXV10GC6U CASE 482A-01

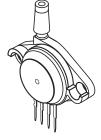


MPXV10GC7U CASE 482C-03



MPX10D CASE 344-15

# UNIBODY PACKAGE



MPX10GP CASE 344B-01



MPX10DP CASE 344C-01



# **Operating Characteristics**

Table 1. Operating Characteristics ( $V_S = 3.0 \text{ Vdc}$ ,  $T_A = 25^{\circ}\text{C}$  unless otherwise noted, P1 > P2)

Characteristic	Symbol	Min	Тур	Max	Units
Differential Pressure Range <sup>(1)</sup>	P <sub>OP</sub>	0	_	10	kPa
Supply Voltage <sup>(2)</sup>	V <sub>S</sub>	_	3.0	6.0	V <sub>DC</sub>
Supply Current	Io	_	6.0	_	mAdc
Full Scale Span <sup>(3)</sup>	V <sub>FSS</sub>	20	35	50	mV
Offset <sup>(4)</sup>	V <sub>OFF</sub>	0	20	35	mV
Sensitivity	ΔV/ΔΡ	_	3.5	_	mV/kPa
Linearity	_	-1.0	_	1.0	%V <sub>FSS</sub>
Pressure Hysteresis (0 to 10 kPa)	_	_	±0.1	_	%V <sub>FSS</sub>
Temperature Hysteresis	_	_	±0.5	_	%V <sub>FSS</sub>
Temperature Coefficient of Full Scale Span	TCV <sub>FSS</sub>	-0.22	_	-0.16	%V <sub>FSS</sub> /°C
Temperature Coefficient of Offset	TCV <sub>OFF</sub>	_	±15	_	μV/°C
Temperature Coefficient of Resistance	TCR	0.21	_	0.27	%Z <sub>IN</sub> /°C
Input Impedance	Z <sub>IN</sub>	400	_	550	Ω
Output Impedance	Z <sub>OUT</sub>	750	_	1250	Ω
Response Time <sup>(5)</sup> (10% to 90%)	t <sub>R</sub>	_	1.0	_	ms
Warm-Up Time <sup>(6)</sup>	_	_	20	_	ms
Offset Stability <sup>(7)</sup>	_	_	±0.5	_	%V <sub>FSS</sub>

- 1. 1.0 kPa (kiloPascal) equals 0.145 psi.
- 2. Device is ratiometric within this specified excitation range. Operating the device above the specified excitation range may induce additional error due to device self-heating.
- Full Scale Span (V<sub>FSS</sub>) is defined as the algebraic difference between the output voltage at full rated pressure and the output voltage at the minimum rated pressure.
- 4. Offset (V<sub>OFF</sub>) is defined as the output voltage at the minimum rated pressure.
- 5. Response Time is defined as the time for the incremental change in the output to go from 10% to 90% of its final value when subjected to a specified step change in pressure.
- 6. Warm-up Time is defined as the time required for the product to meet the specified output voltage after the pressure is stabilized.
- 7. Offset stability is the product's output deviation when subjected to 1000 hours of Pulsed Pressure, Temperature Cycling with Bias Test.

# **Maximum Ratings**

Table 2. Maximum Ratings<sup>(1)</sup>

Rating	Symbol	Value	Unit
Maximum Pressure (P1 > P2)	P <sub>MAX</sub>	75	kPa
Burst Pressure (P1 > P2)	P <sub>BURST</sub>	100	kPa
Storage Temperature	T <sub>STG</sub>	-40 to +125	°C
Operating Temperature	T <sub>A</sub>	-40 to +125	°C

<sup>1.</sup> Exposure beyond the specified limits may cause permanent damage or degradation to the device.

Figure 1 shows a schematic of the internal circuitry on the stand-alone pressure sensor chip.

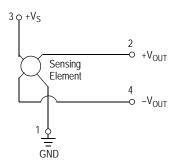


Figure 1. Uncompensated Pressure Sensor Schematic

# **Voltage Output versus Applied Differential Pressure**

The output voltage of the differential or gauge sensor increases with increasing pressure applied to the pressure side (P1) relative to the vacuum side (P2). Similarly, output

voltage increases as increasing vacuum is applied to the vacuum side (P2) relative to the pressure side (P1).

### **Temperature Compensation**

Figure 2 shows the typical output characteristics of the MPX10 series over temperature.

Because this strain gauge is an integral part of the silicon diaphragm, there are no temperature effects due to differences in the thermal expansion of the strain gauge and the diaphragm, as are often encountered in bonded strain gauge pressure sensors. However, the properties of the strain gauge itself are temperature dependent, requiring that the device be temperature compensated if it is to be used over an extensive temperature range.

Temperature compensation and offset calibration can be achieved rather simply with additional resistive components, or by designing your system using the MPX2010D series sensor.

#### **LINEARITY**

Linearity refers to how well a transducer's output follows the equation:  $V_{out} = V_{off} + \text{sensitivity } \times P$  over the operating pressure range (Figure 3). There are two basic methods for calculating nonlinearity: 1) end point straight line fit or 2) a least squares best line fit. While a least squares fit gives the

"best case" linearity error (lower numerical value), the calculations required are burdensome.

Conversely, an end point fit will give the "worst case" error (often more desirable in error budget calculations) and the calculations are more straightforward for the user. Freescale's specified pressure sensor linearities are based on the end point straight line method measured at the midrange pressure.

Figure 4 illustrates the differential or gauge configuration in the basic chip carrier (Case 344). A silicone gel isolates the die surface and wire bonds from the environment, while allowing the pressure signal to be transmitted to the silicon diaphragm.

The MPX10 series pressure sensor operating characteristics and internal reliability and qualification tests are based on use of dry air as the pressure media. Media other than dry air may have adverse effects on sensor performance and long term reliability. Contact the factory for information regarding media compatibility in your application.Refer to application note AN3728, for more information regarding media compatibility.

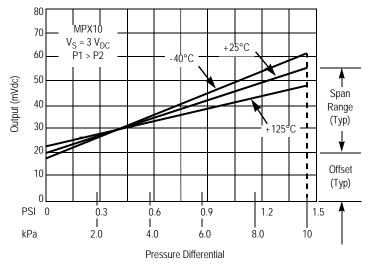


Figure 2. Output vs. Pressure Differential

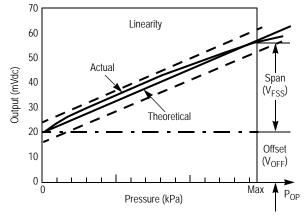


Figure 3. Linearity Specification Comparison

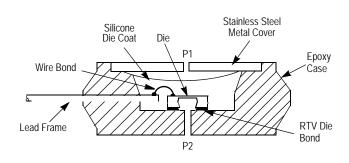


Figure 4. Unibody Package — Cross-Sectional Diagram (Not to Scale)

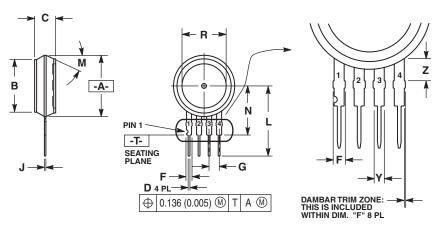
# PRESSURE (P1)/VACUUM (P2) SIDE IDENTIFICATION TABLE

Freescale designates the two sides of the pressure sensor as the Pressure (P1) side and the Vacuum (P2) side. The Pressure (P1) side is the side containing silicone gel which isolates the die from the environment. The pressure sensor is designed to operate with positive differential pressure applied, P1 > P2.

The Pressure (P1) side may be identified by using the following table.

Part Number	Case Type	Pressure (P1) Side Identifier
MPX10D	344	Stainless Steel Cap
MPX10DP	344C	Side with Part Marking
MPX10GP	344B	Side with Port Attached
MPXV10GC6U	482A	Side with Port Attached
MPXV10GC7U	482C	Side with Port Attached

#### PACKAGE DIMENSIONS

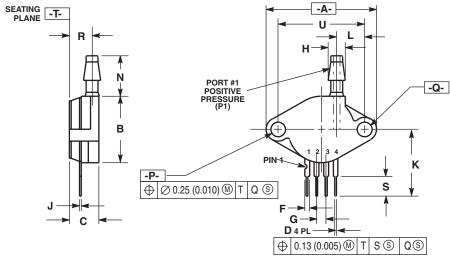


#### NOTES:

- DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
- 2. CONTROLLING DIMENSION: INCH.
  3. DIMENSION -A- IS INCLUSIVE OF THE MOLD STOP RING. MOLD STOP RING NOT TO EXCEED. 16.00 (0.630).

0.00 (0.000).						
	INC	HES	MILLIMETERS			
DIM	MIN	MAX	MIN	MAX		
Α	0.595	0.630	15.11	16.00		
В	0.514	0.534	13.06	13.56		
С	0.200	0.220	5.08	5.59		
D	0.016	0.020	0.41	0.51		
F	0.048	0.064	1.22	1.63		
G	0.100 BSC		2.54 BSC			
J	0.014	0.016	0.36	0.40		
L	0.695	0.725	17.65	18.42		
M	30°	NOM	1 °08	MOV		
N	0.475	0.495	12.07	12.57		
R	0.430	0.450	10.92	11.43		
Υ	0.048	0.052	1.22	1.32		
Z	0.106	0.118	2.68	3.00		

#### **CASE 344-15 ISSUE AA UNIBODY PACKAGE**



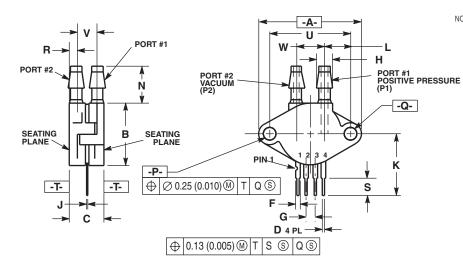
#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982. 2. CONTROLLING DIMENSION: INCH.

	INC	HES	MILLIM	<b>IETERS</b>
DIM	MIN	MAX	MIN	MAX
Α	1.145	1.175	29.08	29.85
В	0.685	0.715	17.40	18.16
С	0.305	0.325	7.75	8.26
D	0.016	0.020	0.41	0.51
F	0.048	0.064	1.22	1.63
G	0.100	BSC	2.54 BSC	
Н	0.182	0.194	4.62	4.93
J	0.014	0.016	0.36	0.41
K	0.695	0.725	17.65	18.42
L	0.290	0.300	7.37	7.62
Ν	0.420	0.440	10.67	11.18
Р	0.153	0.159	3.89	4.04
Q	0.153	0.159	3.89	4.04
R	0.230	0.250	5.84	6.35
S	0.220	0.240	5.59	6.10
U	0.910	BSC	23.11	BSC

**CASE 344B-01 ISSUE B UNIBODY PACKAGE** 

#### **PACKAGE DIMENSIONS**

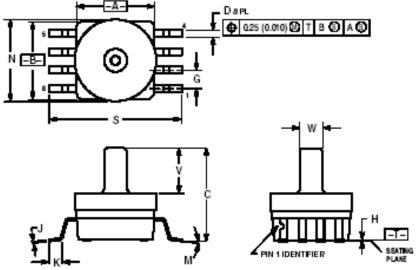


#### NOTES:

- DIMENSIONING AND TOLERANCING PER ANSI
- 2. CONTROLLING DIMENSION: INCH.

	INC	HES	MILLIMETERS		
DIM	MIN	MAX	MIN	MAX	
Α	1.145	1.175	29.08	29.85	
В	0.685	0.715	17.40	18.16	
C	0.405	0.435	10.29	11.05	
D	0.016	0.020	0.41	0.51	
F	0.048	0.064	1.22	1.63	
G	0.100 BSC		2.54	BSC	
Н	0.182	0.194	4.62	4.93	
٦	0.014	0.016	0.36	0.41	
K	0.695	0.725	17.65	18.42	
L	0.290	0.300	7.37	7.62	
N	0.420	0.440	10.67	11.18	
Р	0.153	0.159	3.89	4.04	
Ø	0.153	0.159	3.89	4.04	
R	0.063	0.083	1.60	2.11	
S	0.220	0.240	5.59	6.10	
U	0.910 BSC		23.11 BSC		
٧	0.248	0.278	6.30	7.06	
W	0.310	0.330	7.87	8.38	

#### **CASE 344C-01 ISSUE B UNIBODY PACKAGE**



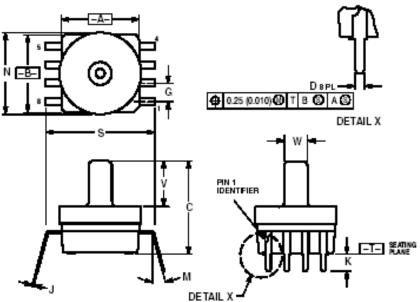
#### NO TES:

- DIMENSIONING AND TOLERANDING PER ANSI
  YIA SM, 1982.
   CONTROLLING DIMENSION: NICH.
   DIMENSION A AND B DO NOT INCLUDE MOLD.
- PROTRUSION.
  MAXIMUM MOLD PROTRUSION 0.15 (0.006).
  ALL VERTICAL SURRICES 5° TYPICAL DRAFT.
  - 0.500 H 0.002 0.010 J 0.009 0.011 K 0.061

**CASE 482A-01 ISSUE A SMALL OUTLINE PACKAGE** 

# **PACKAGE DIMENSIONS**

**CASE 482C-03 ISSUE B SMALL OUTLINE PACKAGE** 



- NOTES:
  1. CIMENISONING WID TO LEPANCING PER WISI
  Y145W, 1982.
  2. CONTROLLING DIMENISON: NICH.
  3. CIMENISONIA AND BIDONOT INCLUDE MOID
  PROTRUSION.
  4. MAXIMUM MOLD PROTRUSION 0.15 (0.008).
  5. ALL VERTICAL SURFACES 5° TYPICAL DRAFT.
  6. CIMENISONIS TO CENTER OF LEAD WHEN
  PORMED PARALLEL.

П	NC	HES:	MILLIMETERS			
DIN	Ne	MAX	MEN	MAX		
4	0.415	0.425	10.54	10.79		
ø	0.415	0.425	1054	10.79		
ø	0.500	0.520	12.70	1321		
0	0.026	0.034	0.66	0.864		
ø	0.100	BSC	254 BSC			
7	0.009	0.011	0.23	0.28		
K	0.100	0.120	254	305		
2	0.0	15 0	0 0	15 □		
×	0.444	0.448	11.28	11.38		
œ	0.540	0.560	13.72	14.22		
٧	0.245	0.255	622	6.48		
W	0.115	0.125	2.92	3.17		

#### How to Reach Us:

#### Home Page:

www.freescale.com

#### Web Support:

http://www.freescale.com/support

#### **USA/Europe or Locations Not Listed:**

Freescale Semiconductor, Inc.
Technical Information Center, EL516
2100 East Elliot Road
Tempe, Arizona 85284
1-800-521-6274 or +1-480-768-2130
www.freescale.com/support

### Europe, Middle East, and Africa:

Freescale Halbleiter Deutschland GmbH Technical Information Center Schatzbogen 7 81829 Muenchen, Germany +44 1296 380 456 (English) +46 8 52200080 (English) +49 89 92103 559 (German) +33 1 69 35 48 48 (French) www.freescale.com/support

#### Japan:

Freescale Semiconductor Japan Ltd. Headquarters ARCO Tower 15F 1-8-1, Shimo-Meguro, Meguro-ku, Tokyo 153-0064 Japan 0120 191014 or +81 3 5437 9125 support.japan@freescale.com

#### Asia/Pacific:

Freescale Semiconductor China Ltd. Exchange Building 23F No. 118 Jianguo Road Chaoyang District Beijing 100022 China +86 010 5879 8000 support.asia@freescale.com

#### For Literature Requests Only:

Freescale Semiconductor Literature Distribution Center P.O. Box 5405
Denver, Colorado 80217
1-800-441-2447 or +1-303-675-2140
Fax: +1-303-675-2150
LDCForFreescaleSemiconductor@hibbertgroup.com

Information in this document is provided solely to enable system and software implementers to use Freescale Semiconductor products. There are no express or implied copyright licenses granted hereunder to design or fabricate any integrated circuits or integrated circuits based on the information in this document.

Freescale Semiconductor reserves the right to make changes without further notice to any products herein. Freescale Semiconductor makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does Freescale Semiconductor assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation consequential or incidental damages. "Typical" parameters that may be provided in Freescale Semiconductor data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals", must be validated for each customer application by customer's technical experts. Freescale Semiconductor does not convey any license under its patent rights nor the rights of others. Freescale Semiconductor products are not designed, intended, or authorized for use as components in systems intended for surgical implant into the body, or other applications intended to support or sustain life, or for any other application in which the failure of the Freescale Semiconductor product could create a situation where personal injury or death may occur. Should Buyer purchase or use Freescale Semiconductor products for any such unintended or unauthorized application, Buyer shall indemnify and hold Freescale Semiconductor and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that Freescale Semiconductor was negligent regarding the design or manufacture of the part.

Freescale<sup>™</sup> and the Freescale logo are trademarks of Freescale Semiconductor, Inc. All other product or service names are the property of their respective owners.

© Freescale Semiconductor, Inc. 2008. All rights reserved.

