ELECTRONICS, INC.
44 FARRAND STREET
BLOOMFIELD, NJ 07003
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NTE7031
Integrated Circuit
Module – AF Power Amp, Single Channel, 100W Min

Features:

- Built–In Muting Circuit Reduces Pop On Noises

Absolute Maximum Ratings: (T_A = +25°C unless otherwise specified)

Maximum Supply Voltage, V_{CC}max . . . . . ±73V
Thermal Resistance, Junction–to–Case, R_{thJC} . . . . . 1.1°C/W
Junction Temperature, T_J . . . . . +150°C
Operating Case Temperature, T_C . . . . . +125°C
Storage Temperature Range, T_{stg} . . . . . –30° to +125°C
Available Time for Shorted Load (V_{CC} = ±51.0V, R_L = 8Ω, f = 50Hz, P_O = 100W), t_s 2sec

Recommended Operating Conditions: (T_A = +25°C unless otherwise specified)

Operating Voltage, V_{CC} . . . . . ±51.0V
Load Resistance, R_L 8Ω

Operating Characteristics: (T_A = +25°C, V_{CC} = ±51.0V, R_L = 8Ω, R_g = 600Ω, VG = 40dB, R_L: Non–Inductive Load unless otherwise specified)

Parameter	Symbol	Test Conditions	Min	Typ	Max	Unit
Quiescent Current	I_{CCO}	V_{CC} = ±61V	15	–	120	mA
Output Power	P_O	THD = 0.4%, f = 20Hz to 20kHz	100	–	–	W
Total Harmonic Distortion	THD	P_O = 1.0W, f = 1kHz	–	–	0.4	%
Frequency Response	f	P_O = 1.0W, +0dB, –3dB	20 to 50k			Hz
Input Resistance	r_i	P_O = 1.0W, f = 1kHz	–	55	–	kΩ
Output Noise Voltage	V_{NO}	V_{CC} = ±61V, R_g = 10kΩ	–	–	1.2	mVrms
Midpoint Voltage	V_N	V_{CC} = ±61V	–70	0	+70	mV

Pin Connection Diagram
(Front View)