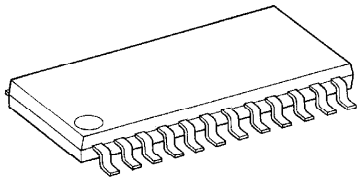
TOSHIBA BIPOLAR LINEAR INTEGRATED CIRCUIT SILICON MONOLITHIC

TA2145AF

3 V STEREO HEADPHONE AMPLIFIER (3 V USE)

The TA2145AF is developed for play-back stereo headphone equipments (3 V USE).
It is built in dual preamplifiers, dual OCL power amplifiers, motor governor, DC volume control and preamplifier on / off switch etc.



SSOP24-P-300-1.00

Weight : 0.32 g (Typ.)

FEATURES

- Built-in preamplifier
  Input coupling condenser-less
  Built-in input capacitor for reducing buzz noise
  Low noise : V_{ni} = 1.2 μV_{rms} (Typ.)
  Preamplifier on / off switch.
- Built-in power amplifier
  OCL (Output condenser-less)
  Voltage gain : G_V = 31 dB (Typ.)
- Built-in motor governor
  Current proportion type
- Built-in DC volume control function
  DC volume maximum attenuation : ATT = 82 dB (Ta = 25°C, Typ.)
- Built-in bass boost function
- Low supply current (V_{CC} = 3 V, Ta = 25°C, Typ.)
  PRE + PW (f = 1 kHz, PRE OUT = 100 mV_{rms})

	No Signal (Vol : MIN.)	Output Power	
		0.1 mW × 2	0.5 mW × 2
R_L = 16 Ω	8.5 mA	10.5 mA	16.5 mA
R_L = 32 Ω	8.5 mA	9.8 mA	14.0 mA

  GVN : I_{CC} = 2.5 mA
- Operating supply voltage range (Ta = 25°C)
  PRE + PW : $V_{CC\,(opr)}$ = 1.8~3.6 V
  GVN : $V_{CC\,(opr)}$ = 2.1~3.6 V (Motor voltage = 1.8 V)

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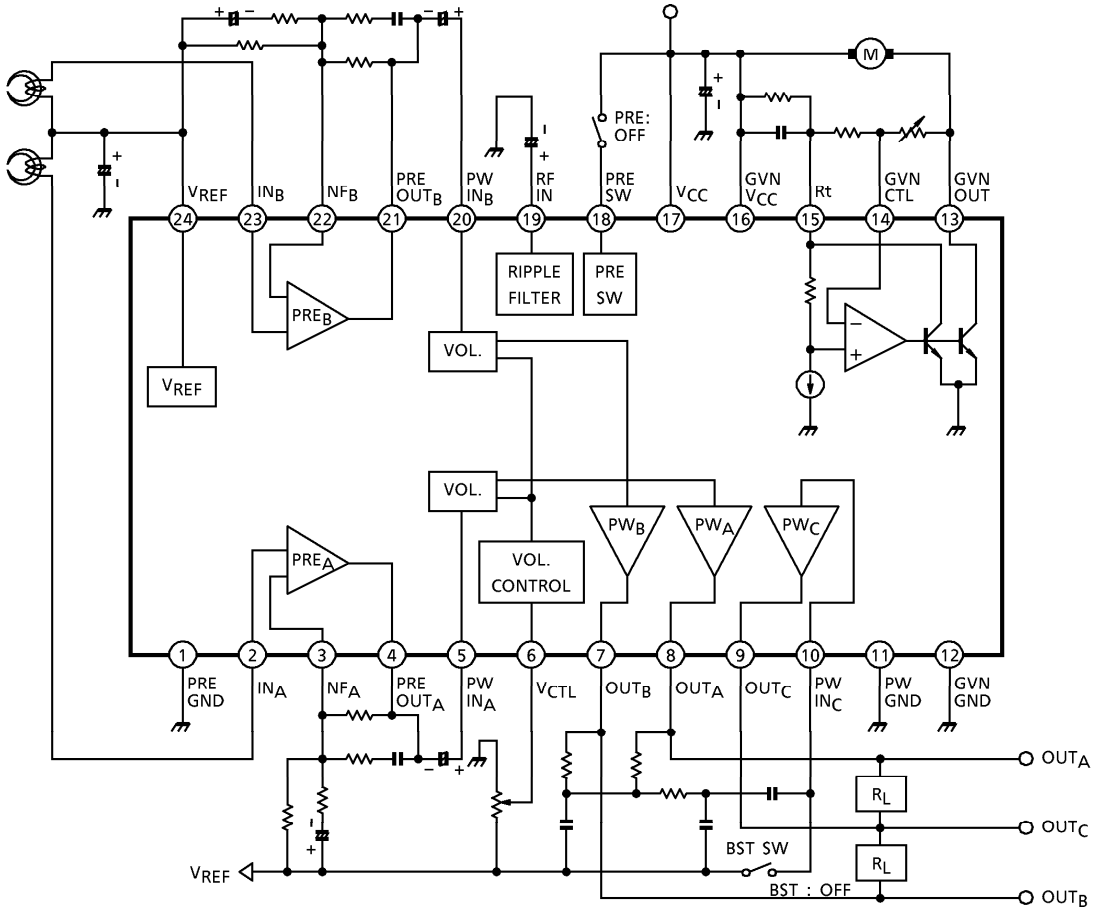
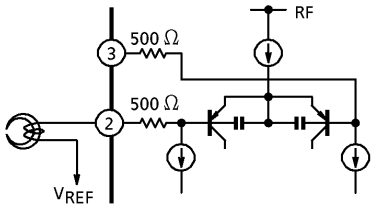
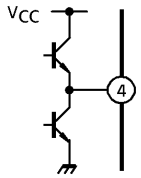
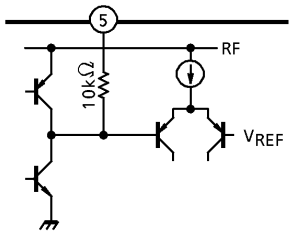
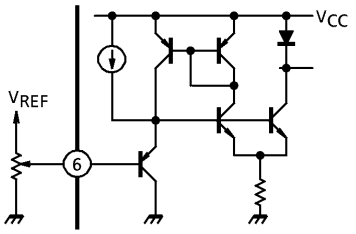
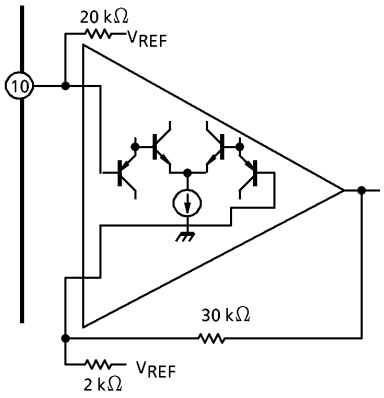
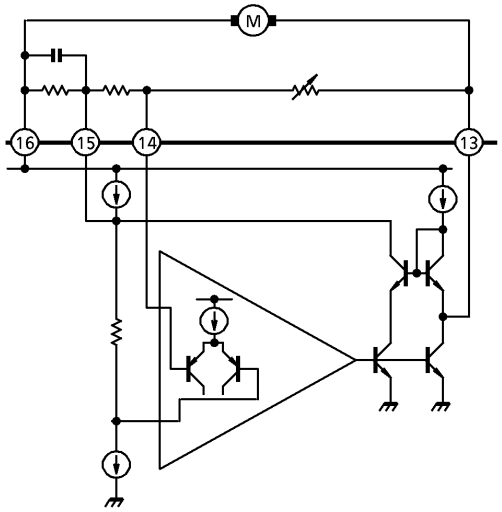
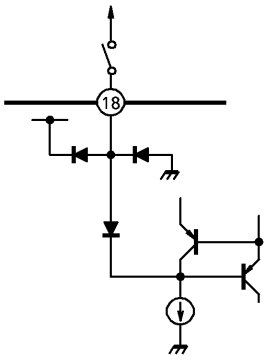
BLOCK DIAGRAM

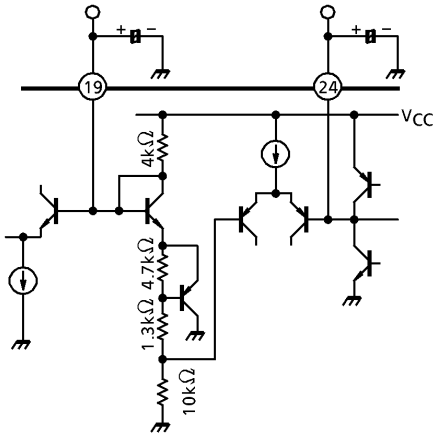


TERMINAL EXPLANATION

TERMINAL VOLTAGE : Typical terminal voltage at no signal with test circuit (V_{CC} = 3 V, Ta = 25°C)

TERMINAL No.	TERMINAL NAME	FUNCTION	INTERNAL CIRCUIT	TERMINAL VOLTAGE (V)
1	PRE GND	The GND, except for power drive stage and motor governer stage.	—	0
2	IN_A	Input of preamplifier		1.2
23	IN_B			
3	NF_A	NF of preamplifier		1.2
22	NF_B			
4	PRE OUT_A	Output of preamplifier		1.2
21	PRE OUT_B			
7	OUT_B	Output of power amplifier		
8	OUT_A			
9	OUT_C			
5	PW IN_A	Input of power amplifier		1.2
20	PW IN_B			
6	V_{CTL}	The terminal of DC volume control		—

TERMINAL No.	TERMINAL NAME	FUNCTION	INTERNAL CIRCUIT	TERMINAL VOLTAGE (V)
10	PW IN_C	Input of center amplifier		1.2
11	PW GND	GND for power drive stage	—	0
12	GVN GND	GND for motor governor stage	—	0
13	GVN OUT	Motor terminal		—
14	GVN CTL	The terminal of motor speed control		—
15	Rt	The terminal of amateur compensation resistor		—
16	GVN V_{CC}	V_{CC} for motor governor stage		3
17	V_{CC}	V_{CC} for preamplifier stage and power amplifier stage.	—	3
18	PRE SW	Muting switch of preamplifier (Preamp. on : "L" level / open Preamp. off : "H" level Refer to application note		—

TERMINAL No.	TERMINAL NAME	FUNCTION	INTERNAL CIRCUIT	TERMINAL VOLTAGE (V)
19	RF IN	Ripple filter of power supply		2.5
24	V_{REF}	Reference voltage ● Preamplifier and power amplifier operate on this reference.		1.2

APPLICATION NOTE

1. V_{CC} and GND

   This IC has two V_{CC} terminals and three GND terminals. Pattern layout should be designed carefully to reduce the common impedance.

   V_{CC}

V_{CC} (pin 17)	:	Preamplifier stage and power amplifier stage.
GVN V_{CC} (pin 16)	:	Motor governor stage.

   GND

PRE GND (pin 1)	:	Preamplifier stage, and power amplifier stage except for the power drive stage.
PW GND (pin 11)	:	Power drive stage of power amplifier.
GVN GND (pin 12)	:	Motor governor stage.

2. V_{REF}

   It is necessary to stabilize the V_{REF} circuit, because the internal circuit operate on this reference.

3. RF IN

   As this terminal is an input terminal of the ripple filter, it cannot supply a power supply to other ICs etc.

4. Preamplifier

   Input signal should be applied to V_{REF} standard, otherwise pop noise become bigger when V_{CC} is turned on and off.

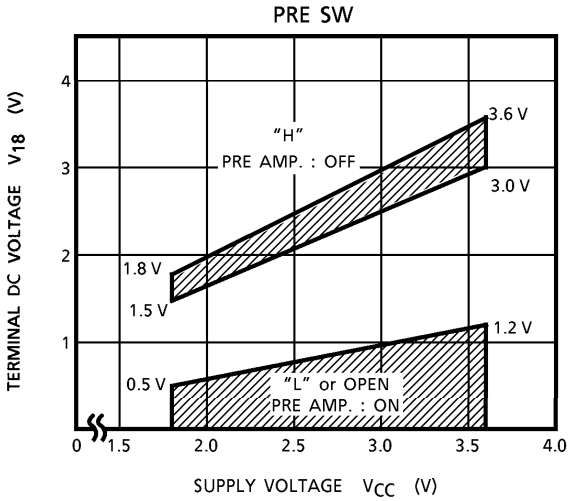
5. Power amplifier

   It is necessary to insert the coupling capacitor through the PW IN terminal. In case that DC current or DC voltage is applied to the PW IN terminal, the internal circuit has unbalance and the power amplifier doesn't operate normally.

6. Operating supply voltage range of motor governor stage

   As for the minimum of operating supply voltage range, the motor voltage is 1.8 V.
   In case that it is more than 1.8 V, the low voltage performance becomes bad.

7. PRE SW sensitivity (Ta = 25°C)



MAXIMUM RATINGS (Ta = 25°C)

CHARACTERISTIC	SYMBOL		RATING	UNIT
Supply Voltage	V_{CC}		4	V
Power Dissipation	P_D	Note 1	400	mW
		Note 2	925	
Output Current (PW AMP.)	$I_{O\ (PW)}$		200	mA
Output Current (GVN)	$I_{O\ (GVN)}$		700	mA
Operating Temperature	Topr		− 25~75	°C
Storage Temperature	T_{stg}		− 55~150	°C

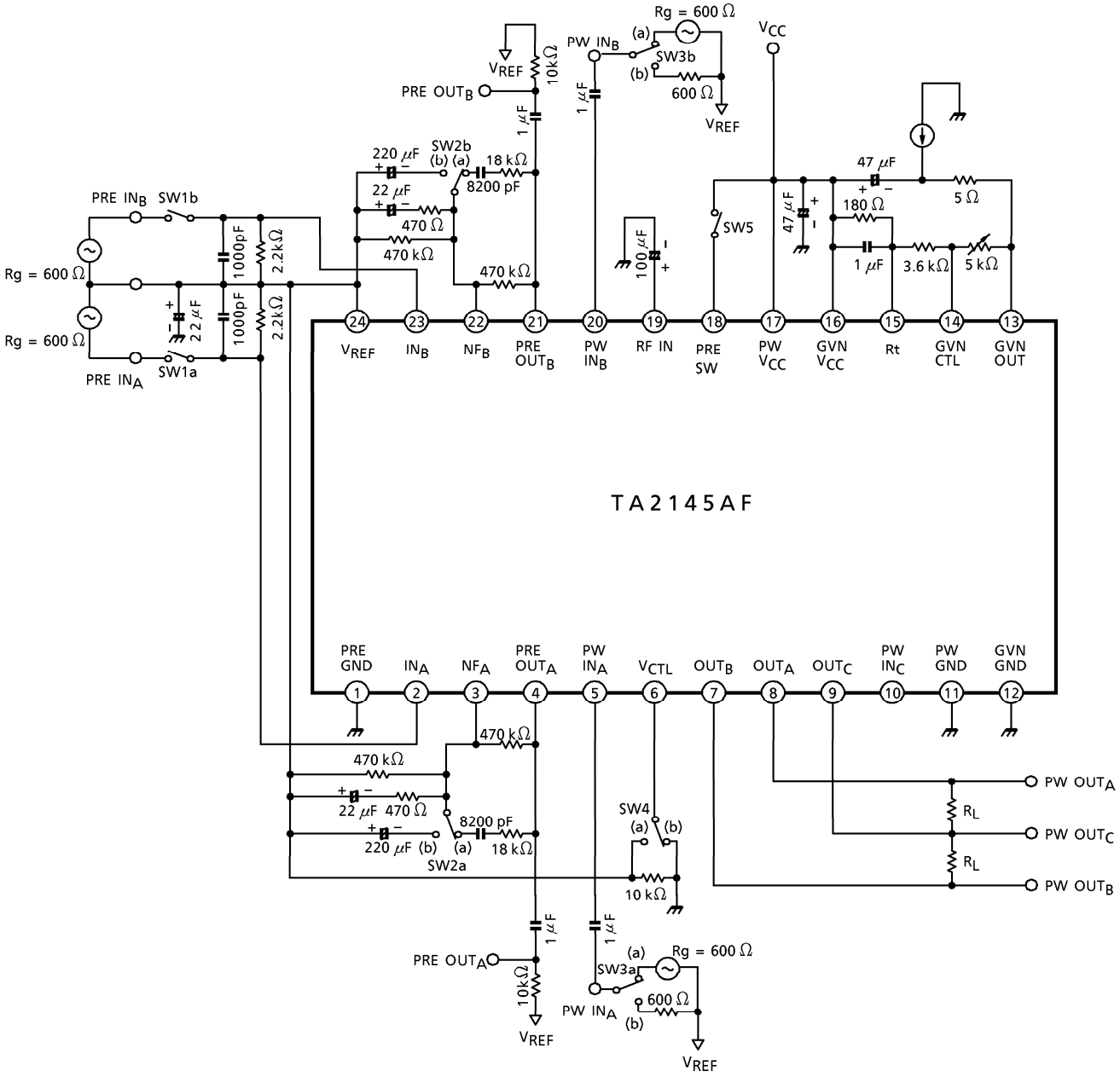
(Note 1) : IC only : Derated above Ta = 25°C in the proportion 3.2 mW / °C
(Note 2) : IC + PCB (TOSHIBA typical PCB) : Derated above Ta = 25°C in the proportion 7.4 mW / °C

ELECTRICAL CHARACTERISTICS

Unless otherwise specified, V_{CC} = 3 V, Ta = 25°C, f = 1 kHz, SW2 : a, SW5 : OPEN
Preamplifier : R_g = 2.2 kΩ, R_L = 10 kΩ, SW1 : ON, SW3 : b, SW4 : b
Power amplifier : R_g = 600 Ω, R_L = 16 Ω, Vol : MAX, SW1 : OPEN, SW3 : a, SW4 : a
Motor governor : I_m = 100 mA, SW1 : OPEN, SW3 : b, SW4 : b

	CHARACTERISTIC	SYMBOL	TEST CIR-CUIT	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
	Quiescent Supply Current 1	I_{CCQ1}	—	PRE OFF, V_{in} = 0, Vol : MIN, SW4 : b, SW5 : ON	—	7.5	13	mA
	Quiescent Supply Current 2	I_{CCQ2}	—	V_{in} = 0, Vol : MIN, SW4 : b	—	8.5	14.5	
PRE AMP.	Open Loop Voltage Gain	G_{VO}	—	V_o = −10 dBV, SW2 : b	—	86	—	dB
	Closed Loop Voltage Gain	G_{VC}	—	V_o = −10 dBV	—	35	—	dB
	Maximum Output Voltage	V_{om}	—	THD = 1%	550	720	—	mV_{rms}
	Total Harmonic Distortion	THD1	—	V_o = −10 dBV	—	0.02	0.3	%
	Equivalent Input Noise Voltage	V_{ni}	—	R_g = 2.2 kΩ, SW1 : OPEN BPF = 20 Hz~20 kHz, NAB (G_V = 35 dB, f = 1 kHz)	—	1.2	2.4	μV_{rms}
	Cross Talk	CT1	—	V_o = −10 dBV	—	70	—	dB
	Ripple Rejection Ratio	RR1	—	f_r = 100 Hz, V_r = −20 dBV	—	48	—	dB
	Preamplifier Muting Attenuation	ATT1	—	V_o = −10 dBV SW5 : OPEN → ON	—	80	—	dB
	Preamplifier On Voltage	$V_{18\ (OFF)}$	—	V_{CC} = 1.8 V	0	—	0.5	V
	Preamplifier Off Voltage	$V_{18\ (OFF)}$	—		1.5	—	1.8	V
POWER AMP.	Voltage Gain	G_V	—	V_o = −10 dBV	29	31	33	dB
	Channel Balance	CB	—		−1.5	0	+1.5	dB
	Output Power 1	P_{o1}	—	R_L = 16 Ω, THD = 10%	17	28	—	mW
	Output Power 2	P_{o2}	—	R_L = 32 Ω, THD = 10%	—	20	—	mW
	Total Harmonic Distortion	THD2	—	P_o = 1 mW	—	0.5	—	%
	Output Noise Voltage	V_{no}	—	R_g = 600 Ω, SW3 : b BPF = 20 Hz~20 kHz	—	270	400	μV_{rms}
	Ripple Rejection Ratio	RR2	—	f_r = 100 Hz, V_r = −20 dBV	—	52	—	dB
	Cross Talk	CT2	—	V_o = −10 dBV	—	32	—	dB
	DC Volume Maximum Attenuation	ATT2	—	V_o = −10 dBV SW4 : a → b (Vol : MAX → MIN)	—	82	—	dB
MOTOR GOVERNOR	Supply Current	I_{CC}	—	I_m = 0	—	2.5	3.5	mA
	Saturation Voltage	$V_{CE\ (sat)}$	—	I_m = 200 mA	—	—	0.5	V
	Reference Voltage	V_{REF}	—	I_m = 100 mA	0.76	0.81	0.86	V
	Reference Voltage Fluctuation 1	ΔV_{REF1}	—	V_{CC} = 2.1~3.6 V	—	0.25	—	% / V
	Reference Voltage Fluctuation 2	ΔV_{REF2}	—	I_m = 25~250 mA	—	0.003	—	% / mA
	Reference Voltage Fluctuation 3	ΔV_{REF3}	—	Ta = −25~75°C	—	0.005	—	% / °C
	Current Ratio	K	—		34.5	37.5	40.5	
	Current Ratio Fluctuation 1	ΔK1	—	V_{CC} = 2.1~3.6 V	—	0.25	—	% / V
	Current Ratio Fluctuation 2	ΔK2	—	I_m = 25~250 mA	—	0.08	—	% / mA
	Current Ratio Fluctuation 3	ΔK3	—	Ta = −25~75°C	—	0.005	—	% / °C

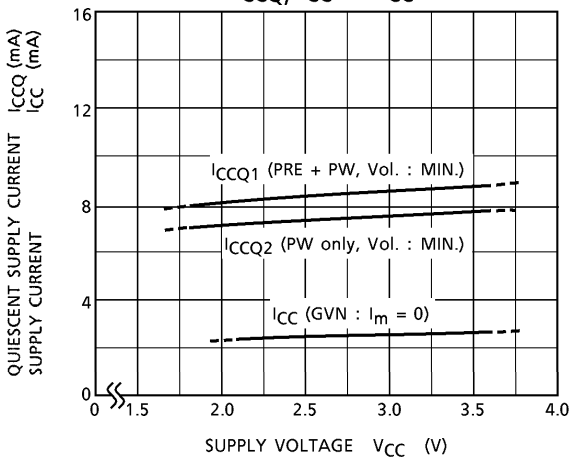
TEST CIRCUIT



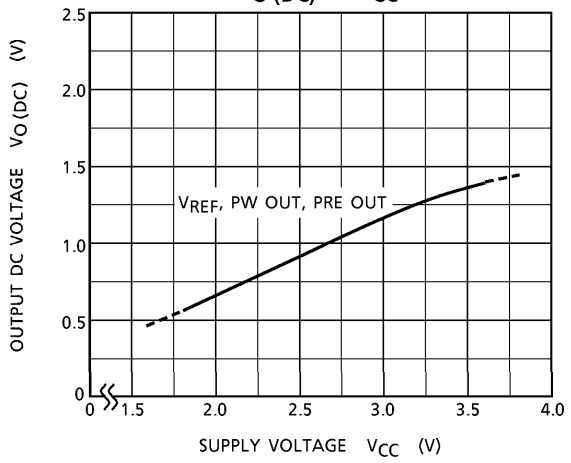
CHARACTERISTIC CURVES

Unless otherwise specified V_{CC} = 3 V, Ta = 25°C, f = 1 kHz
Preamplifier : Rg = 2.2 kΩ, R_L = 10 kΩ
Power amplifier : Rg = 600 Ω, R_L = 16 Ω, Vol = MAX.
Motor governor : I_m = 100 mA

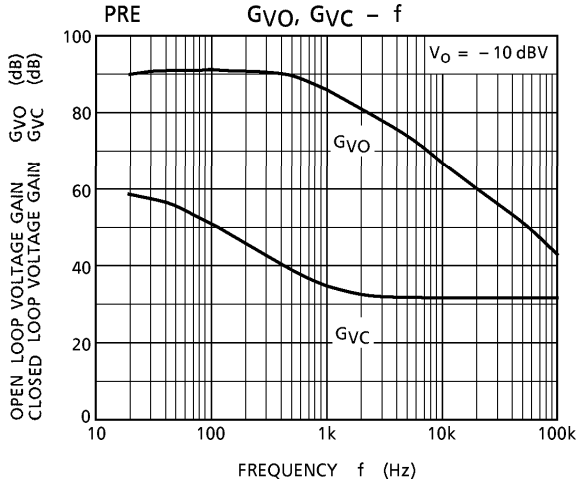
I_{CCQ}, I_{CC} – V_{CC}



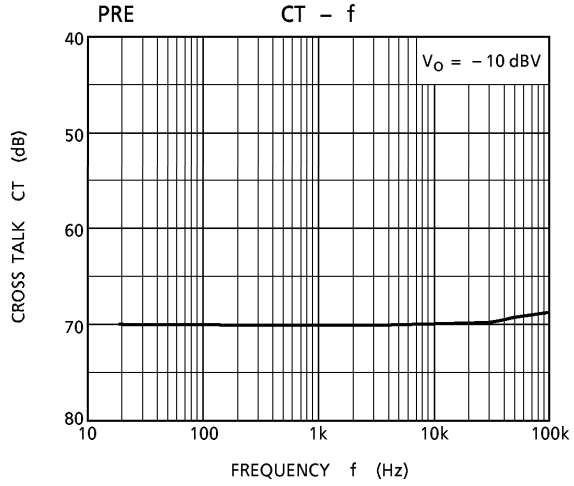
$V_{O\,(DC)}$ – V_{CC}



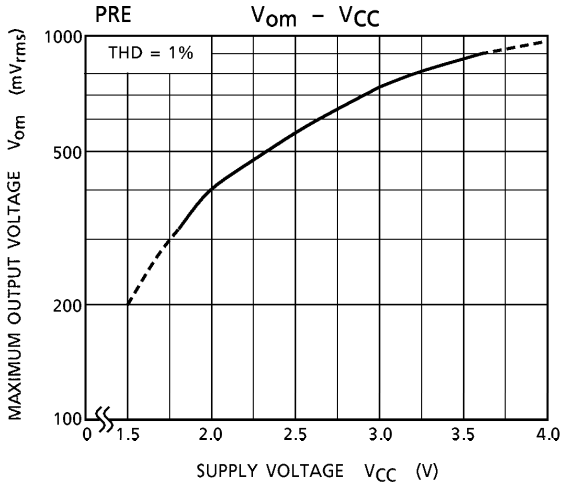
PRE G_{VO}, G_{VC} – f



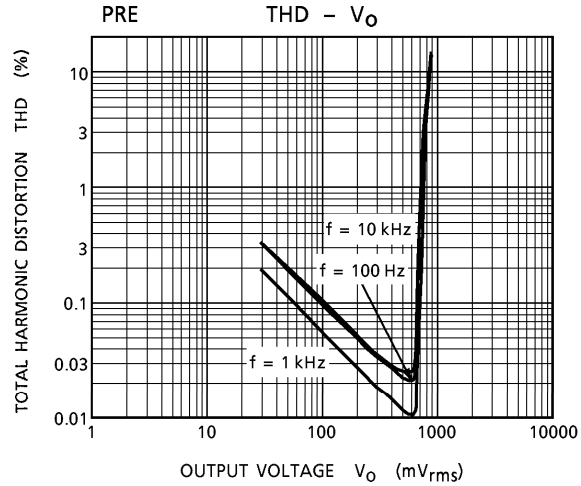
PRE CT – f



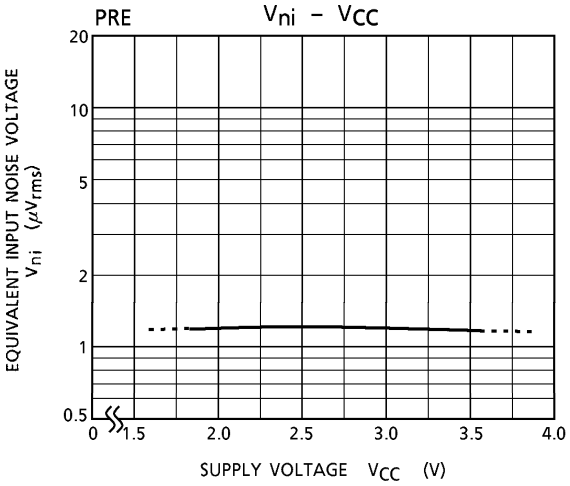
PRE V_{om} – V_{CC}



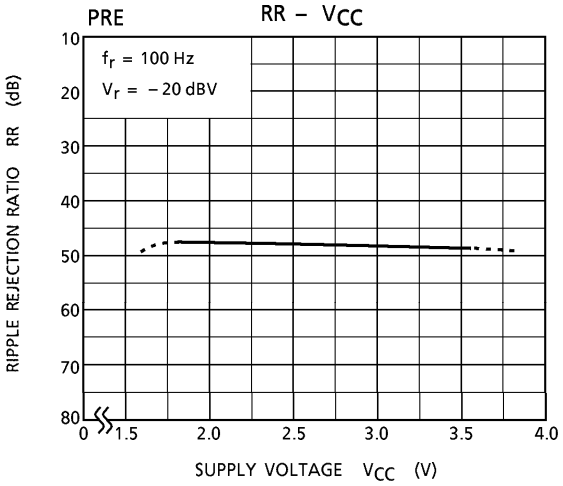
PRE THD – V_O



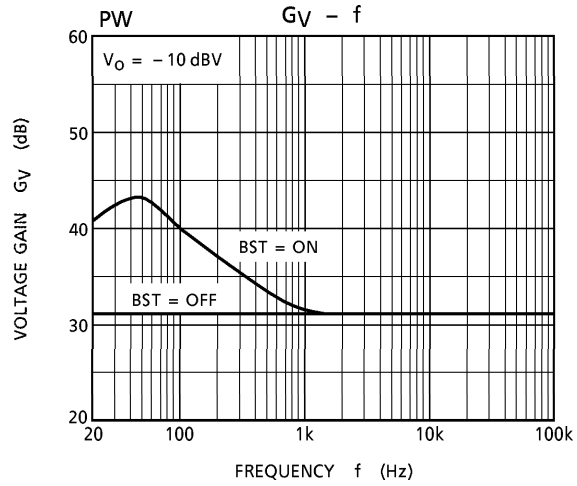
PRE V_{ni} – V_{CC}



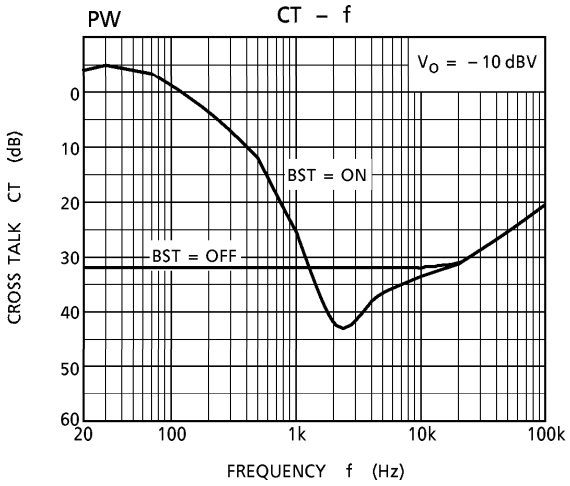
PRE RR – V_{CC}



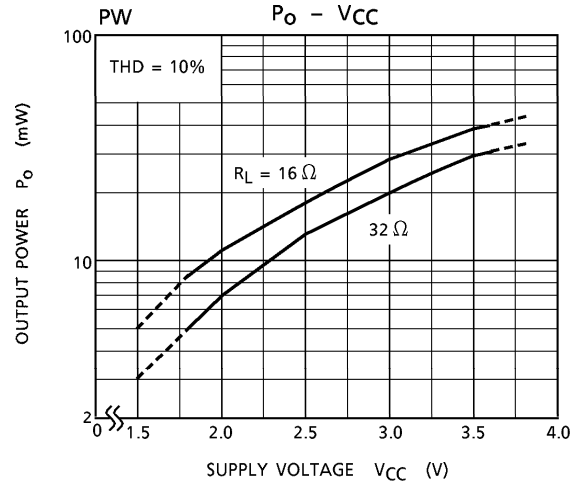
PW G_V – f



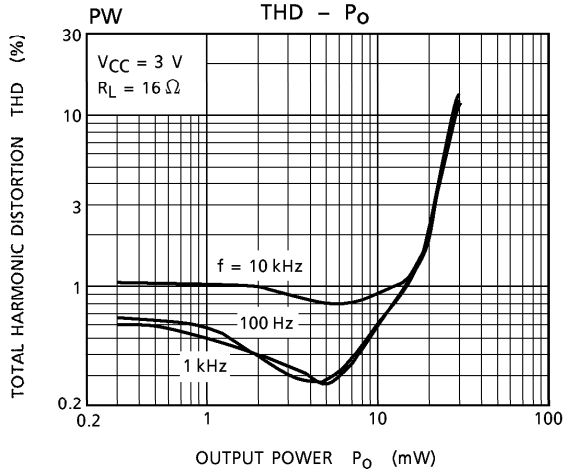
PW CT – f



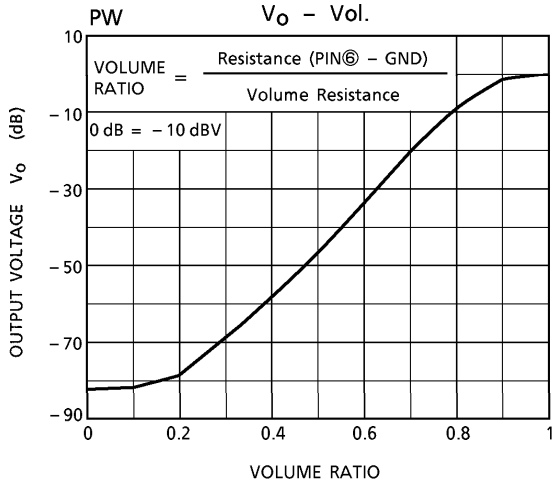
PW P_O – V_{CC}



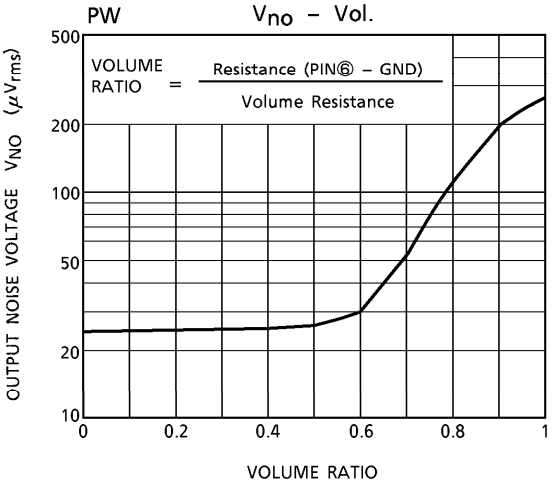
PW THD – P_O



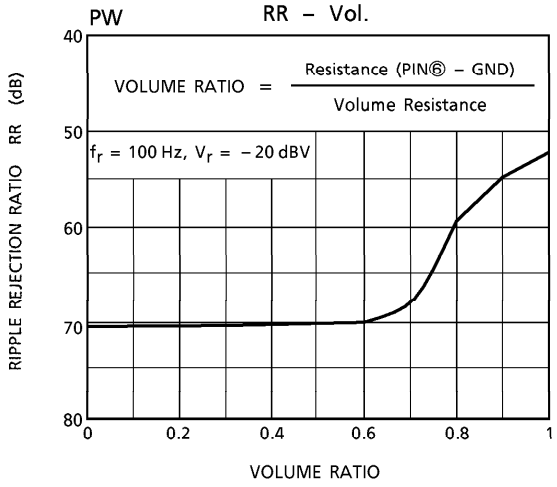
PW V_o – Vol.



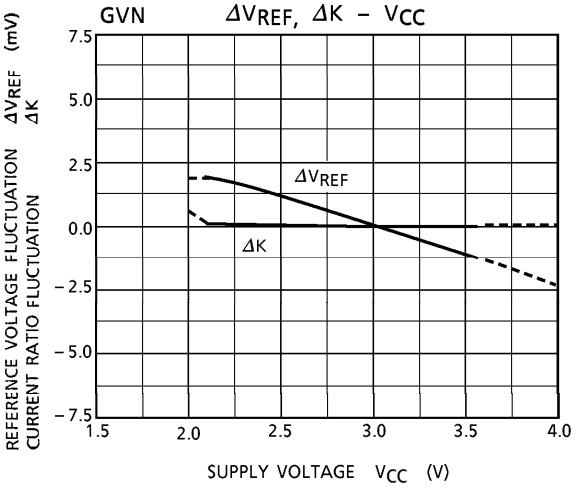
PW V_{no} – Vol.



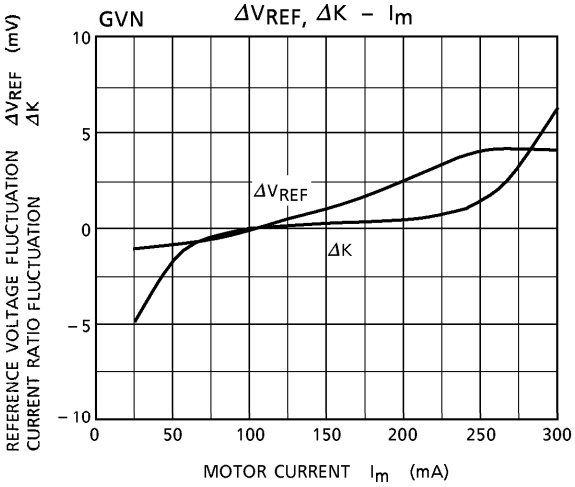
PW RR – Vol.



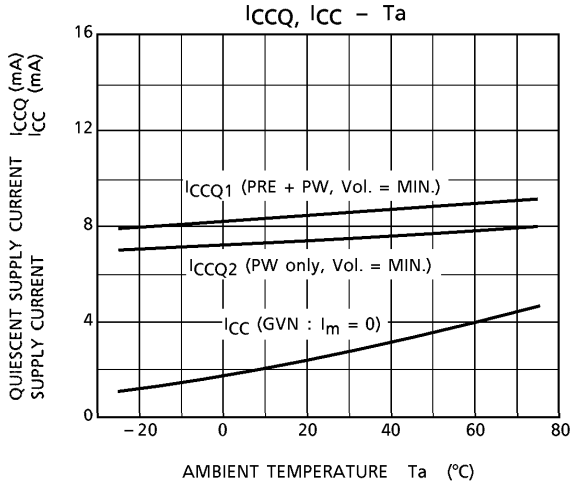
GVN ΔV_{REF}, ΔK – V_{CC}



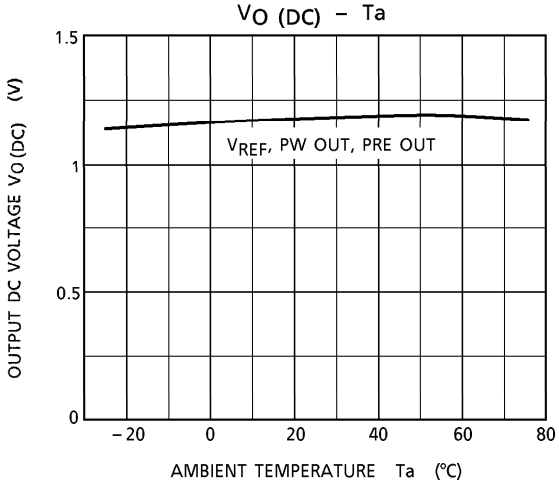
GVN ΔV_{REF}, ΔK – I_m



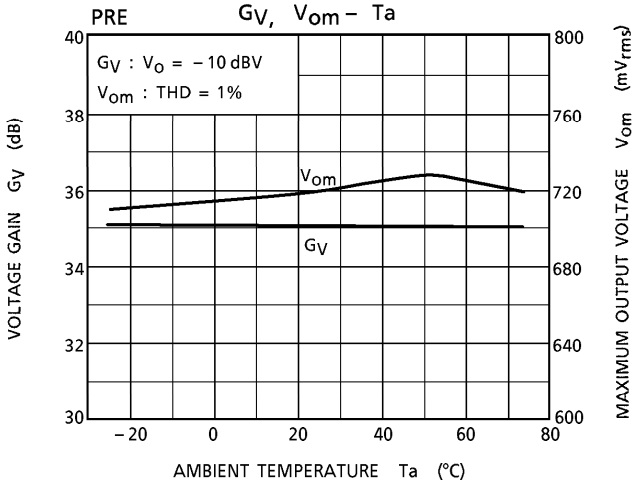
I_{CCQ}, I_{CC} – Ta



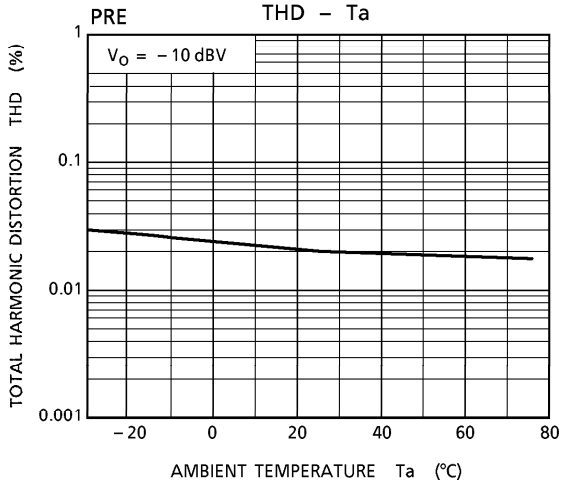
V_O (DC) – Ta



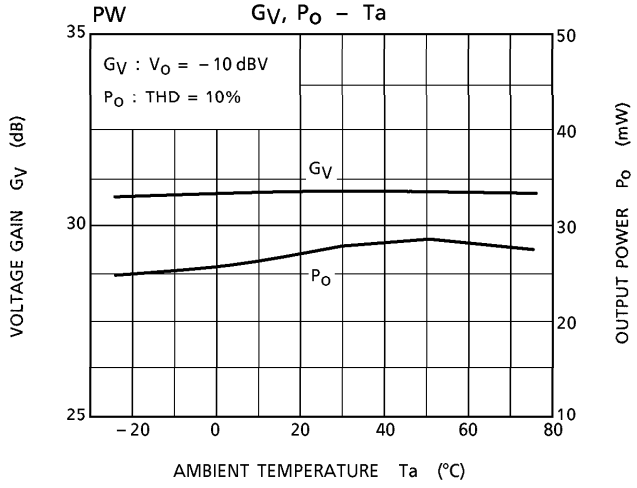
PRE G_V, V_{om} – Ta



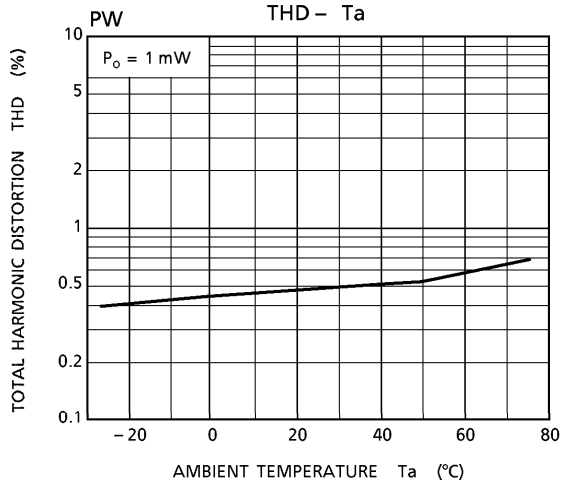
PRE THD – Ta



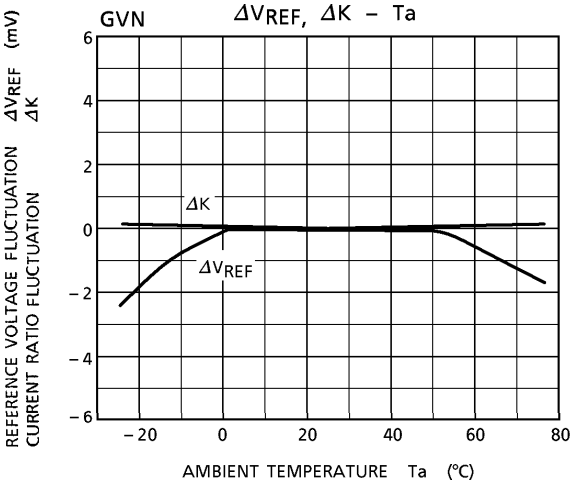
PW G_V, P_O – Ta



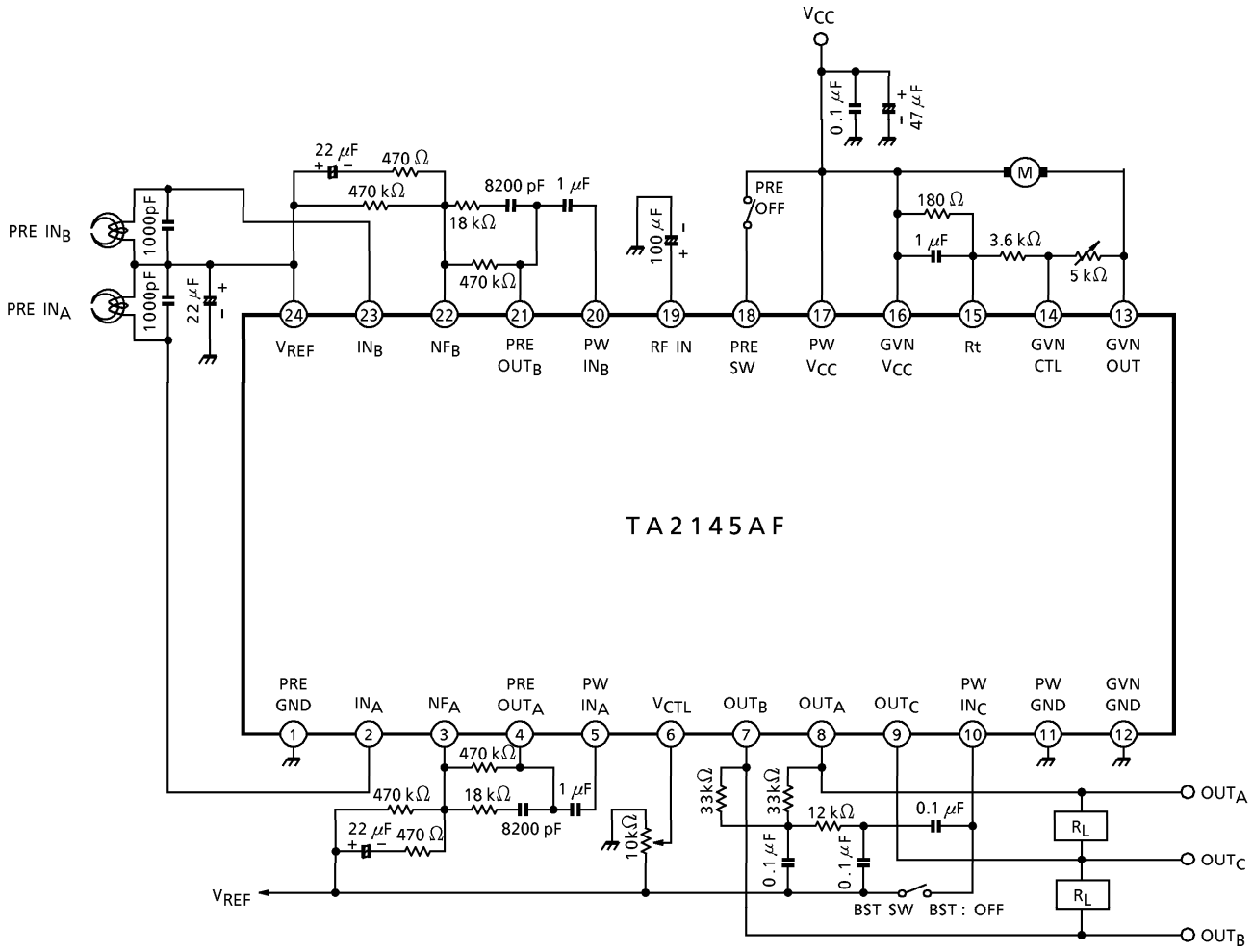
PW THD – Ta



GVN ΔV_{REF}, ΔK – Ta

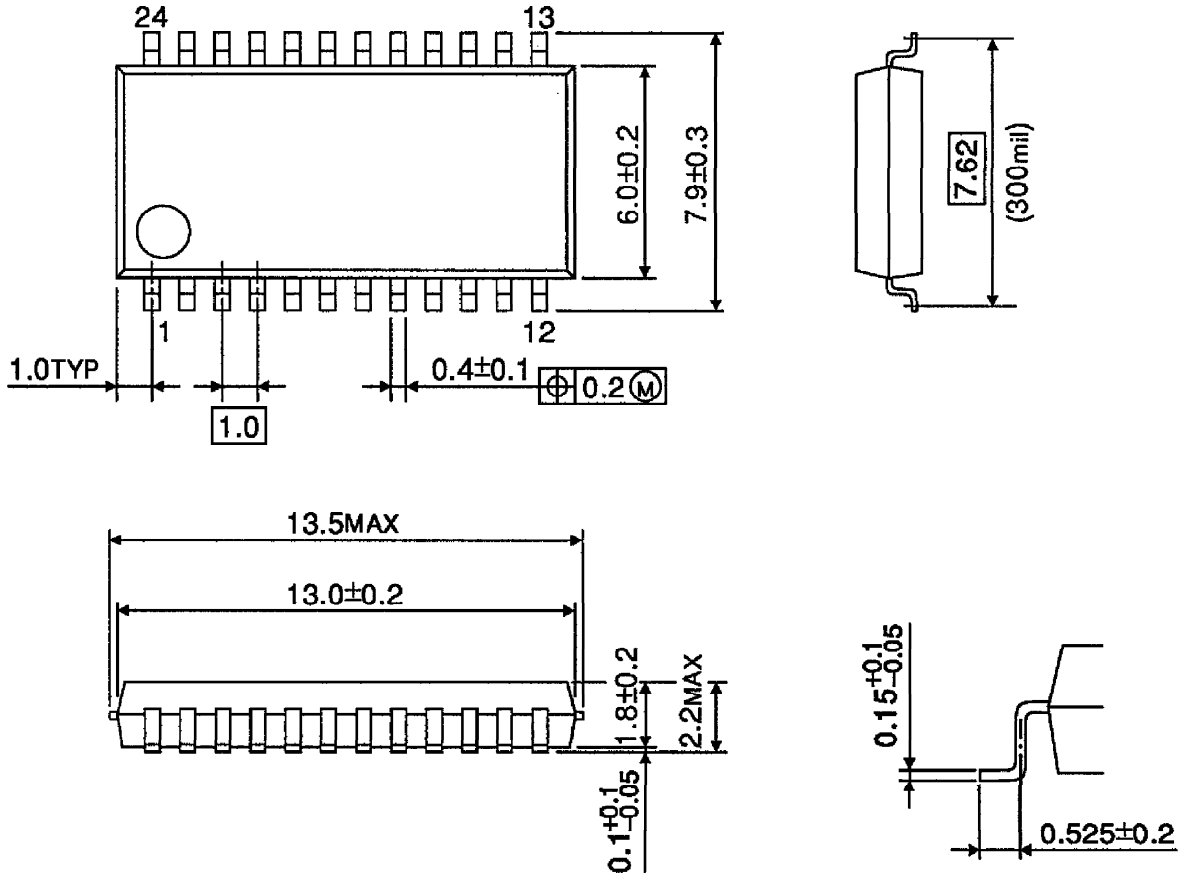


APPLICATION CIRCUIT



PACKAGE DIMENSIONS

SSOP24-P-300-1.00 Unit : mm



Weight : 0.32 g (Typ.)