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Renesas Technology Corp. Customer Support Dept. April 1, 2003



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Silicon N-Channel MOS FET



ADE-208-1290 (Z) 1st. Edition Mar. 2001

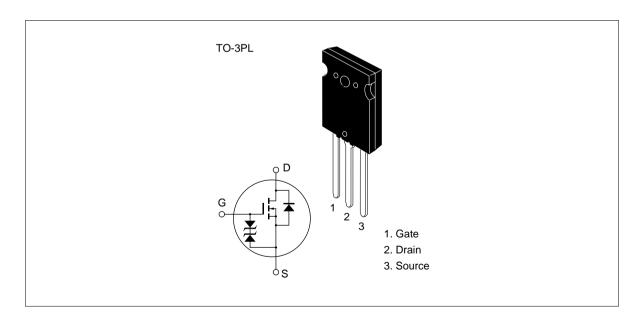
Application

High speed power switching

Features

- Low on-resistance
- High speed switching
- Low drive current
- No secondary breakdown
- Suitable for switching regulator and DC-DC converter

Outline



Absolute Maximum Ratings $(Ta = 25^{\circ}C)$

Item		Symbol	Ratings	Unit
Drain to source voltage	2SK1526	V _{DSS}	450	V
	2SK1527		500	
Gate to source voltage		V _{GSS}	±30	V
Drain current		I _D	40	Α
Drain peak current		l _{D(pulse)} *1	160	Α
Body to drain diode reverse drain current		I _{DR}	40	А
Channel dissipation		Pch*2	250	W
Channel temperature		Tch	150	°C
Storage temperature		Tstg	-55 to +150	°C

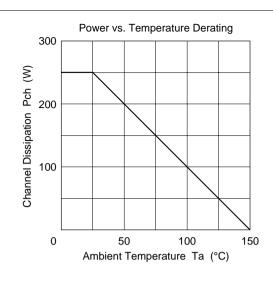
Notes: 1. PW \leq 10 μ s, duty cycle \leq 1%

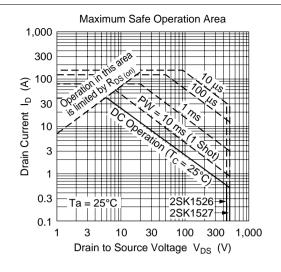
2. Value at $T_c = 25^{\circ}C$

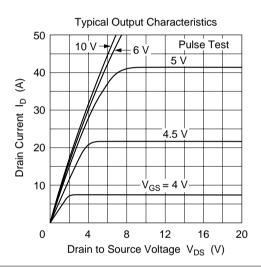
Electrical Characteristics (Ta = 25°C)

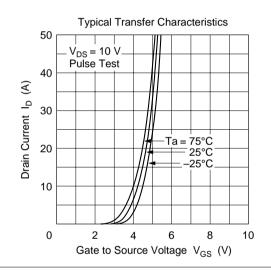
Symbol	Min	Тур	Max	Unit	Test conditions
V _{(BR)DSS}	450	_	_	V	$I_D = 10 \text{ mA}, V_{GS} = 0$
7	500				
$V_{(BR)GSS}$	±30	_	_	V	$I_{G} = \pm 100 \ \mu A, \ V_{DS} = 0$
I _{GSS}	_	_	±10	μΑ	$V_{GS} = \pm 25 \text{ V}, V_{DS} = 0$
6 I _{DSS}	_	_	250	μΑ	$V_{DS} = 360 \text{ V}, V_{GS} = 0$
					$V_{DS} = 400 \text{ V}, V_{GS} = 0$
$V_{GS(off)}$	2.0	_	3.0	V	$I_{D} = 1 \text{ mA}, V_{DS} = 10 \text{ V}$
R _{DS(on)}	_	0.11	0.15	Ω	$I_D = 20 \text{ A}, V_{GS} = 10 \text{ V}^{*1}$
	_	0.12	0.16		
yfs	20	30	_	S	$I_D = 20 \text{ A}, V_{DS} = 10 \text{ V}^{*1}$
Ciss	_	5800	_	pF	$V_{DS} = 10 \text{ V}, V_{GS} = 0,$
Coss	_	1430	_	pF	f = 1 MHz
Crss	_	150	_	pF	
t _{d(on)}	_	60	_	ns	$I_D = 20 \text{ A}, V_{GS} = 10 \text{ V},$
t _r	_	175	_	ns	$R_L = 1.5 \Omega$
t _{d(off)}	_	420	_	ns	
t _f	_	160	_	ns	
V_{DF}	_	1.2	_	V	$I_F = 40 \text{ A}, V_{GS} = 0$
t _{rr}		600		ns	$I_F = 40 \text{ A}, V_{GS} = 0,$ $di_F/dt = 100 \text{ A}/\mu\text{s}$
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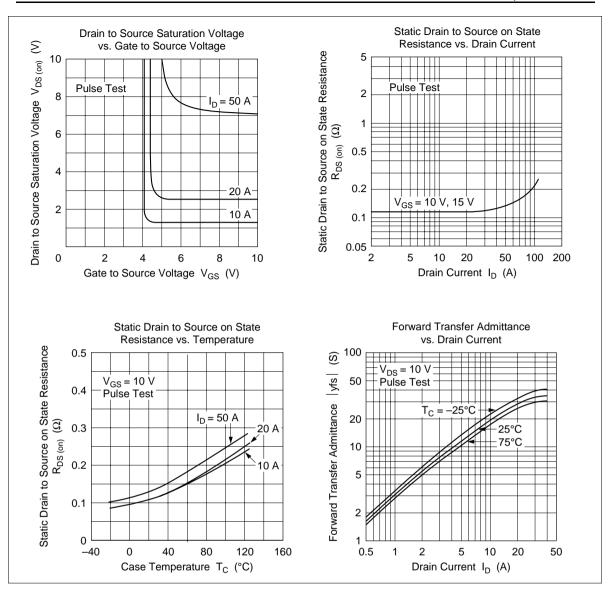
Note: 1. Pulse test

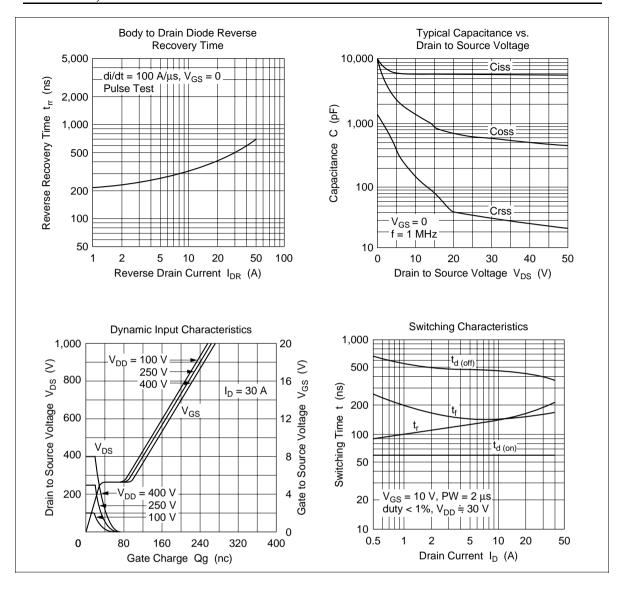


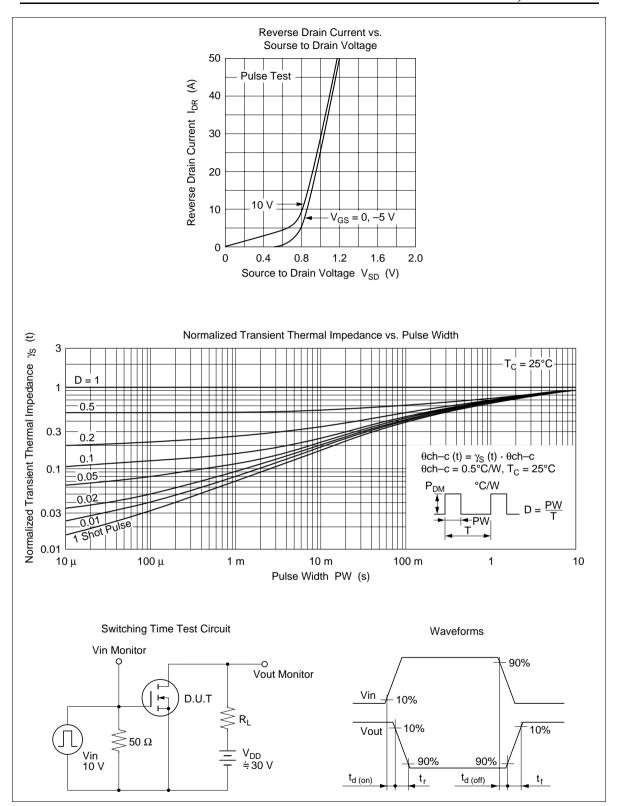






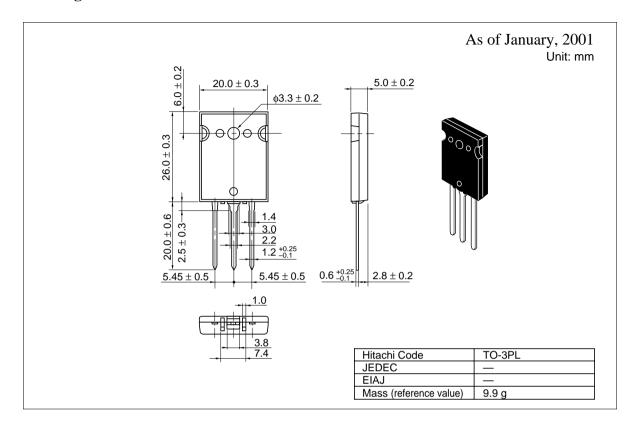






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Package Dimensions



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