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Silicon N Channel Power MOS FET High Speed Power Switching

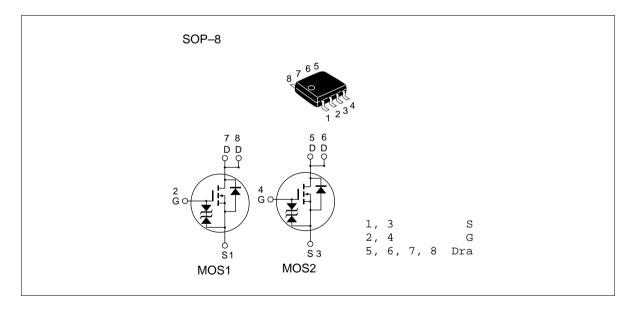


ADE-208-494C (Z) 4th. Edition Aug. 1997

#### Features

- Low on-resistance
- Capable of 4 V gate drive
- Low drive current
- High density mounting

### Outline



## **Absolute Maximum Ratings** (Ta = 25°C)

Item	Symbol	Ratings	Unit	
Drain to source voltage	V <sub>DSS</sub>	30	V	
Gate to source voltage	V <sub>GSS</sub>	±20	V	
Drain current	I <sub>D</sub>	5.5	А	
Drain peak current	I *1 D(pulse)	44	А	
Body to drain diode reverse drain current	I <sub>DR</sub>	5.5	А	
Channel dissipation	Pch*2	2	W	
Channel dissipation	Pch *3	3	W	
Channel temperature	Tch	150	°C	
Storage temperature	Tstg	-55 to +150	°C	

Notes: 1.  $PW \le 10\mu s$ , duty cycle  $\le 1 \%$ 

2. 1 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10s

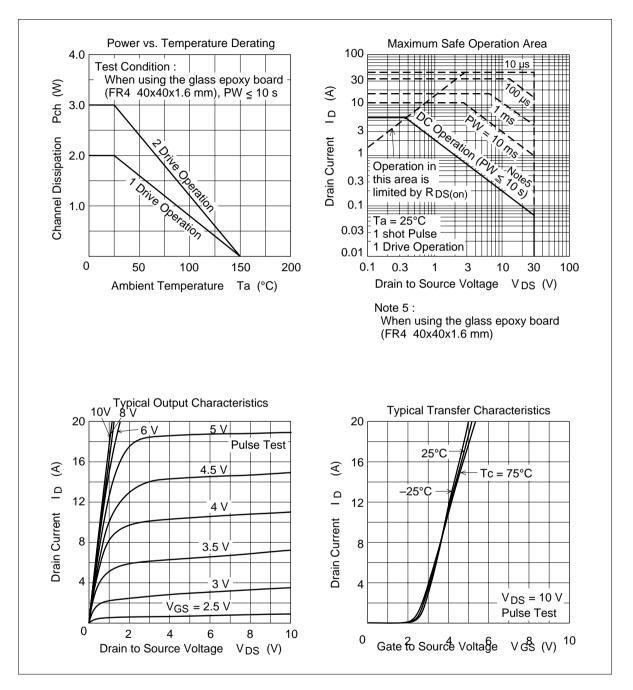
3. 2 Drive operation : When using the glass epoxy board (FR4 40 x 40 x 1.6 mm), PW  $\leq$  10s

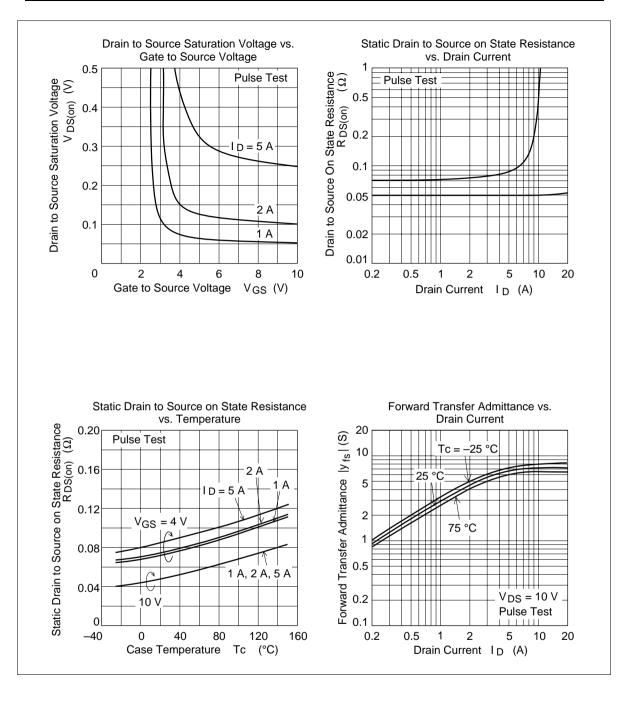
## **Electrical Characteristics** (Ta = $25^{\circ}$ C)

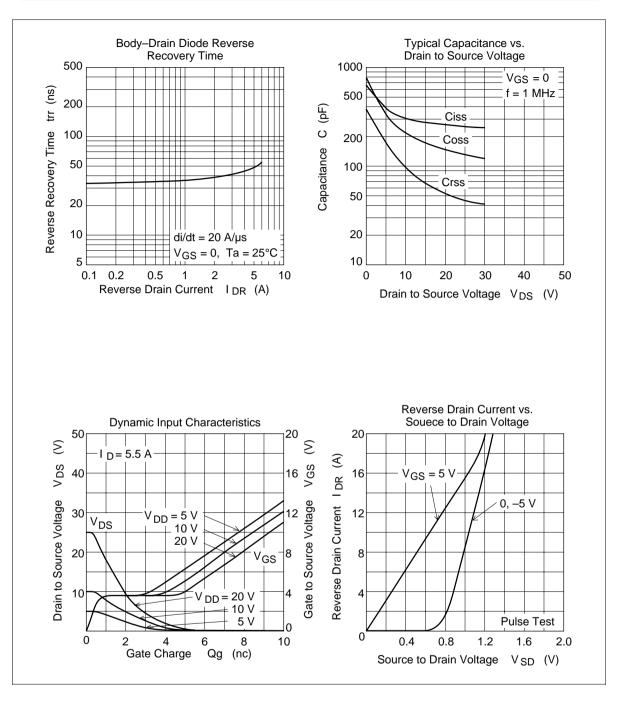
Item	Symbol	Min	Тур	Max	Unit	Test Conditions
Drain to source breakdown voltage	$V_{(BR)DSS}$	30	—		V	$I_{\rm D} = 10 {\rm mA}, V_{\rm GS} = 0$
Gate to source breakdown voltage	$V_{(BR)GSS}$	±20	_		V	$I_{G} = \pm 100 \mu A, V_{DS} = 0$
Gate to source leak current	I <sub>GSS</sub>		_	±10	μA	$V_{GS} = \pm 16V, V_{DS} = 0$
Zero gate voltege drain current	I <sub>DSS</sub>	—	—	10	μA	$V_{DS} = 30 \text{ V}, \text{ V}_{GS} = 0$
Gate to source cutoff voltage	$V_{\text{GS(off)}}$	1.0	_	2.0	V	$V_{DS} = 10V, I_{D} = 1mA$
Static drain to source on state	$R_{DS(on)}$		0.05	0.065	Ω	$I_{\rm D} = 3A, V_{\rm GS} = 10V^{*1}$
resistance	R <sub>DS(on)</sub>	_	0.078	0.11	Ω	$I_{\rm D} = 3A, V_{\rm GS} = 4V^{*1}$
Forward transfer admittance	y <sub>fs</sub>	3.5	5.5		S	$I_{\rm D} = 3A, V_{\rm DS} = 10V^{*1}$
Input capacitance	Ciss	_	310		pF	$V_{DS} = 10V$
Output capacitance	Coss		220		pF	$V_{GS} = 0$
Reverse transfer capacitance	Crss		100		pF	f = 1MHz
Turn-on delay time	$t_{d(on)}$		17		ns	$V_{GS} = 4V, I_{D} = 3A$
Rise time	t,		190		ns	$V_{\text{DD}} \cong 10V$
Turn-off delay time	t <sub>d(off)</sub>		25	_	ns	
Fall time	t <sub>f</sub>	_	60	_	ns	
Body to drain diode forward voltage	$V_{DF}$	—	0.9	1.4	V	$IF = 5.5A, V_{GS} = 0^{*1}$
Body to drain diode reverse recovery time	t <sub>rr</sub>	—	50	—	ns	IF = 5.5A, V <sub>GS</sub> = 0 diF/ dt =20A/μs

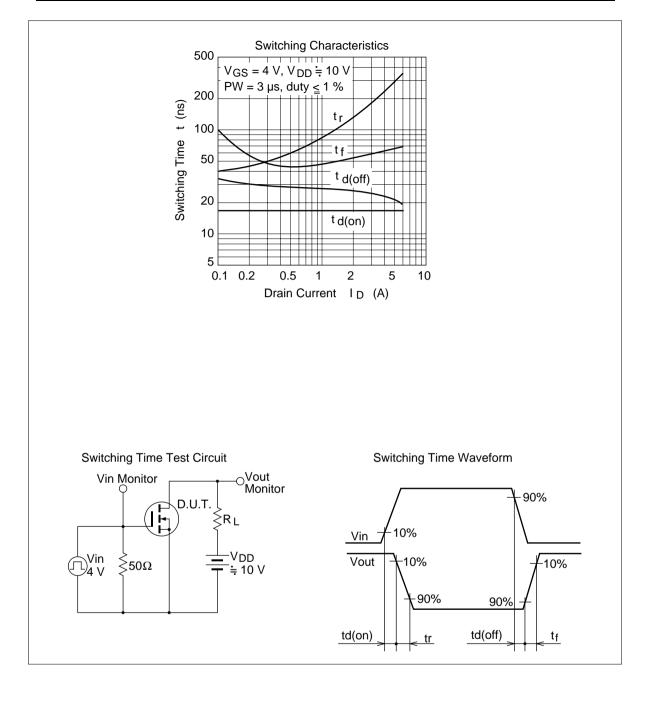
Note: 1. Pulse test

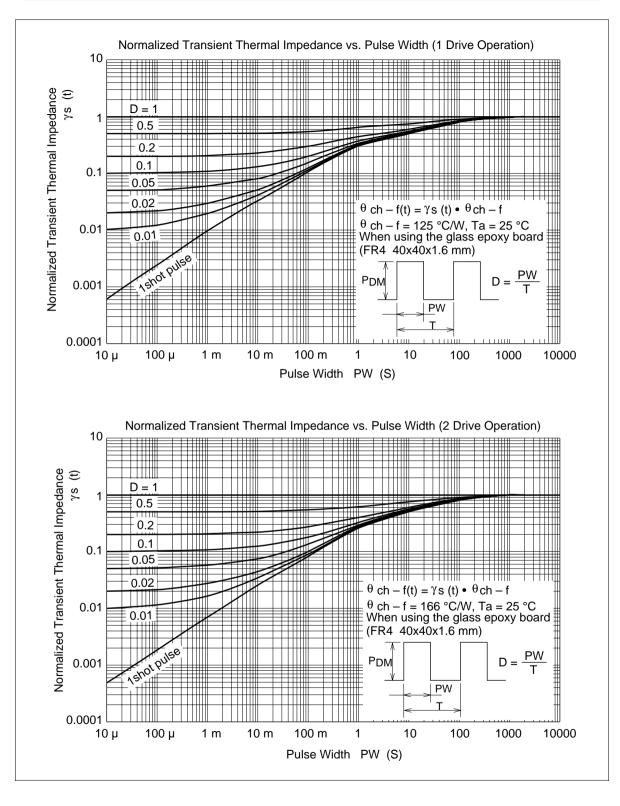
## **Main Characteristics**



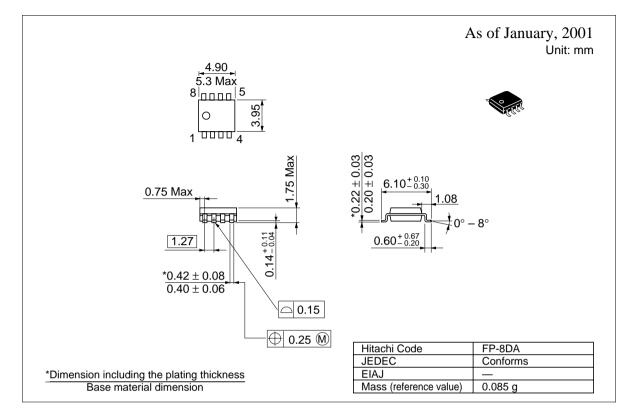








### **Package Dimensions**



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