

# FLC01-200x

Application Specific Discretes  $A.S.D.^{TM}$ 

# FIRE LIGHTER CIRCUIT

# FEATURES

- Dedicated thyristor structure for capacitance discharge ignition operation
- High pulse current capability 190A @ tp = 10µs
- Fast turn-on operation
- Designed for high ambient temperature (up to 120°C)

#### **BENEFITS**

- Space saving thanks to monolithic function integration
- High reliability with planar technology

## DESCRIPTION

The FLC01 series has been especially developed for capacitance discharge operation. The main applications are gas lighters or ignitors such as cookers / gas boilers / gas hobs...

Based on ST's ASD<sup>TM</sup> technology, it provides a fully integrated function, with high performance and reliability levels, adapted to severe and hot temperature environment.

Th: Thyristor for switching operation.

**Z**: Zener diode to set the threshold voltage.

**D:** Diode for reverse conduction.

**R**: 2 k $\Omega$  resistor.



# FUNCTIONAL DIAGRAM



# FLC01-200x

# ABSOLUTE RATINGS (limiting values)

Symbol	Parameter	Value	Unit	
I <sub>TRM</sub>	$ \begin{array}{l} \mbox{Repetitive surge peak on state current for thyristor} \\ -30^\circ C \leq T_{amb} \leq 120^\circ C \end{array} \  \  \begin{array}{l} \mbox{tp} = 10 \mu s \\ (\ note \ 1) \end{array} $		190	A
I <sub>FRM</sub>	Repetitive surge peak on state current for diode $-30^{\circ}C \le T_{amb} \le 120^{\circ}C$			
dl/dt	Critical rate of rise time on state current $\mbox{-}30^\circ C \le T_{amb}$	120	A/µs	
Tstg Tj	Storage junction temperature range Maximum junction temperature	- 40 to + 150 + 125	°C	
Toper	Operating temperature range	-30 + 120	°C	
TL	Maximum lead temperature for soldering during 10s	260	°C	

Note 1 : Test current waveform



# THERMAL RESISTANCE

Symbol	Parameter	Value	Unit
Rth(j-a)	Thermal resistance junction to ambient	100	°C/W

# **ORDERING INFORMATION**



57

2/8

# **ELECTRICAL CHARACTERISTICS**

Symbol	Parameters		
V <sub>RM</sub>	Stand-off voltage		
V <sub>BO</sub>	Breakover voltage		
V <sub>T</sub> On-state voltage			
VF	Diode forward voltage drop		
I <sub>BO</sub>	Breakover current		
I <sub>RM</sub> Leakage current			
αΤ	Temperature coefficient for $V_{BO}$		



# DIODE (D) PARAMETER

Symbol	Test Conditions				Value	Unit
VF	$I_F = 2A$	tp ≤ 500µs	Tj = 25°C	Max.	1.7	V

# THYRISTOR (Th) and ZENER (Z) PARAMETERS

Symbol	Test conditions			Тур.	Max.	Unit
I <sub>RM</sub>	V <sub>RM</sub> = 200 V	Tj = 25°C			1	μA
		Tj = 125°C			10	μΑ
V <sub>BO</sub>	at I <sub>BO</sub>	Tj = 25°C	206	220	233	V
I <sub>BO</sub>	at V <sub>BO</sub>	Tj = 25°C			0.5	mA
VT	I <sub>T</sub> = 2A tp ≤ 500μs	Tj = 25°C			1.7	V
αΤ				0.27		V/°C

**Fig. 1:** Relative variation of breakover current versus junction temperature.



# FLC01-200x

## Fig. 2: BASIC APPLICATION



The applications of the lighter using the capacitance discharge topology operate in 2 phases :

#### PHASE 1

The energy coming from the mains is stored into the capacitor C. For that, the AC voltage is rectified by the diode Ds.

# PHASE 2

At the end of the phase 1, the voltage across the capacitor C reaches the avalanche threshold of the zener. Then a current flows through the gate of the thyristor Th which fires.

The firing of the thyristor causes an alternating current to flow through the capacitor C. The positive parts of this current flow through C, Th and the primary of the HV transformer.

The negative parts of the current flow through C, D and the primaty of the HV transformer.

<u>/</u>//

# **RS RESISTOR CALCULATION**

The Rs resistor allows, in addition with the capacitor C, the spark frequency to be adjusted and the current from the mains to be limited. Its value shall allow the thyristor Th to fire even in the worst case. In this case the system must fire with the lower RMS mains voltage value while the breakdown voltage and current of the FLC are at the maximum.

The maximum Rs value is equal to :

$$Rs \max = \frac{(V_{AC} \min \sqrt{2}) - [V_{BO} \max (1 + \alpha T (T_{amb} - 25))]}{k I_{BO}}$$

\* : see fig 1

4/8

Fig. 3: Spark frequency versus Rs and C



The couple Rs/C can be chosen with the previous curve. Keep in mind the Rs maximum limit for which the system would not work when the AC

mains is minimum. The next curve shows the behavior with Rs=15k $\Omega$  and C=1 $\mu F.$ 



Fig. 4: Voltage across the capacitance with Rs =  $15k\Omega$ , C =  $1\mu$ F and V<sub>BO</sub> = 225V.

57

# PEAK CURRENT LIMIT

This component is designed to withstand  $I_{TRM}$  = 190A for a pulse duration of 10  $\mu s$  for an



10.0

**tp (μs)** 

ambient temperature of  $120^{\circ}$ C in repetitive surge. The curve of peak current versus the pulse duration allows us to verify if the application is

within the FLC operating limit.

12.5

15.0

Fig. 5: Peak current limit

**POWER LOSSES** (For  $10\mu s$ , see note 1)

50 ⊾ 5.0

To evaluate the power losses, please use the following equations :

7.5

For the thyristor : P = 1.18 x  $I_{T(AV)}$  + 0.035  $I^2_{T(RMS)}$ 

For the diode : P = 0.67 x  $I_{F(AV)}$  + 0.106  $I^2_{F(RMS)}$ 

57

# PACKAGE MECHANICAL DATA DPAK



	DIMENSIONS				
REF.	Millimeters		Inches		
	Min.	Max	Min.	Max.	
А	2.20	2.40	0.086	0.094	
A1	0.90	1.10	0.035	0.043	
A2	0.03	0.23	0.001	0.009	
В	0.64	0.90	0.025	0.035	
B2	5.20	5.40	0.204	0.212	
С	0.45	0.60	0.017	0.023	
C2	0.48	0.60	0.018	0.023	
D	6.00	6.20	0.236	0.244	
Е	6.40	6.60	0.251	0.259	
G	4.40	4.60	0.173	0.181	
Н	9.35	10.10	0.368	0.397	
L2	0.80 typ.		0.031 typ.		
L4	0.60	1.00	0.023	0.039	
V2	0°	8°	0°	8°	

# FOOTPRINT (in millimeters)



57

#### PACKAGE MECHANICAL DATA





#### **OTHER INFORMATION**

Туре	Marking	Package	Weight	Base qty	Delivery mode
FLC01-200H	FLC01-200H	IPAK	0.40 g	75	Tube
FLC01-200B	FLC01-200B	DPAK	0.40 g	75	Tube
FLC01-200B-TR	FLC01-200B	DPAK	0.40 g	2500	Tape & reel

Information furnished is believed to be accurate and reliable. However, STMicroelectronics assumes no responsibility for the consequences of use of such information nor for any infringement of patents or other rights of third parties which may result from its use. No license is granted by implication or otherwise under any patent or patent rights of STMicroelectronics. Specifications mentioned in this publication are subject to change without notice. This publication supersedes and replaces all information previously supplied. STMicroelectronics or systems without express written approval of STMicroelectronics.

The ST logo is a registered trademark of STMicroelectronics

© 2002 STMicroelectronics - Printed in Italy - All rights reserved.

STMicroelectronics GROUP OF COMPANIES

Australia - Brazil - Canada - China - Finland - France - Germany Hong Kong - India - Israel - Italy - Japan - Malaysia - Malta - Morocco - Singapore Spain - Sweden - Switzerland - United Kingdom - United States.

http://www.st.com

8/8

